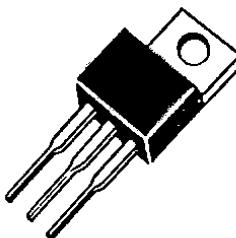


### Description

The devices are n-channel, enhancement mode, power MOSFETs designed especially for high speed applications, such as switching power supplies, converters, AC and DC motor controls, relay and solenoid drivers and other pulse circuits.

- Low  $R_{DS(on)}$
- $V_{GS}$  Rated at  $\pm 20V$
- Silicon Gate for Fast Switching Speeds
- $I_{DSS}$ ,  $V_{DS(on)}$ , Specified at Elevated Temperature
- Rugged
- Low Drive Requirements
- Ease of Parallelling

TO-220AB



IS00010F

IRF510  
IRF511  
IRF512  
IRF513  
MTP4N08  
MTP4N10

### Maximum Ratings

Symbol	Characteristic	Rating <b>IRF510/512</b> <b>MTP4N10</b>	Rating <b>MTP4N08</b>	Rating <b>IRF511/513</b>	Unit
$V_{DSS}$	Drain to Source Voltage 1	100	80	60	V
$V_{DGR}$	Drain to Gate Voltage 1 $R_{GS}=20k\ \Omega$	100	80	60	V
$V_{GS}$	Gate to Source Voltage	$\pm 20$	$\pm 20$	$\pm 20$	V
TJ, Tstg	Operating Junction and Storage Temperatures	-55 to +150	-55 to +150	-55 to +150	
TL	Maximum Lead Temperature for Soldering Purposes, 1/8" From Case for 5s	275	275	275	

### Maximum On-State Characteristics

		<b>IRF510/511</b>	<b>IRF512/513</b>	<b>MTP4N08/10</b>	
$R_{DS(on)}$	Static Drain-to-Source On Resistance	0.60	0.80	0.80	$\Omega$
$I_D$	Drain Current Continuous at $T_c=25$ Continuous at $T_c=100$ Pulsed	4.0 2.5 16	3.5 2.0 14	5.0 3.5 14	A

### Maximum Thermal Characteristics

$R_{eJC}$	Thermal Resistance Junction to Case	6.4	6.4	2.5	/W
$R_{eJA}$	Thermal Resistance Junction to Ambient	80	80	80	/W
$P_D$	Total Power Dissipation at $T_c=25$	20	20	50	W

### Notes

For information concerning connection diagram and package outline, refer to Section 7.

**Electrical Characteristics** ( $T_c=25^\circ C$  unless otherwise noted)

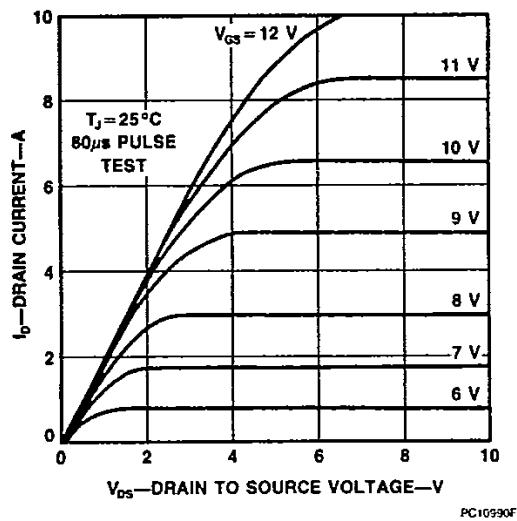
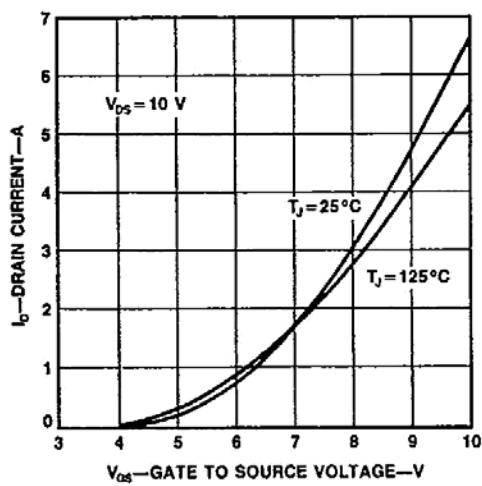
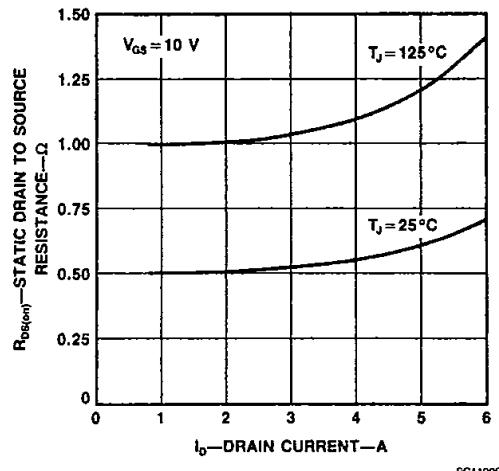
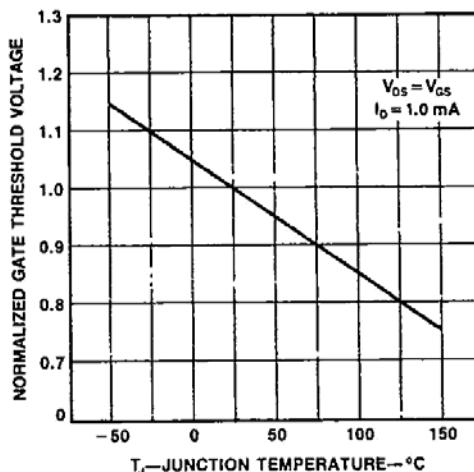
Symbol	Characteristic	Min	Max	Unit	Test Conditions
<b>Off Characteristics</b>					
$V_{(BR)DSS}$	Drain Source Breakdown Voltage <sup>1</sup> IRF510/512/MTP4N10 MTP4N08 IRF511/513			V	$V_{GS}=0V, I_D=250\mu A$
		100			
		80			
		60			
$I_{DSS}$	Zero Gate Voltage Drain Current		250	$\mu A$	$V_{DS}=\text{Rated } V_{DSS}, V_{GS}=0V$
			100	$\mu A$	$V_{DS}=0.8 \times \text{Rated } V_{DSS}, V_{GS}=0V, T_c=125^\circ C$
$I_{GSS}$	Gate-Body Leakage Current		$\pm 500$	nA	$V_{GS}=\pm 20V, V_{DS}=0V$
<b>On Characteristics</b>					
$V_{GS(th)}$	Gate Threshold Voltage IRF510-513 MTP4N08/10			V	$I_D=250\mu A, V_{DS}=V_{GS}$ $I_D=1mA, V_{DS}=V_{GS}$
		2.0	4.0		
		2.0	4.5		
$R_{DS(on)}$	Static Drain-Source On-Resistance <sup>2</sup> IRF510/511 IRF512/513/MTP4N08/4N10			$\Omega$	$V_{GS}=10V, I_D=2.0A$
			0.60		
			0.80		
$V_{DS(on)}$	Drain-Source On-Voltage <sup>2</sup> MTP4N08/4N10		4.8	V	$V_{GS}=10V; I_D=4.0A$
			3.2	V	$V_{GS}=10V; I_D=2.0A; T_c=100^\circ C$
$g_{fs}$	Forward Transconductance	1.0		S( $\text{U}$ )	$V_{DS}=10V, I_D=2.0A$
<b>Dynamic Characteristics</b>					
$C_{iss}$	Input Capacitance		200	pF	$V_{DS}=25V, V_{GS}=0V$ $F=1.0\text{MHz}$
$C_{oss}$	Output Capacitance		100	pF	
$C_{rss}$	Reverse Transfer Capacitance		30	pF	
<b>Switching Characteristics</b> ( $T_c=25^\circ C$ , Figure 11,12) <sup>3</sup>					
$t_{d(on)}$	Turn-On Delay Time		20	ns	$V_{DD}=50V, I_D=2.0A$ $V_{GS}=10V, R_{GEN}=50\Omega$ $R_{GS}=50\Omega$
$t_r$	Rise Time		25	ns	
$t_{d(off)}$	Turn-Off Delay Time		25	ns	
$t_f$	Fall Time		20	ns	
$Q_g$	Total Gate Charge		7.5	nC	$V_{GS}=10V, I_D=8.0A$ $V_{DD}=40V$

**Electrical Characteristics (Cont.) ( $T_c=25^\circ C$  unless otherwise noted)**

Symbol	Characteristic	Typ	Max	Unit	Test Conditions
<b>Source-Drain Diode Characteristics</b>					
$V_{SD}$	Diode Forward Voltage IRF510/511			V	$I_S=4.0A; V_{GS}=0V$
	IRF512/513	2.5			$I_S=3.5A; V_{GS}=0V$
		2.0			
$t_{rr}$	Reverse Recovery Time	230		ns	$I_S=4.0A; dI_S/dt=25A/\mu S$

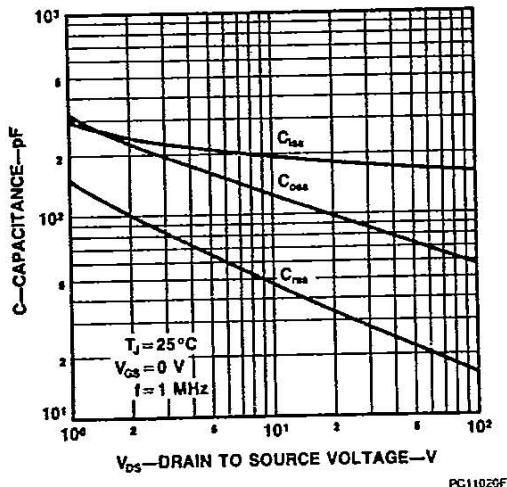
**Notes**

1.  $T_J=+25^\circ C$  to  $+150^\circ C$
2. Pulse test; Pulse width  $\leq 80\mu s$ , Duty cycle  $\leq 1\%$
3. Switching time measurements performed on LEM TR-58 test equipment

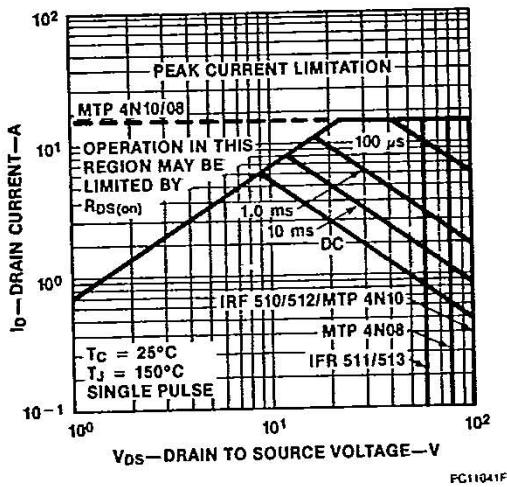
**Typical Performance Curves**
**Figure 1 Output Characteristics**

**Figure 3 Transfer Characteristics**

**Figure 2 Static Drain to Source Resistance vs Drain Current**

**Figure 4 Temperature Variation of Gate to Source Threshold Voltage**


## Typical Performance Curves (Cont.)

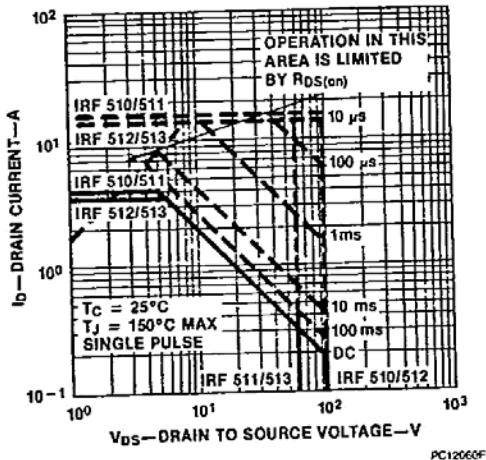
**Figure 5 Capacitance vs Drain to Source Voltage**



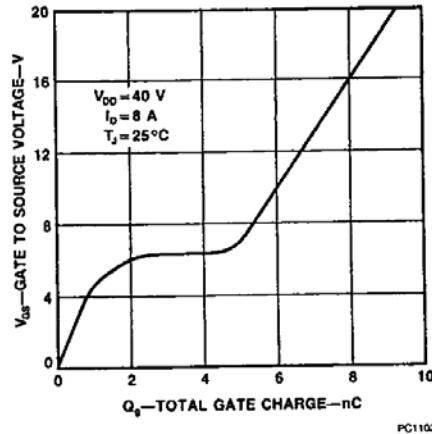
**Figure 7 Forward Biased Operating Area for  
MTP4N08/4N10**



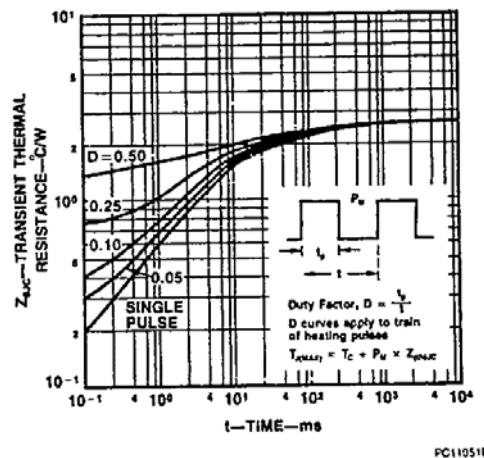
**Figure 9 Forward Biased Safe Operating Area  
For IRF510-513**



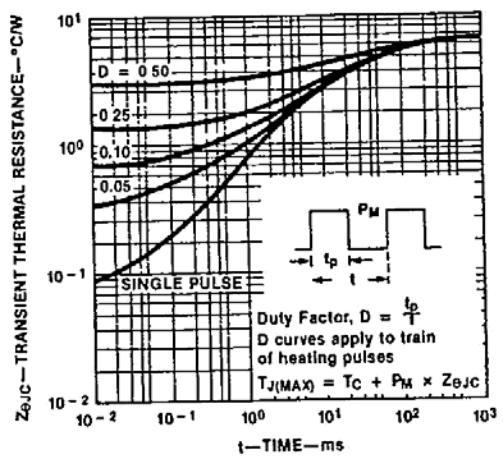
**Figure 6 Gate to Source Voltage vs Total Gate Charge**



**Figure 8 Transient Thermal Resistance vs Time for  
MTP4N08/4N10**



**Figure 10 Transient Thermal Resistance vs Time  
For IRF510-513**



### Typical Electrical Characteristics

Figure 11 Switching Test Circuit

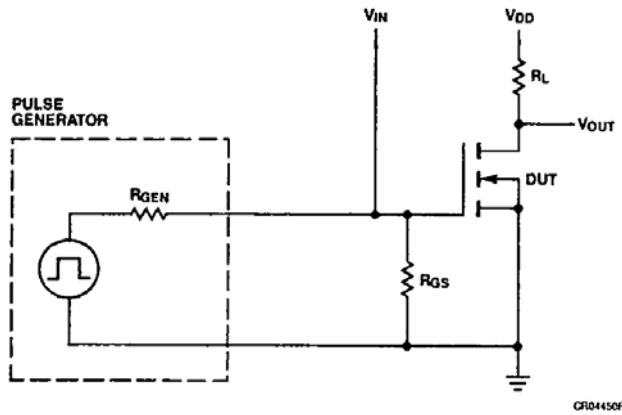


Figure 12 Switching Waveforms

