

INCHANGE SEMICONDUCTOR

isc P-Channel MOSFET Transistor

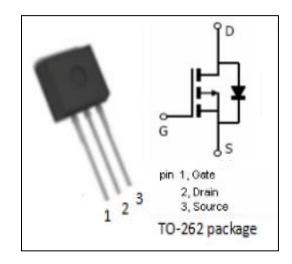
IRF5210L

·FEATURES

- Static drain-source on-resistance:
 - RDs(on)≤60mΩ(@V_{GS}= -10V; I_D= -24A)
- Advanced trench process technology
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

• Fast switching application.



• ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

| SYMBOL | PARAMETER | VALUE | UNIT |
|------------------|--|---------|------|
| V _{DSS} | Drain-Source Voltage | -100 | V |
| V _{GS} | Gate-Source Voltage | ±20 | V |
| Ι _D | Drain Current-Continuous | -40 | А |
| P _D | Total Dissipation @T _c =25℃ | 200 | W |
| Tj | Max. Operating Junction Temperature | -55~175 | °C |
| T _{stg} | Storage Temperature | -55~175 | °C |

THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | МАХ | UNIT |
|----------|------------------------------------|------|------|
| Rth(j-c) | Channel-to-case thermal resistance | 0.75 | °C/W |

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ELECTRICAL CHARACTERISTICS

T_c=25℃ unless otherwise specified

| SYMBOL | PARAMETER | CONDITIONS | MIN | МАХ | UNIT |
|----------------------------|--------------------------------|--|------|------|------------|
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V; I _D = -250 μ A | -100 | | V |
| $V_{\text{GS}(\text{th})}$ | Gate Threshold Voltage | V _{DS} =V _{GS} ; I _D = -250 μ A | -2 | -4 | V |
| $R_{\text{DS(on)}}$ | Drain-Source On-Resistance | V _{GS} = -10V; I _D = -24A | | 60 | mΩ |
| IGSS | Gate-Source Leakage Current | V _{GS} = ±20V; V _{DS} = 0V | | ±100 | nA |
| IDSS | Drain-Source Leakage Current | V _{DS} = -100V; V _{GS} = 0V | | -25 | μ Α |
| V_{SD} | Diode forward voltage | I _s = -24A, V _{GS} = 0V | | -1.6 | V |



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