J111 / J112 / J113 / MMBFJ111 / MMBFJ112 / MMBFJ112\_SB51338 / MMBFJ113 — N-Channel Switch

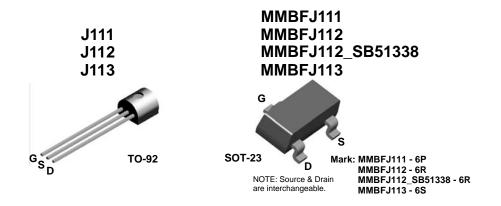


August 2012

# J111 / J112 / J113 / MMBFJ111 / MMBFJ112 / MMBFJ112\_SB51338 / MMBFJ113 N-Channel Switch

# Features

- This device is designed for low level analog switching, sample and hold circuits and chopper stabilized amplifiers.
- Sourced from Process 51.
- Source & Drain are interchangeable.



# Absolute Maximum Ratings\* $T_a = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>DG</sub>	Drain-Gate Voltage	35	V
V <sub>GS</sub>	Gate-Source Voltage	-35	V
I <sub>GF</sub>	Forward Gate Current	50	mA
T <sub>J,</sub> T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

\* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired. **NOTES:** 

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

## Thermal Characteristics $T_a = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter		Units	
		J111-113	*MMBFJ111-113	Units
PD	Total Device Dissipation Derate above 25°C	625 5.0	350 2.8	mW mW/°C
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case	125		°C/W
R <sub>0JA</sub>	Thermal Resistance, Junction to Ambient	357	556	°C/W

\* Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06".

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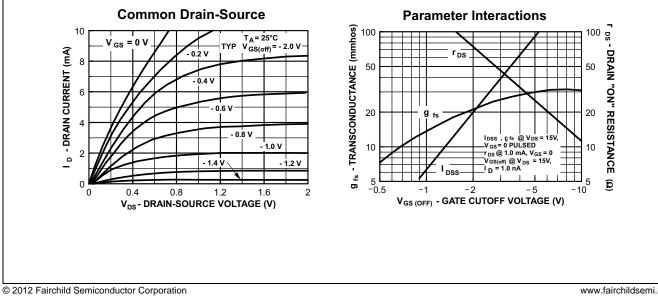
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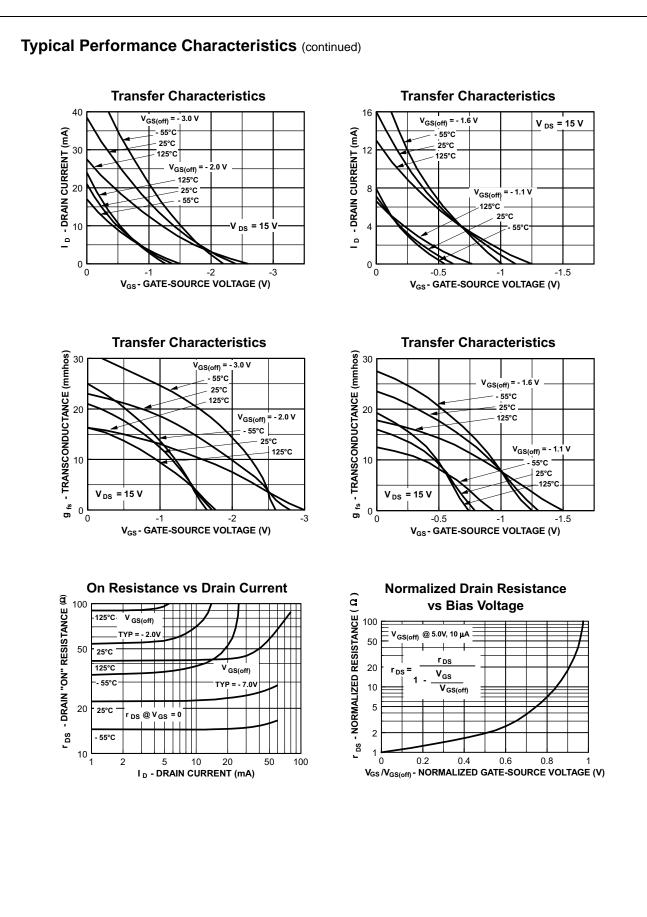
Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
Off Charac	teristics					
BV <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	$I_{G} = -1.0 \mu A, V_{DS} = 0$	-35			V
I <sub>GSS</sub>	Gate Reverse Current	$V_{GS} = -15V, V_{DS} = 0$			-1.0	nA
V <sub>GS(off)</sub>	Gate-Source Cutoff Voltage	$V_{DS} = 5.0V, I_D = 1.0\mu A$ 111 112	-3.0 -1.0		-10 -5.0	V V
		MMBFJ112_SB51338 113	-3.0 -0.5		-5.0 -3.0	V V
I <sub>D(off)</sub>	Drain Cutoff Leakage Current	V <sub>DS</sub> = 5.0V, V <sub>GS</sub> = -10V			1.0	nA
On Charac	teristics					
I <sub>DSS</sub>	Zero-Gate Voltage Drain Current*	$V_{DS} = 15V, I_{GS} = 0$ 111 112 113	20 5.0 2.0			mA mA mA
r <sub>DS(on)</sub>	Drain-Source On Resistance	$\label{eq:VDS} \begin{array}{ll} V_{DS} \leq 0.1 \text{V}, \ \text{V}_{GS} = 0 & \begin{array}{ll} \textbf{111} \\ \textbf{112} \\ \textbf{113} \end{array}$			30 50 100	Ω Ω Ω
Small Sigr	al Characteristics					
C <sub>dg(on)</sub> C <sub>sg(on)</sub>	Drain Gate & Source Gate On Capacitance	$V_{DS} = 0, V_{GS} = 0, f = 1.0MHz$			28	pF
C <sub>dg(off)</sub>	Drain-Gate Off Capacitance	$V_{DS} = 0, V_{GS} = -10V, f = 1.0MHz$			5.0	pF
C <sub>sg(off)</sub>	Source-Gate Off Capacitance	V <sub>DS</sub> = 0, V <sub>GS</sub> = -10V, f = 1.0MHz			5.0	pF

\* Pulse Test: Pulse Width  $\leq$  300µs, Duty Cycle  $\leq$  3.0%

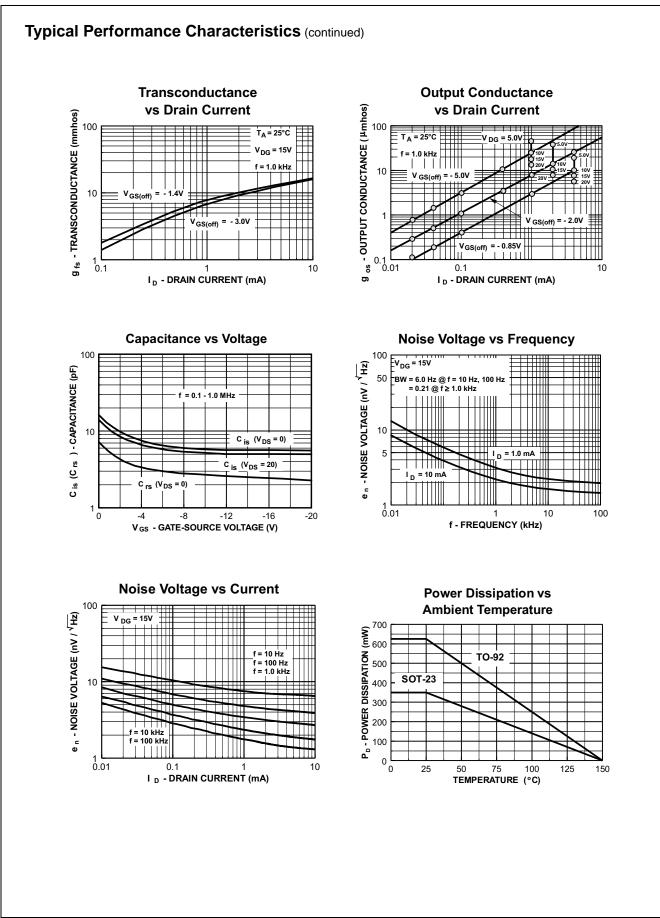
# **Typical Performance Characteristics**



J111 / J112 / J113 / MMBFJ111 / MMBFJ112 / MMBFJ112\_SB51338 / MMBFJ113 Rev. B0 2

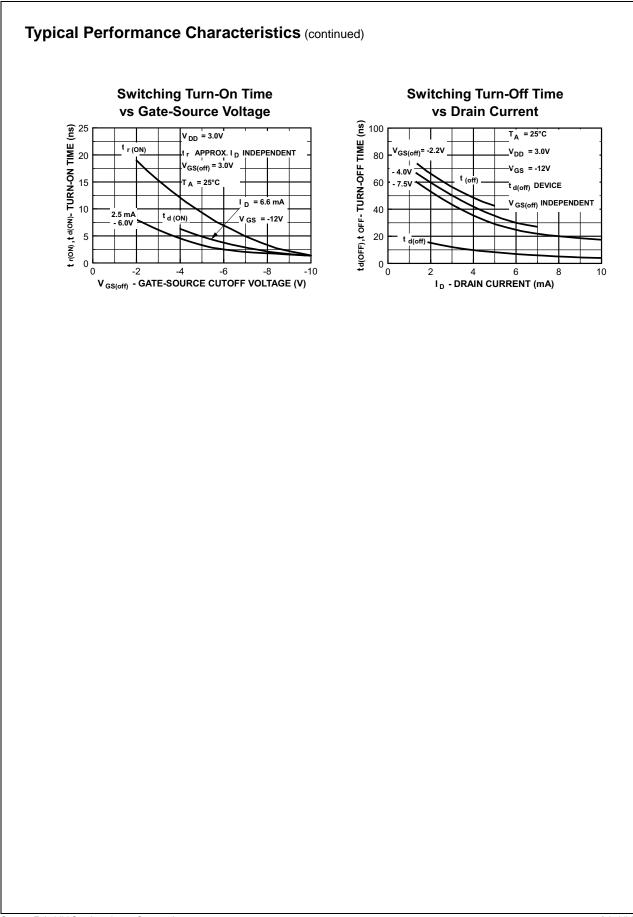


Rev. B0



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Rev. 161