# UTC UNISONIC TECHNOLOGIES CO., LTD

K4059 **N-CHANNEL JFET** 

# FIELD EFFECT TRANSISTOR SILICON N CHANNEL JUNCTION TYPE

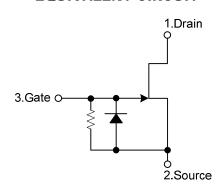
#### DESCRIPTION

The UTC K4059 is an N-channel JFET, it uses UTC's advanced technology to provide customers with low input capacitance and low forward transfer admittance.

#### **FEATURES**

- \* Low forward transfer admittance
- \* Low input capacitance

#### **EQUIVALENT CIRCUIT**

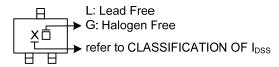


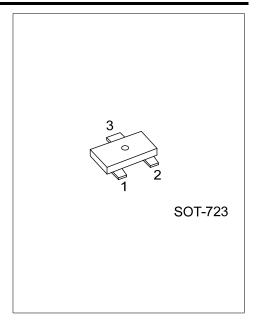
### ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dooking		
Lead Free	Halogen Fr	ree	Package	1	2	3	Packing	
K4059L-x-AQ3-R	K4059G-x-A0	K4059G-x-AQ3-R		D	S	G	Tape Reel	
Note: Pin Assignment: D: Dra	ain S: Source	G: Gate					_	

K4059G-x-AQ3-R (1) R: Tape Reel (1)Packing Type (2) AQ3: SOT-723 (2)Package Type (3) x: refer to CLASSIFICATION of IDSS (3)Rank (4) G: Halogen Free and Lead Free, L: Lead Free (4)Green Package

# **MARKING**





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## ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Gate-Drain Voltage	$V_{GDO}$	-20	V
Gate-Current	$I_{G}$	10	mA
Drain Power Dissipation (T <sub>A</sub> =25°C)	P <sub>D</sub>	100	mW
Junction Temperature	TJ	+125	°C
Storage Temperature Range	T <sub>STG</sub>	-55 ~ +125	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

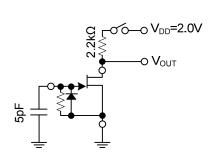
# ■ **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub>=25°C ,unless otherwise specified )

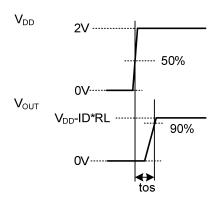
PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Drain Current	I <sub>DSS</sub>		K4059-A	140		240	μΑ
		$V_{GS}$ =0, $V_{DS}$ =2 $V$	K4059-B	210		350	μΑ
			K4059-C	320		500	μΑ
Drain Current	I <sub>D</sub>	$V_{DD}$ =2V, R <sub>L</sub> =2.2k $\Omega$ , C <sub>g</sub> =5pF	K4059-A	125		260	μΑ
			K4059-B	190		370	μΑ
			K4059-C	290		500	μΑ
Gate-Drain Voltage	$V_{(BR)GDO}$	I <sub>G</sub> =-10μA		-20			V
Gate-Source Cut-Off Voltage	$V_{GS(OFF)}$	V <sub>DS</sub> =2V, I <sub>D</sub> =1μA		-0.1		-1.0	V
Forward Transfer Admittance	Y <sub>fs</sub>	V <sub>DS</sub> =2V, V <sub>GS</sub> =0V			1.85		mS
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =2V, V <sub>GS</sub> =0, f=1MHz			4.0		pF
Voltage Gain	G <sub>V</sub>	$V_{DD}=2V,R_L=2.2k\Omega,$	K4059-A	-1.2	+0.9		dB
		C <sub>g</sub> =5pF, f=1kHz,	K4059-B	-0.2	+1.4		dB
		V <sub>IN</sub> =100mV	K4059-C	+0.5	+1.8		dB
Dolta Voltago Cain	A.C	$V_{DD}$ =2V, $R_L$ =2.2k $\Omega$ , $C_g$ =5pF,			0	-1	dB
Delta Voltage Gain	$\Delta G_{V(f)}$	f=1kHz~100Hz, V <sub>IN</sub> =100mV					uБ
	$\Delta G_{V(V)}$	$V_{DD}$ =2V~1.5V, $R_L$ =2.2k $\Omega$ ,	K4059-A		-0.6	-1.1	dB
Delta Voltage Gain		C <sub>g</sub> =5pF, f=1kHz,	K4059-B		-0.8	-1.7	dB
		V <sub>IN</sub> =100mV	K4059-C		-1.4	-3.2	dB
	V <sub>N</sub>	$V_{DD}$ =2V, $R_L$ =1k $\Omega$ ,	K4059-A		33	75	mV
Noise Voltage		$C_g$ =10pF, $G_V$ =80dB,	K4059-B		38	80	mV
		A-Curve Filter	K4059-C		42	90	mV
Total Harmonic Distortion	THD	$V_{DD}$ =2V, $R_L$ =2.2k $\Omega$ ,	K4059-A		1.3		%
		C <sub>g</sub> =5pF, f=1kHz,	K4059-B		0.6		%
		V <sub>IN</sub> =50mV	K4059-C		0.1		%
Time Output Stability	tos	$V_{DD}$ =2V, R <sub>L</sub> =2.2kΩ, C <sub>g</sub> =5p	F		100	200	ms

# ■ CLASSIFICATION OF I<sub>DSS</sub>

RANK	Α	В	С
RANGE	140-240	210-350	320-500

### ■ TEST CIRCUIT





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