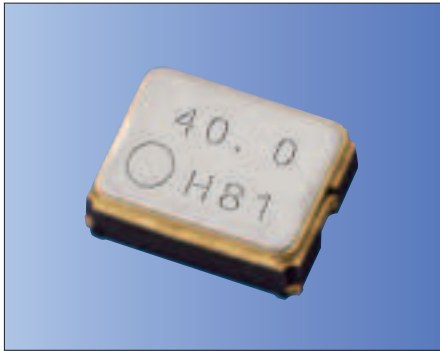


CMOS/ 1.8V to 3.3V/ 2.0×1.6mm



RoHS Compliant

Features

- Ultra Miniature ceramic package
2.0 (L) ×1.6 (W) ×0.55 (H) mm (Typ.)
- Highly reliable with seam welding
- CMOS output
- Supply voltage Vcc = 1.8V/ 2.5V/ 3.3V
Wide operating voltage range 1.6 to 3.63V
- Low current consumption

Table 1

Freq. Tol. Code	Tol. × 10 ⁻⁶	Operating Temperature Range (°C)	Note
O	± 50	-10 to +70	Standard specifications
S	± 30		
U	± 25	-40 to +85	Please contact us for available frequencies.
F	±100		
G	± 50	-40 to +105	
6	± 50		

How to Order

KC2016B 40.0000 C 1 □ E 00
① ② ③ ④ ⑤ ⑥ ⑦

- ① Series
- ② Output Frequency
- ③ Output Type (CMOS)
- ④ Supply Voltage (1.8V, 2.5V, 3.3V Compatible)
- ⑤ Frequency Tolerance (See Table 1)
- ⑥ Symmetry/ INH Function (45/ 55%, Stand-by)
- ⑦ Individual Specification (STD Specification is "00")

Packaging (Tape & Reel 2000 pcs./ reel)

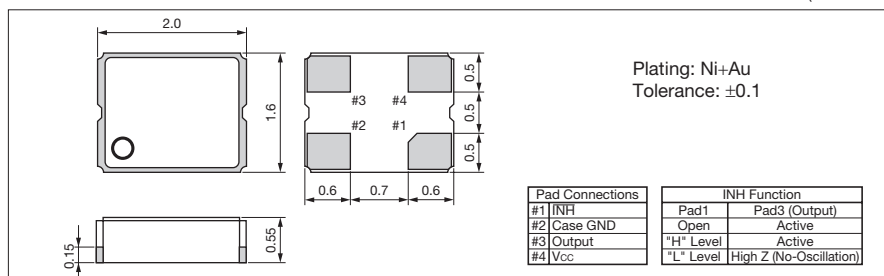
Specifications

Item	Symbol	Conditions	Min.	Max.	Units	
Output Frequency Range	fo		1.5	50	MHz	
Frequency Tolerance	f _{tol}	Initial tolerance, Operating temperature range, Rated power supply voltage change, Aging (1 year @25°C), Shock and vibration	Op. Temp.: -40 to +85°C	-100	+100	×10 ⁻⁶
			Op. Temp.: -10 to +70°C/ -40 to +85°C/-40 to +105°C	-50	+50	
			Op. Temp.: -10 to +70°C	-30	+30	
			Op. Temp.: -10 to +70°C	-25	+25	
Storage Temperature Range	T _{stg}		-55	+125	°C	
Operating Temperature Range	T _{use}	Standard Specifications	-10	+70	°C	
		Extend (Option)	-40	+85		
Max. Supply Voltage	—		-0.6	+6.0	V	
Supply Voltage	V _{cc}		+1.6	+3.63	V	
Current Consumption (Maximum Loaded/ 1.6≤V _{cc} ≤2.0V)	I _{cc}	1.5≤fo≤24MHz	—	2.5	mA	
		24<fo≤40MHz	—	3.5		
		40<fo≤50MHz	—	4.5		
Current Consumption (Maximum Loaded/ 2.0<V _{cc} ≤2.8V)	1.5≤fo≤24MHz	—	3.0			
	24<fo≤40MHz	—	4.5			
	40<fo≤50MHz	—	5.0			
Current Consumption (Maximum Loaded/ 2.8<V _{cc} ≤3.63V)	1.5≤fo≤24MHz	—	3.5			
	24<fo≤40MHz	—	5.0			
	40<fo≤50MHz	—	6.0			
Stand-by Current	I _{std}		—	10	μA	
Symmetry	SYM	@50% V _{cc}	45	55	%	
Rise/ Fall Time (10% V _{cc} to 90% V _{cc} Maximum Loaded)	tr/ tf	1.6<V _{cc} ≤2.0V	—	6.5	ns	
		2.0<V _{cc} ≤2.8V	—	5.0		
		2.8<V _{cc} ≤3.63V	—	4.5		
Low Level Output Voltage	V _{OL}	I _{oL} = 4mA	—	10% V _{cc}	V	
High Level Output Voltage	V _{OH}	I _{oH} = -4mA	90% V _{cc}	—	V	
CMOS Load	L _{CMOS}	CMOS Output	—	15	pF	
Input Voltage Range	V _{IN}		0	V _{cc}	V	
Low Level Input Voltage	V _{IL}		—	30% V _{cc}	V	
High Level Input Voltage	V _{IH}		70% V _{cc}	—	V	
Disable Time	t _{dis}		—	100	ns	
Enable Time	t _{ena}		—	5	ms	
Start-up Time	t _{str}	@Minimum operating voltage to be 0 sec.	—	10	ms	
1 Sigma Jitter	J _{Sigma}	Measured with Wavecrest SIA-3000	—	8	ps	
Peak to Peak Jitter	J _{PK-PK}		—	80	ps	

Note: All electrical characteristics are defined at the maximum load and operating temperature range.
Please contact us for inquiry about operating temperature range, available frequencies and other conditions.

Dimensions

(Unit: mm)



Recommended Land Pattern

(Unit: mm)

