

General Description

This Trench MOSFET has better characteristics, such as fast switching time, low on resistance, low gate charge and excellent avalanche characteristics. It is mainly suitable for LED Lighting and DC/DC Converters.

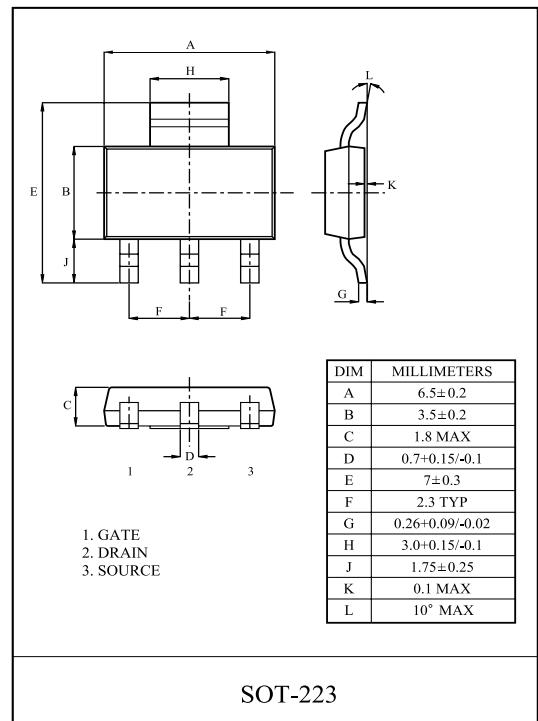
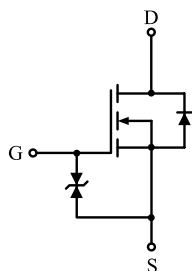
FEATURES

- $V_{DSS(\text{Min.})} = 100V$, $I_D = 1.7A$
- Drain-Source ON Resistance : $R_{DS(\text{ON})} = 0.36$ (max) @ $V_{GS} = 10V$
- $Q_g(\text{typ.}) = 4.5nC$

MAXIMUM RATING (T_c=25)

| CHARACTERISTIC | SYMBOL | RATING | UNIT |
|---|------------|----------|------|
| Drain-Source Voltage | V_{DSS} | 100 | V |
| Gate-Source Voltage | V_{GSS} | ± 18 | V |
| Drain Current | I_D | 1.7* | A |
| | | 1.0* | |
| | I_{DP} | 6.4* | |
| Single Pulsed Avalanche Energy (Note 2) | E_{AS} | 12.4 | mJ |
| Repetitive Avalanche Energy (Note 1) | E_{AR} | 0.1 | mJ |
| Peak Diode Recovery dv/dt (Note 3) | dv/dt | 4.5 | V/ns |
| Drain Power Dissipation | P_D | 2.0* | W |
| | | 0.016 | W/ |
| Maximum Junction Temperature | T_j | 150 | |
| Storage Temperature Range | T_{stg} | -55 150 | |
| Thermal Characteristics | | | |
| Thermal Resistance, Junction-to-Ambient | R_{thJA} | 62.5* | /W |

* : Surface Mounted on FR4 Board (50mm × 50mm, 1.0t)

PIN CONNECTION

KU3600N10WZ

ELECTRICAL CHARACTERISTICS (Tc=25 °C)

| CHARACTERISTIC | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
|---|------------------------------------|--|------|------|------|------|
| Static | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | I _D =250 μA, V _{GS} =0V | 100 | - | - | V |
| Breakdown Voltage Temperature Coefficient | BV _{DSS} / T _j | I _D =250 μA, Referenced to 25 | - | 0.1 | - | V/°C |
| Drain Cut-off Current | I _{DSS} | V _{DS} =100V, V _{GS} =0V, | - | - | 10 | μA |
| Gate Threshold Voltage | V _{th} | V _{DS} =V _{GS} , I _D =250 μA | 1.0 | - | 2.5 | V |
| Gate Leakage Current | I _{GSS} | V _{GS} =±16V, V _{DS} =0V | - | - | ±10 | μA |
| Drain-Source ON Resistance | R _{DS(ON)} | V _{GS} =10V, I _D =0.8A | - | 0.30 | 0.36 | |
| | | V _{GS} =4.5V, I _D =0.8A | - | - | 0.44 | |
| Dynamic | | | | | | |
| Total Gate Charge | Q _g | V _{DS} =50V, I _D =1.7A V _{GS} =10V (Note4,5) | - | 4.5 | 5.8 | nC |
| Gate-Source Charge | Q _{gs} | | - | 0.7 | - | |
| Gate-Drain Charge | Q _{gd} | | - | 1.5 | - | |
| Turn-on Delay time | t _{d(on)} | V _{DD} =50V, I _D =1.7A R _G =6 V _{GS} =10V (Note4,5) | - | 10 | - | ns |
| Turn-on Rise time | t _r | | - | 5 | - | |
| Turn-off Delay time | t _{d(off)} | | - | 35 | - | |
| Turn-off Fall time | t _f | | - | 6 | - | |
| Input Capacitance | C _{iss} | V _{DS} =25V, V _{GS} =0V, f=1.0MHz | - | 210 | 300 | pF |
| Output Capacitance | C _{oss} | | - | 30 | - | |
| Reverse Transfer Capacitance | C _{rss} | | - | 13 | - | |
| Source-Drain Diode Ratings | | | | | | |
| Continuous Source Current | I _S | V _{GS} <V _{th} | - | - | 1 | A |
| Pulsed Source Current | I _{SP} | | - | - | 4 | |
| Diode Forward Voltage | V _{SD} | I _S =1A, V _{GS} =0V | - | - | 1.4 | V |
| Reverse Recovery Time | t _{rr} | I _S =1.7A, V _{GS} =0V, dI _S /dt=100A/μs | - | 30 | - | ns |
| Reverse Recovery Charge | Q _{rr} | | - | 40 | - | |

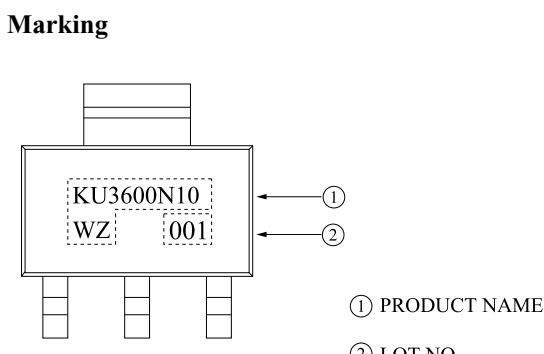
Note 1) Repetitv rating : Pulse width limited by junction temperature.

Note 2) L = 5mH, I_S=1.7A, V_{DD}=50V, R_G = 25 Ω, Starting T_j = 25 °C.

Note 3) I_S = 1.7A, dI/dt = 100A/μs, V_{DD} = BV_{DSS}, Starting T_j = 25 °C.

Note 4) Pulse Test : Pulse width = 300μs, Duty Cycle = 2%.

Note 5) Essentially independent of operating temperature.



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Fig1. V_{DS} - I_D

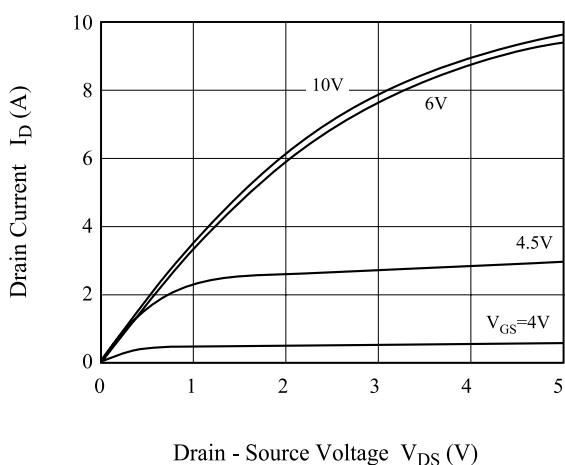


Fig2. $R_{DS(ON)}$ - I_D

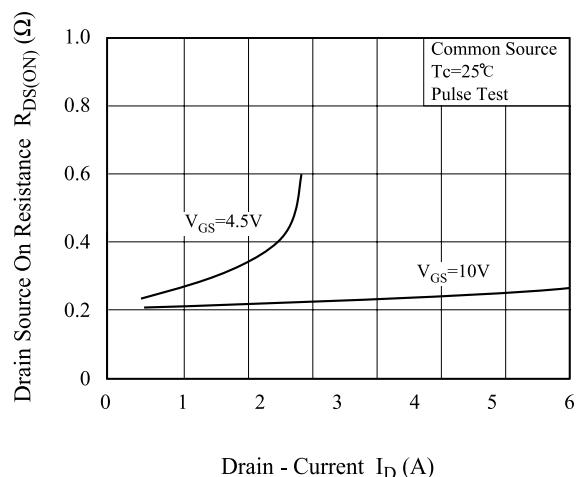


Fig3. I_D - V_{GS}

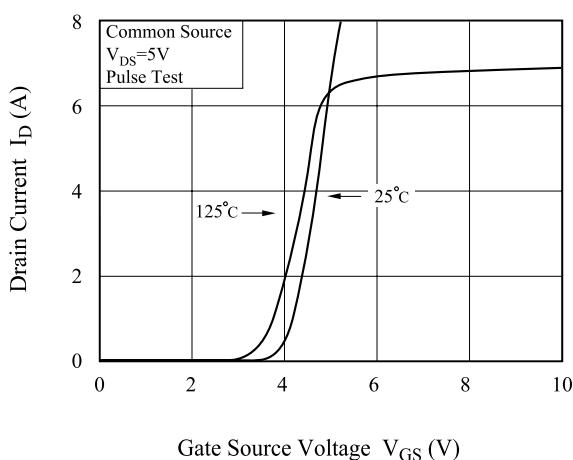


Fig4. $R_{DS(ON)}$ - T_j

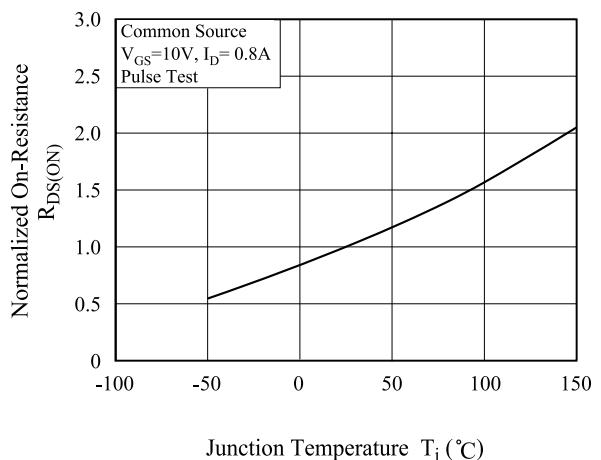


Fig5. V_{th} - T_j

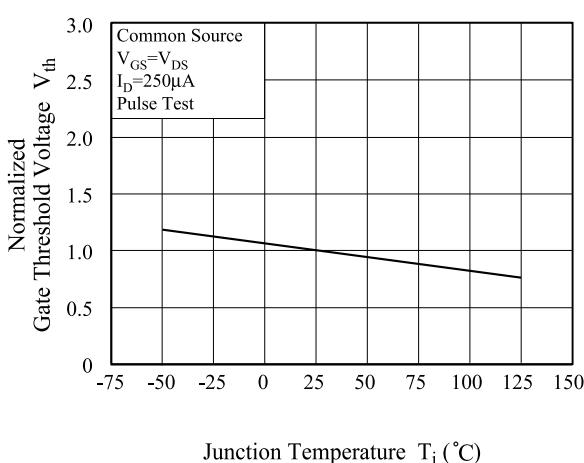
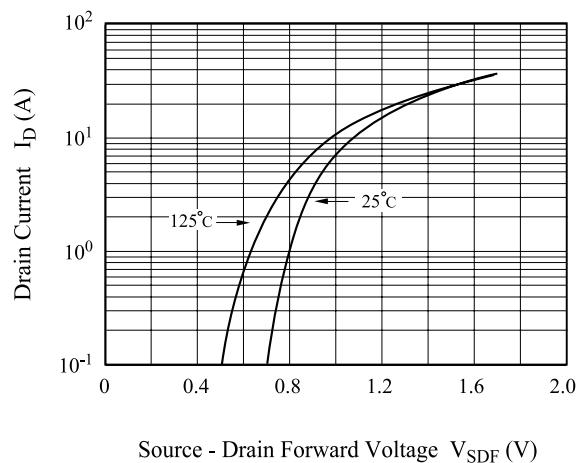


Fig6. I_S - V_{SDF}



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Fig7. BV_{DSS} - T_j

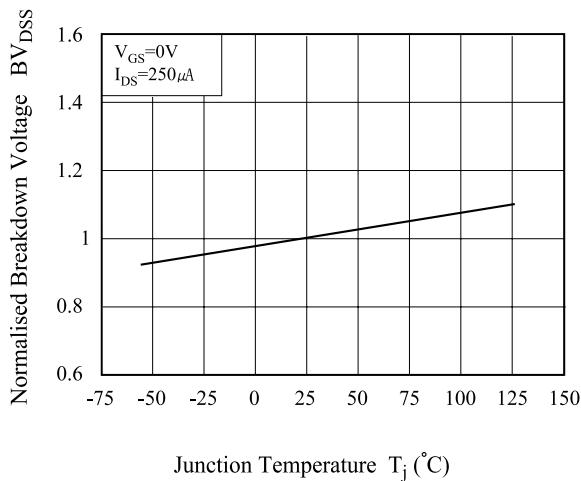


Fig8. C - V_{DS}

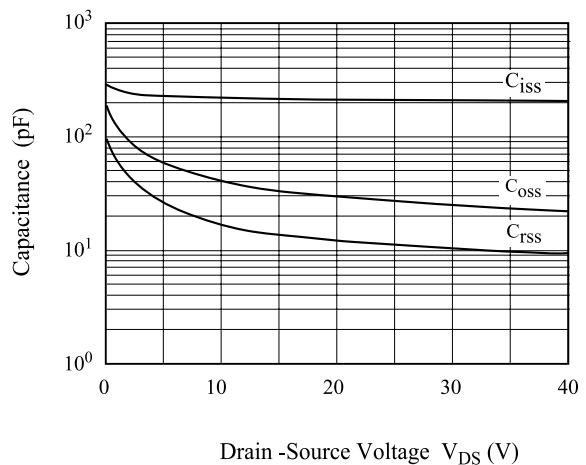


Fig9. Q_g - V_{GS}

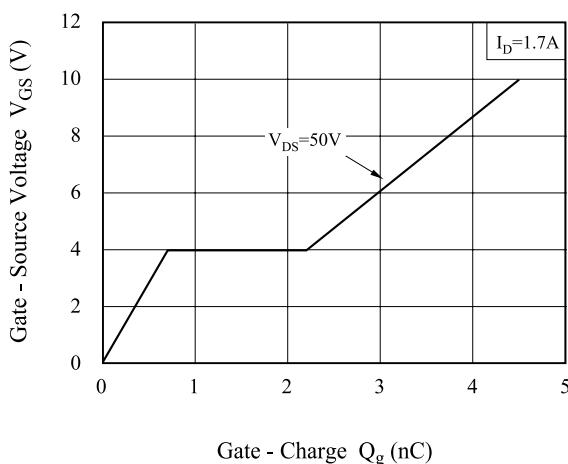


Fig10. Safe Operation Area

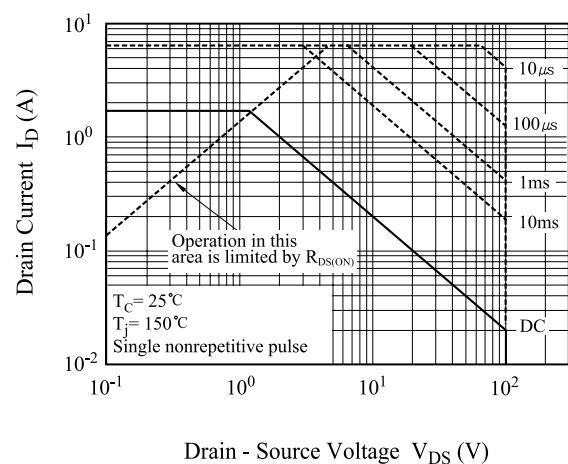
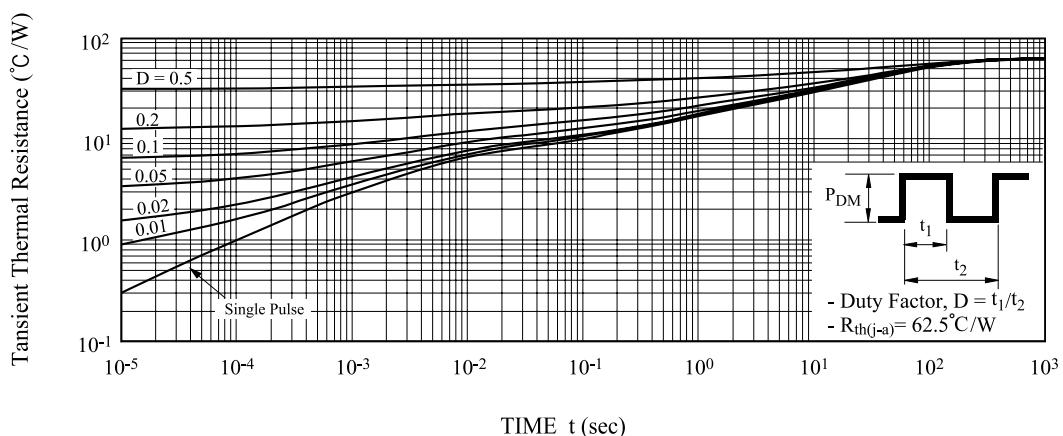


Fig11. Transient Thermal Response Curve



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Fig12. Gate Charge

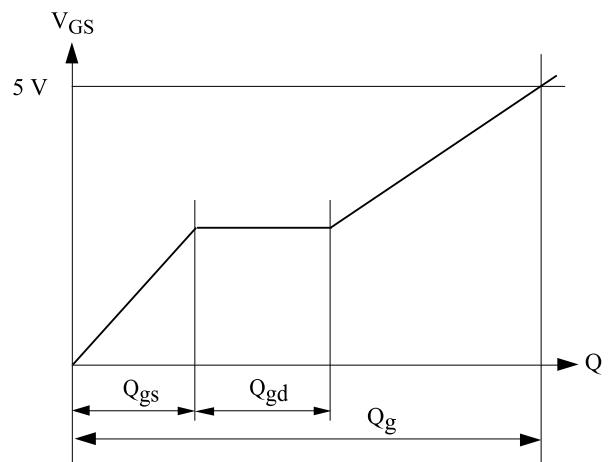
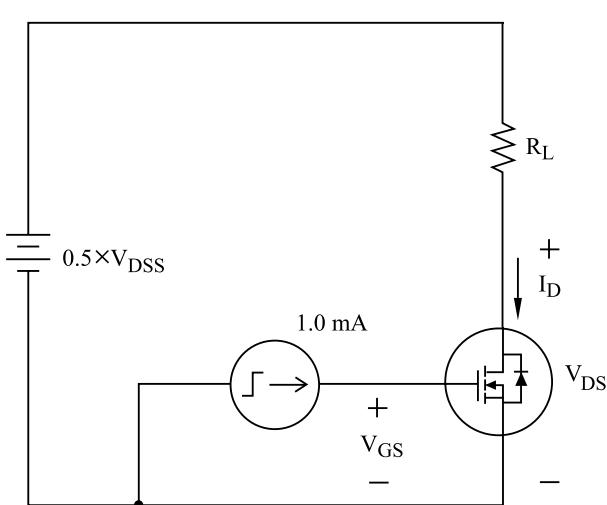


Fig13. Single Pulsed Avalanche Energy

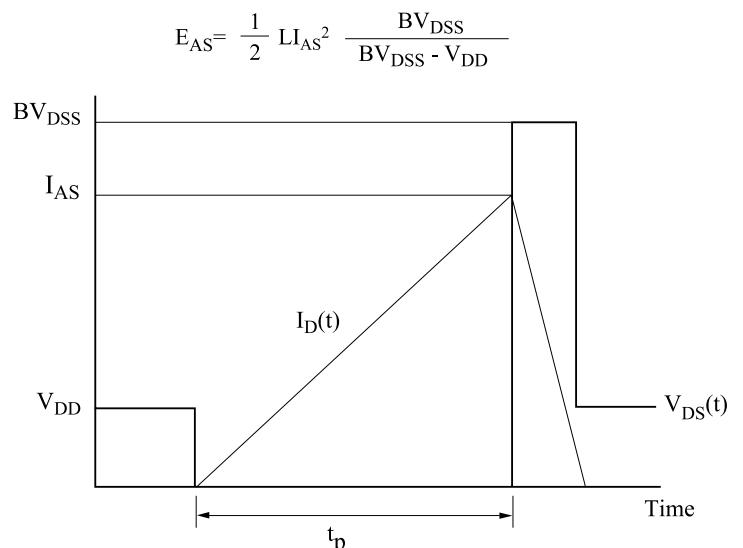
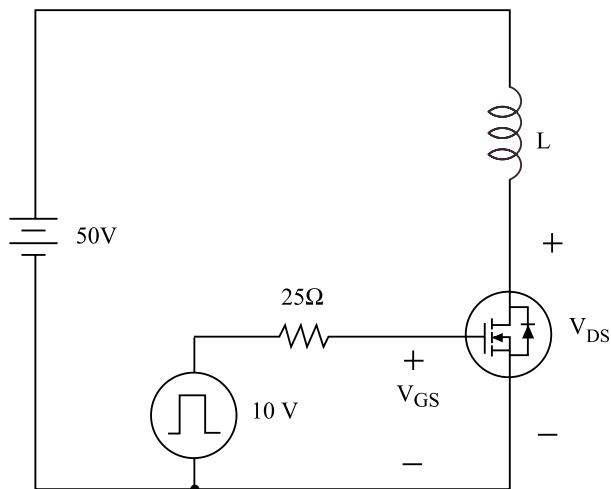
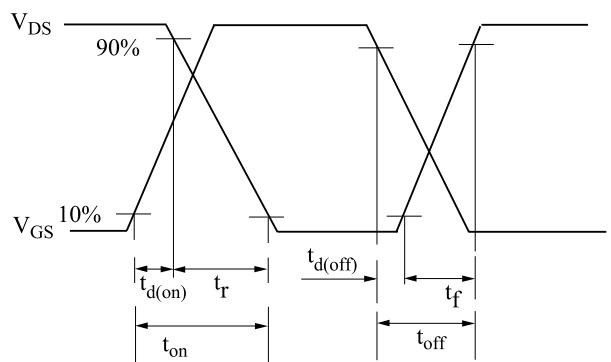
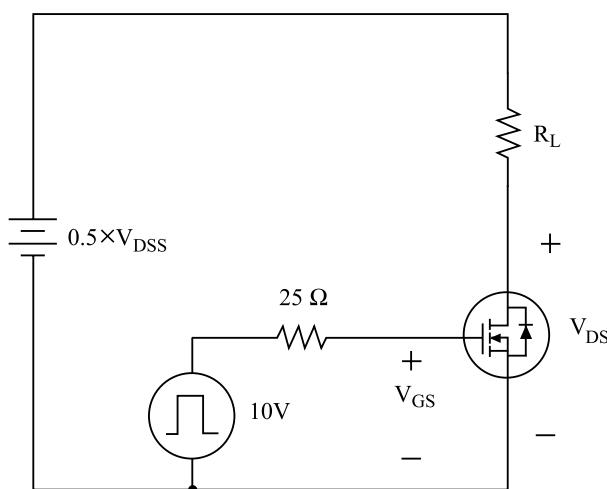


Fig14. Resistive Load Switching



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Fig15. Source - Drain Diode Reverse Recovery and dv /dt

