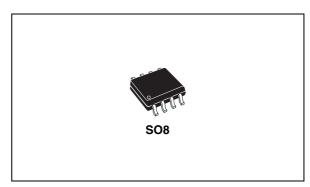


#### Monolithic bus driver with ISO 9141 interface

#### **Features**

- Operating power supply voltage range 4.5 V  $\leq$  V<sub>S</sub>  $\leq$  36 V (40 V for transients)
- Reverse supply (battery) protected down to  $V_S \ge -24 \text{ V}$
- Standby mode with very low current consumption  $IS_{SB} \le 1$  mA @  $V_{CC} \le 0.5$  V
- Low quiescent current in off condition IS<sub>OFF</sub> = 120 µA
- TTL compatible TX input
- Bidirectional K-I/O pin with supply voltage dependent input threshold
- Overtemperature shut down function Selective to K-I/O pin
- Wide input and output voltage range -24  $V \le V_K \le V_S$
- K output current limitation, typ. I<sub>K</sub> = 60 mA
- Defined OFF output status in undervoltage condition and V<sub>S</sub> or GND interruption
- Controlled output slope for low EMI



- High input impedance for open V<sub>S</sub> or GND connection
- Defined output ON status of LO or RX for open LI or K inputs
- Defined K output OFF for TX input open
- Integrated pull up resistors for TX, RX and LO
- EMI robustness optimized

#### **Description**

The L9637 is a monolithic integrated circuit containing standard ISO 9141 compatible interface functions.

Table 1. Device summary

Order code	Package	Packing
L9637D	SO8	Tube
L9637D013TR	SO8	Tape and reel
E-L9637D <sup>(1)</sup>	SO8	Tube
E-L9637D013TR <sup>(1)</sup>	SO8 Tape and reel	

<sup>1.</sup> Device in ECOPACK® package (see Section 4: Package information on page 13).

Contents L9637

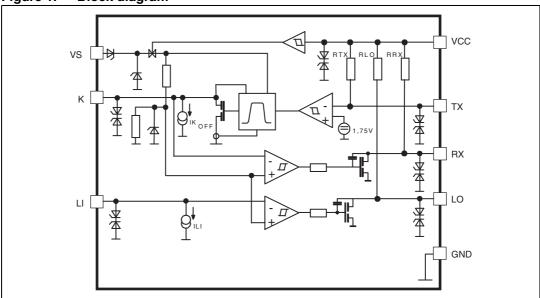
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# 1 Block diagram and pin description

# 1.1 Block diagram

Figure 1. Block diagram



# 1.2 Pin description

Figure 2. Pin connection (top view)

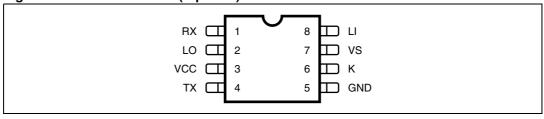


Table 2. Pin description

N.	Name	Function	
1	RX	Output for K as input	
2	LO	Output L comparator	
3	VCC	stabilized voltage supply	
4	TX	Input for K as output	
5	GND	Common GND	
6	K	Bidirectional I/O	
7	VS	Supply voltage	
8	LI	Input L comparator	

# 2 Electrical specification

#### 2.1 Absolute maximum ratings

Table 3. Absolute maximum ratings (No damage or latch)

Symbol	Parameter	Value	Unit
V <sub>S</sub>	Supply voltage ISO transients t = 400 ms	-24 to +36 -24 to +40	V
V <sub>CC</sub>	Stabilized voltage	-0.3 to +7	V
$\Delta V_S/d_t$	Supply voltage transient	-10 to +10	V/µs
V <sub>LI, K</sub>	Pin voltage	-24 to V <sub>S</sub>	V
V <sub>LO, RX, TX</sub>	riii voitage	-24 to V <sub>CC</sub>	٧

Note:

Max. ESD voltages are  $\pm 2kV$  with human body model C=100pF, R=1.5k corresponds to maximum energy dissipation 0.2mJ according to MIL883C.

#### 2.2 Thermal data

Table 4. Thermal data

Symbol	Parameter		Тур.	Max.	Unit
T <sub>JSDon</sub>	Temperature K shutdown switch on threshold		-	200	°C
T <sub>JSDoff</sub>	Temperature K shutdown switch off threshold		-	200	°C
R <sub>th j-amb</sub>	Thermal steady state junction to ambient resistance		155	180	°C/W

#### 2.3 Electrical characteristics

The electrical characteristics are valid within the below defined operating conditions, unless otherwise specified. The function is guaranteed by design until  $T_{\rm JSDon}$  temperature shutdown switch-on-threshold.

Table 5. Electrical characteristics

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
V <sub>S</sub>	Supply voltage	-	4.5	-	36	V
V <sub>CC</sub>	Stabilized voltage	see note (1)	3	5	7	V
T <sub>j</sub>	Junction temperature	-	-40	-	150	°C
I <sub>CC</sub>		$V_{CC} \le 5.5 \text{ V}; \text{ VLI,VTX} = 0 \text{ V}$	-	1.4	2.3	mA
	Supply V <sub>CC</sub> current		-5	<1	5	μΑ

Table 5. Electrical characteristics (continued)

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
IS <sub>ON</sub>		V <sub>S</sub> ≤ 16 V; VLI, VTX = 0 V	-	1.2	3	mA
IS <sub>OFF</sub>	Supply V <sub>S</sub> current	$ \begin{aligned} &VK \geq VK_{high}; \ VLI \geq VLI_{high} \\ &VTX \geq VTX_{high} \ @ \ V_S \leq 12 \ V \end{aligned} $	-	120	220	μΑ
IS <sub>SB</sub>		$V_{CC} \le 0.5 \text{ V } @ V_S \le 12 \text{ V}$	-	<1	-	μΑ
VIX	Input voltage law state	RX output status LOW 4.5 V $\leq$ V <sub>S</sub> $\leq$ 18 V	-24	-	0.45V <sub>S</sub>	V
VK <sub>low</sub>	Input voltage low state	RX output status LOW 18 V < V <sub>S</sub>	-24	-	8	V
VK	Input voltage high state	RX output status HIGH 4.5 V $\leq$ V <sub>S</sub> $\leq$ 18 V	0.55V <sub>S</sub>	-	Vs	V
$VK_{high}$	input voitage night state	RX output status HIGH 18 V < V <sub>S</sub>	- 0.025 V <sub>S</sub>	V <sub>S</sub>	V	
V <sub>Khys</sub>	Input threshold hysteresis	VK <sub>high</sub> - VK <sub>low</sub>	-		0.8	V
I <sub>Koff</sub>	Input current		-5	4	25	μΑ
RK <sub>ON</sub>	Output ON impedance		-	10	30	Ω
IK <sub>SC</sub>	Short circuit current		30	60	100	mA
VTX <sub>low</sub>	Input voltage LOW state		-24	-	1	V
VTX <sub>high</sub>	Input voltage HIGH state		2.5	-	V <sub>CC</sub>	V
RRX <sub>ON</sub> RLO <sub>ON</sub>	Output ON impedance	$ \begin{aligned} &VK \leq VK_{low}; \ VLI \leq VLI_{low} \\ &V_S \geq 6.5 \ V \ I_{RX, \ LO} \geq 1 \ mA \end{aligned} $	-	40	90	Ω
IRX <sub>SC</sub> ILO <sub>SC</sub>	Output short circuit current		9	20	35	mA
VRX <sub>H</sub> VLO <sub>H</sub>	Output voltage HIGH state	$10M\Omega \le R_{LRX}$ $10M\Omega \le R_{LLO}$	V <sub>CC</sub> - 0.25	V <sub>CC</sub> - 0.1	V <sub>CC</sub> -	٧
RLO RRX	Output pull-up resistance	Output status = (HIGH) -0.15 V $\leq$ VLO $\leq$ V <sub>CC</sub> + 0.15 V -0.15 V $\leq$ VRX $\leq$ V <sub>CC</sub> + 0.15 V	5	10	20	kΩ
RTX	Input pull up resistance	-0.15 V ≤ VTX ≤ V <sub>CC</sub> + 0.15 V	10	20	40	kΩ
VLI <sub>low</sub>	Input voltage LOW state	LO output status LOW 4.5 V $\leq$ V <sub>S</sub> $\leq$ 18 V LO output status LOW 18 V $<$ V <sub>S</sub>	-24 -24	-	0.45V <sub>S</sub> 8	V

Table 5. Electrical characteristics (continued)

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
VL <sub>high</sub>	Input voltage HIGH state	LO output status HIGH 4.5 V $\leq$ V <sub>S</sub> $\leq$ 18 V LO output status HIGH 18 V $<$ V <sub>S</sub>	0.55V <sub>S</sub> 12	-	V <sub>S</sub>	V
VLI <sub>hys</sub>	Input threshold hysteresis	VLI <sub>high</sub> - VLI <sub>low</sub>	-	0.025V <sub>S</sub>	0.8	V
ILI	Input current	$\begin{aligned} \text{VLI} \leq \text{V}_S \; \text{V}_S, \; \text{V}_{CC} \geq \text{0 or} \\ \text{V}_S, \; \text{V}_{CC} = \text{open} \end{aligned}$	-5	4	25	μΑ
C <sub>Ki,LO,RX</sub>	Internal output capacities		-	-	20	pF
f <sub>LI-LO</sub> f <sub>K-RX</sub> f <sub>TX-k</sub>	Transmission frequency	9 V < V <sub>S</sub> < 16 V (external loads) R <sub>KO</sub> = 510 $\Omega$ , C <sub>K</sub> $\leq$ 1.3 nF in active mode see <i>Figure 5</i>	-	-	50	kHz
t <sub>rLI-LO</sub> t <sub>rK-RX</sub> t <sub>rTX-K</sub>	Rise time	for the definition of $t_r$ , $t_f$ see Figure 3	-	2	6	μs
t <sub>fLI-LO</sub> t <sub>fK-RX</sub> t <sub>fTX-K</sub>	Fall time	9 V < V <sub>S</sub> < 16 V (external loads) R <sub>KO</sub> = 510 $\Omega$ , CK $\leq$ 1.3 nF	-	2	6	μs
t <sub>OFF,LI-LO</sub> t <sub>OFF,K-RX</sub> t <sub>OFF,TX-K</sub>	Switch OFF time	for the definition of ton, t <sub>OFF</sub> see <i>Figure 3</i> .  9 V < V <sub>S</sub> < 16 V (external loads)	-	4	17	μs
t <sub>ON,LI-LO</sub> t <sub>ON,K-RX</sub> t <sub>ON,TX-K</sub>	Switch ON time	$R_{KO} = 510 \Omega$ , $C_{K} \le 1.3 nF$ (inactive mode see <i>Figure 5</i> )	-	4	17	μs

<sup>1.</sup> Specs are tested at 5V only. Compliance on Vcc full range is guaranteed by design.

<sup>2.</sup> For output currents lower than this value a series protection diode can become active. See also *Figure 8* and *9*.

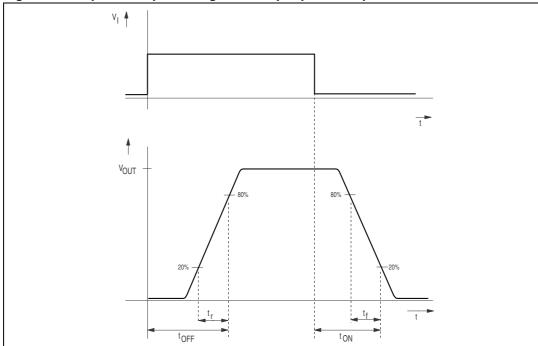
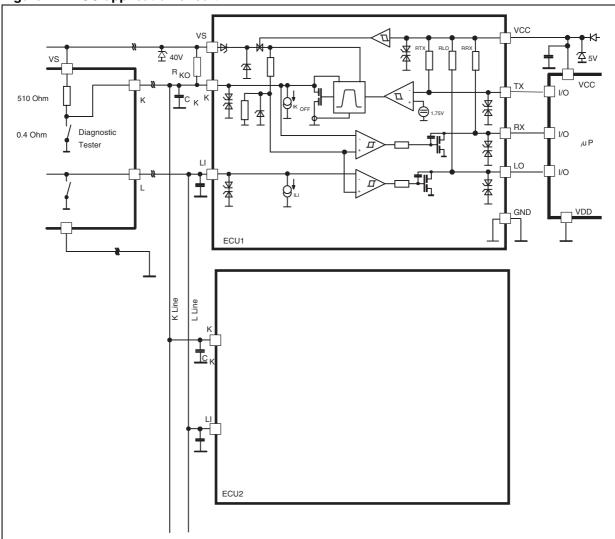


Figure 3. Input to output timings and output pulse shape

Figure 4. ISO application circuit



### 3 Functional description

The L9637 is a monolithic bus driver designed to provide bidirectional serial communication in automotive diagnostic applications according to the specification "Diagnostic Systems ISO9141".

The device provides a bidirectional link, called K, to the  $V_{Ba}$ t related diagnosis bus. It also includes a separate comparator L which is also able to be linked to the  $V_{Ba}$ t bus. The input TX and output RX of K are related to  $V_{CC}$  with her integrated pull up resistances. Also the L comparator output LO has a pull up resistance connected to  $V_{CC}$ .

The maximum external pull up resistance at K related to  $V_S$  should not be higher than  $R_{KO} \le 5 \text{ k}\Omega$  to achieve clear output ON conditions.

All  $V_{Bat}$  bus defined inputs LI and K have supply voltage dependent thresholds together with sufficient hysteresis to suppress line spikes. These pins are protected against overvoltages, shorts to GND and  $V_S$  and can also be driven beyond  $V_S$  and GND.

These features are also given for TX, RX and LI only taking into account the behavior of the internal pull up resistances. The thermal shut down function switches OFF the K output if the chip temperature increases above the thermal shut down threshold. To reactivate K again the temperature must decrease below the K switch ON temp. To achieve no fault for  $V_S$  undervoltage conditions the outputs will be switched OFF and stay at high impedance.

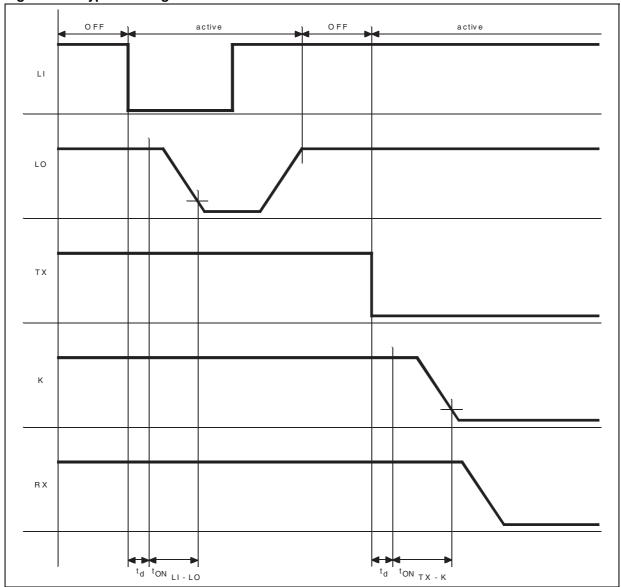
The device is also protected against reverse battery condition. During lack of  $V_S$  or GND all pins shows high impedance characteristic. To realize a lack of the  $V_S$  related bus line LI and K the outputs LO and RX shows defined ON status.

Suppressing all 4 classes of "Schaffner" signals all pins can be load with short energy pulses of max.  $\pm 0.2$  mJ. All these features together with a high possible baud rate >50Kbaud, controlled output slopes for low EMI, a wide power supply voltage range and a very small quiescent current during OFF (TX LI K=High) condition IS $_{\rm off\ typ} \le 120\ \mu A$ , and a real standby function with zero power consumption IS $_{\rm SB\ typ} \le 1\ \mu A$  during system de powering  $V_{\rm CC} \le 0.5\ V$  make this device high efficient for automotive bus system.

After wake up of the system from OFF or SB condition the first output signal will have an additional delay time  $td_{tvp} \le 5 \mu s$  see also *Figure 5*.

The typical output voltage behavior for the K, LO, RX outputs as a function of the output current is shown in *Figure 6*. *Figure 7* shows a waveform of the output signal when the low level changes from  $R_{ON}$  \*  $I_{OUT}$  to  $I_{OUT}$  \* 2 \*  $R_{ON}$  +  $I_{OUT}$  to the output current or after a negative transient forced to the output or to the supply voltage line.

Figure 5. Typical timing for mode transitions



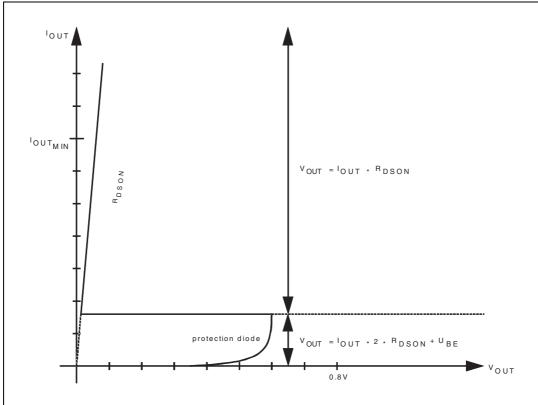
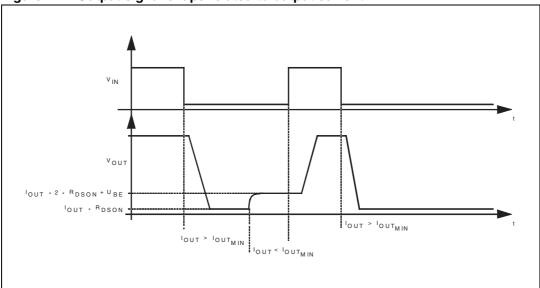


Figure 6. Output characteristics at K, LO, RX





So Ohm

VS

So Ohm

VS

ISO Interface

136 Ohm

NAP - 73

Probe

NAP - 73

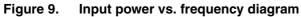
SMG

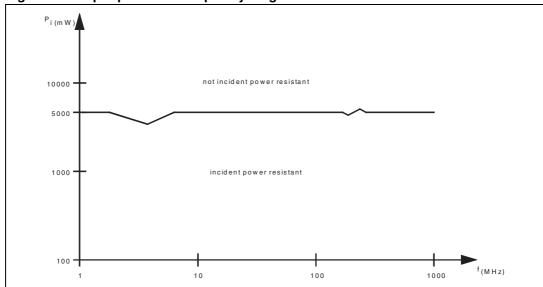
0.1 - 1000MHz

NAP

NAP

Figure 8. EMS performance (ISO 9141 bus system)





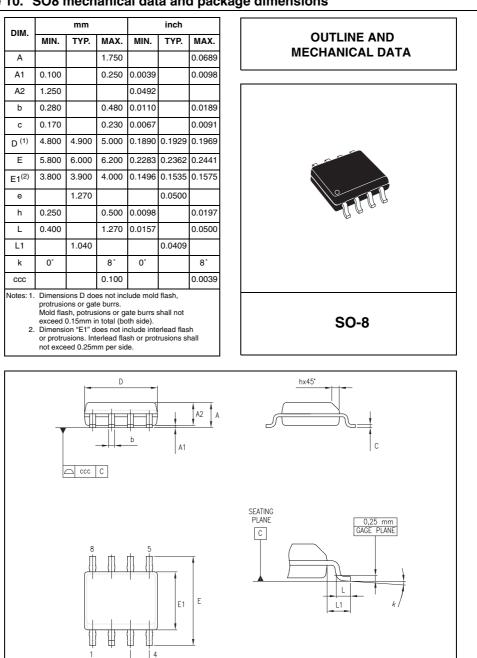
L9637 Package information

# 4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <u>www.st.com</u>.

 $\mathsf{ECOPACK}^{\mathbb{R}}$  is an ST trademark.

Figure 10. SO8 mechanical data and package dimensions



0016023 D

Revision history L9637

# 5 Revision history

Table 6. Document revision history

Date	Revision	Changes
24-Jan-2002	5	Initial release.
07-Nov-2008	6	Document reformatted.  Added <i>Table 1: Device summary on page 1.</i> Updated <i>Section 4: Package information on page 13.</i>
15-Jun-2009	7	Updated the values of "stabilized voltage" and "transmission frequency" parameters on <i>Table 5: Electrical characteristics</i> .
20-Sep-2013	8	Updated disclaimer.

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