



# 2-Channel 10 W AF Power Amplifier for Use in Home Stereo, TV Applications

#### Overview

The LA4282 is an IC which seals a high-output power amplifier for TVs and monitors in a compact package.

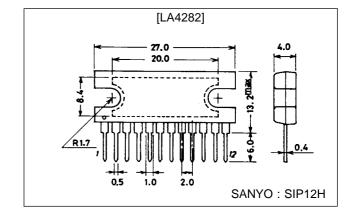
#### **Features**

- High-power 2-channel AF power amplifier
- · Low distortion
- Minimum number of external parts required (no bootstrap capacitor required)
- Low pop noise at the time of power supply ON/OFF
- Good ripple rejection (58 dB typ)
- Wide operating voltage range
- External muting available
- On-chip protector against abnormality (thermal shutdown, overvoltage)

## **Package Dimensions**

unit: mm

#### 3049A-SIP12H



## **Specifications**

## Maximum Ratings at Ta = 25 °C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max	Quiescent	45	V
Maximum output current	I <sub>O peak</sub>		4	Α
Allowable power dissipation	Pd max	With heat sink	25	W
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-40 to +150	°C

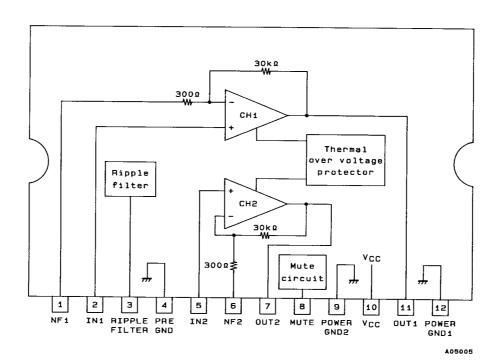
#### Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V <sub>CC</sub>		32	V
	V <sub>CC</sub> op		10 to 40	V
Recommended load resistance	R <sub>L</sub>		8	Ω

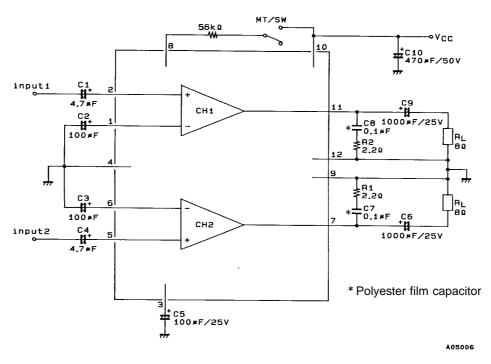
## Operating Characteristics at Ta = 25°C, $V_{\rm CC}$ = 32 V, $R_{\rm L}$ = 8 $\Omega$ , f = 1 kHz, Rg = 600 $\Omega$ , See Test Circuit.

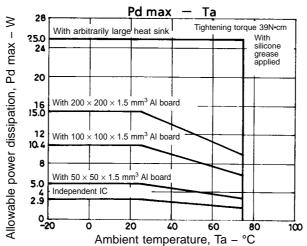
Parameter	Symbol	Conditions	min	typ	max	Unit
Quiescent current	I <sub>CCO</sub> (1)	Quiescent	30	60	100	mA
	I <sub>CCO</sub> (2)	Muting switch On	30	56	100	mA
Voltage gain	VG		38	40	42	dB
Voltage gain difference	ΔVG				1.5	dB
Output power	P <sub>O</sub> (1)	THD = 1%	9.0	10.0		W
	P <sub>O</sub> (2)	THD = 3%	10.0	11.5		W
Total harmonic distortion	THD	P <sub>O</sub> = 2 W		0.05	0.20	%
Output noise voltage	V <sub>NO</sub>	Rg = 10 k $\Omega$ , BW = 20 Hz to 20 kHz		0.25	1.0	mV
Ripple rejection	SVRR	Rg = 10 kΩ, $f_R$ = 100 Hz, $V_R$ = 0 dBm	45	58		dB
Crosstalk	CT	$Rg = 10 \text{ k}\Omega$	45	60		dB
Muting	V <sub>O(MT)</sub>	Muting switch On, V <sub>IN</sub> = -5 dBm			-35	dBm

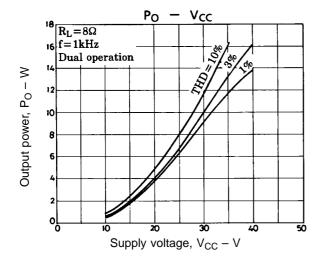
## **Equivalent Circuit Block Diagram and Pin Assignment**

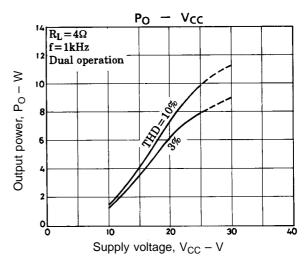


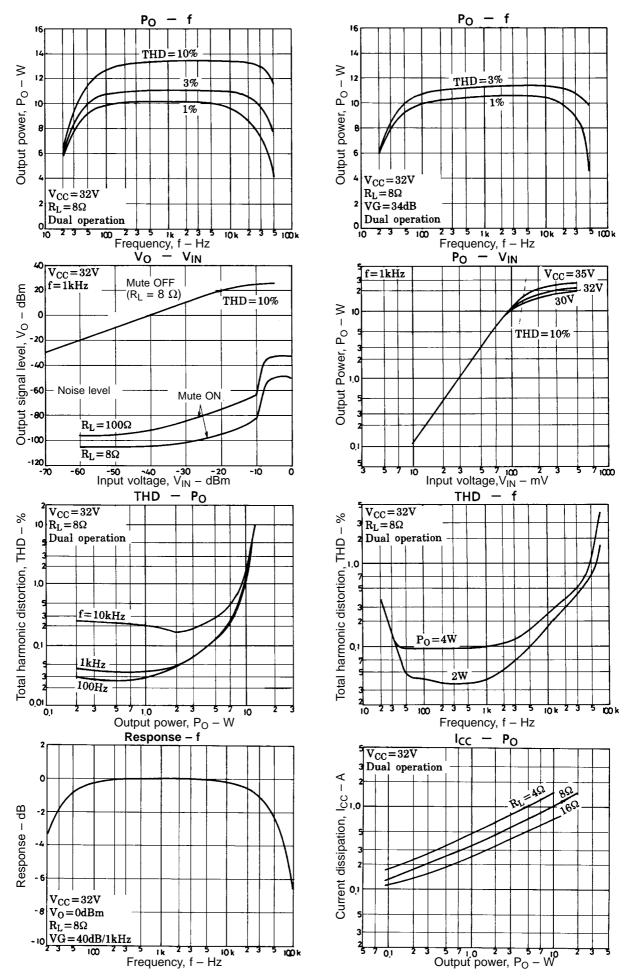
### **Test Circuit**

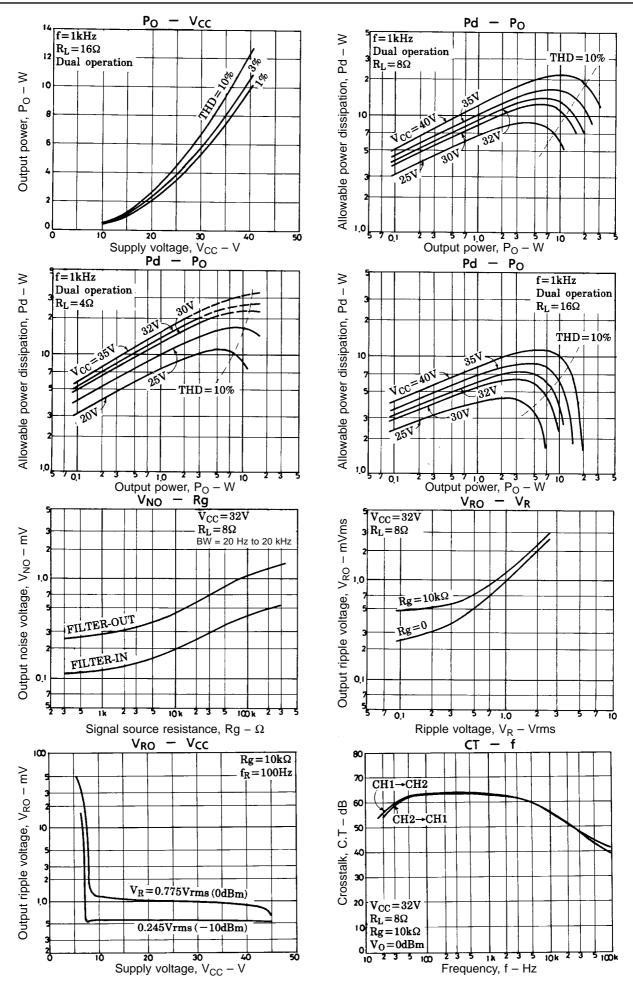












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