



# LA8151V

Monolithic Linear IC  
Downconverter IC  
for Digital CATV

## Overview

The LA8151V is a downconverter IC for digital CATV. It accepts RF input frequencies from 50 to 280 MHz and supports the DOCSIS (USA) and Euro-DOCSIS (Europe) standards.

## Features

- RF Attenuator.
- RF Mixer.
- Driver for SAW filter.
- IF AGC amplifier.
- IF Driver amplifier for ADC.

## Specifications

### Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC max</sub>	Pin 5, 10, 14, 15	6.0	V
Circuit voltages	V max	Pin 6	V <sub>CC</sub>	V
Circuit current	I <sub>8, 9</sub>	Pin 8, 9 sink current	2	mA
Allowable power dissipation	Pd max	Ta ≤ 70 °C	510*	mW
Operating temperature range	Topr		-20 to 70	°C
Storage temperature range	Tstg		-55 to 150	°C

\* On the board (114.3×76.1×1.6mm)

### Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V <sub>CC</sub>	Pin 5, 10, 14, 15	5.0	V
Operating supply voltage range	V <sub>CC op</sub>	Pin 5, 10, 14, 15	4.5 to 5.5	V

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**AC Characteristics** at Ta = 25°C, VCC = 5.0V

Parameter	Symbol	Pin No.	Conditions	Ratings			Unit
				min	typ	max	
Circuit current	$I_{total}$	5, 10, 14, 15	No Signal	55	65	78	mA
RF input frequency range*1	$f_{(RF)}$	16, 17	$F_c = -3dB$	50		280	MHz
RF AGC range*1	GR1	19, 20	$V_6 = 0.5 \text{ to } 2.5V$	30	36		dB
Mixer conversion gain*1	CG1	19/16, 17 20/16, 17	$V_6 = 2.5V$	21	24	27	dB
Mixer inter modulation 1*1	IM3 1	19/16, 17 20/16, 17	Input = 70dB $\mu$ V $V_6 = 2.5V$	50	55		dB
IF input frequency range*2	$f_{(IF)}$	2, 3	$F_c = -3dB$	30		100	MHz
IF amplifier gain *2	$G_{(AGC)}$	8/2, 3 9/2, 3	$V_6 = 2.5V$	34	38	42	dB
IF inter modulation 2*2	IM3 2	8/2, 3 9/2, 3	Output = 104dB $\mu$ V @2tone each	45	50		dB
IF AGC range *2	GR2	8, 9	IF Output Level < $\pm 1dB$	6	8		dB
IF output level*2	$V_{O(IF)1}$	8	Single output		1.0		Vp-p
IF output level*2	$V_{O(IF)2}$	9	Single output		1.0		Vp-p

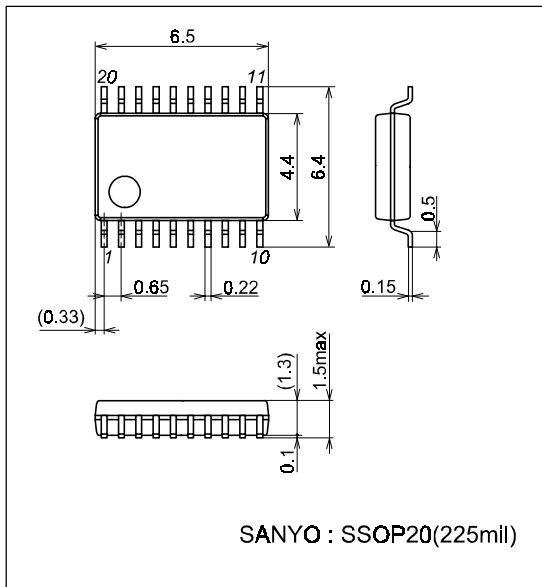
\*1 Measurement circuit 1

\*2 Measurement circuit 2

## Package Dimensions

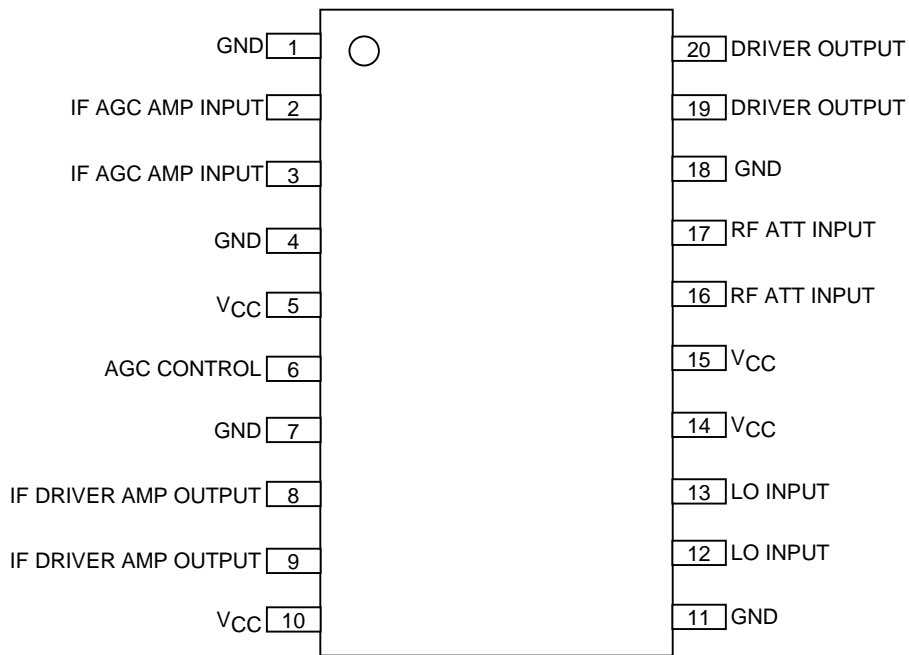
unit: mm

3179C

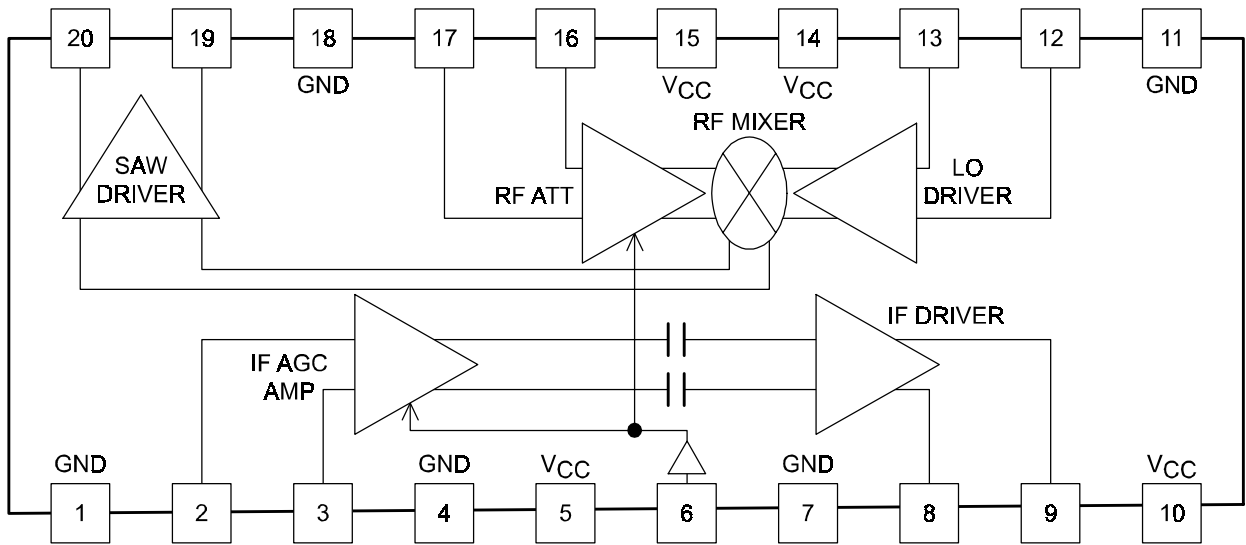


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## Pin Assignment



## Block Diagram

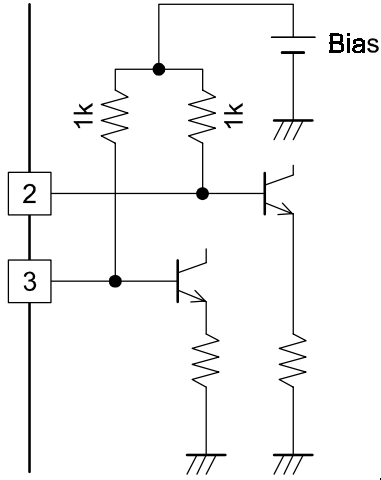
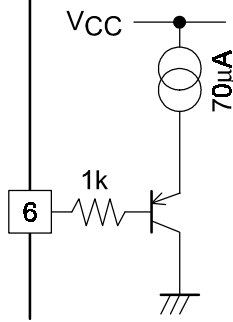
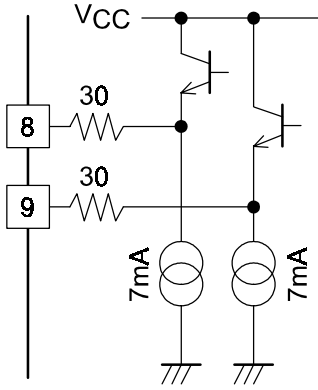


OMB05022

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## Pin Description

(unit:  $\Omega$ )

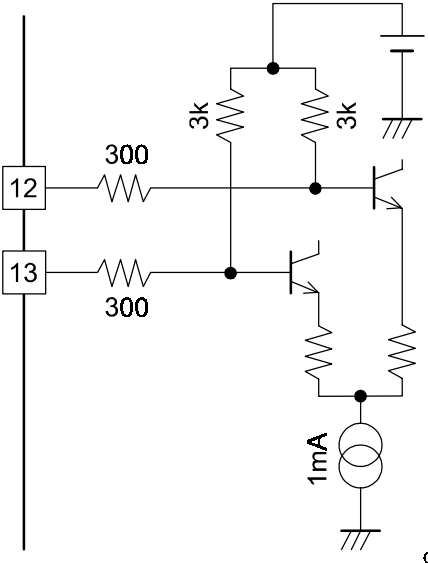
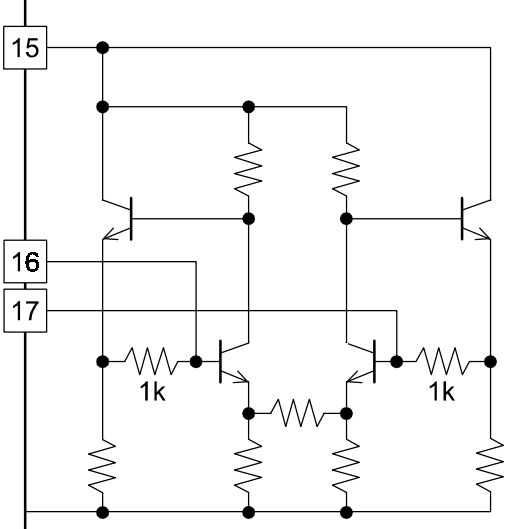
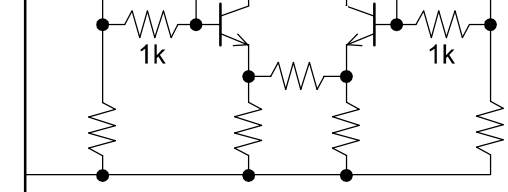
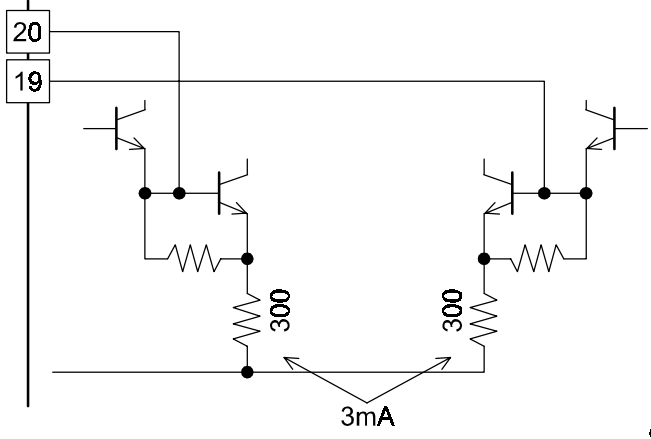
Pin Number	Description	Equivalent circuit
1	GND	
2 3	AGC Amp Input	 <p style="text-align: right;">OMP05096</p>
4	GND	
5	V <sub>CC</sub>	
6	AGC Control	 <p style="text-align: right;">OMP05097</p>
7	GND	
8 9	Post Amp Outputs	 <p style="text-align: right;">OMP05098</p>
10	V <sub>CC</sub>	
11	GND	

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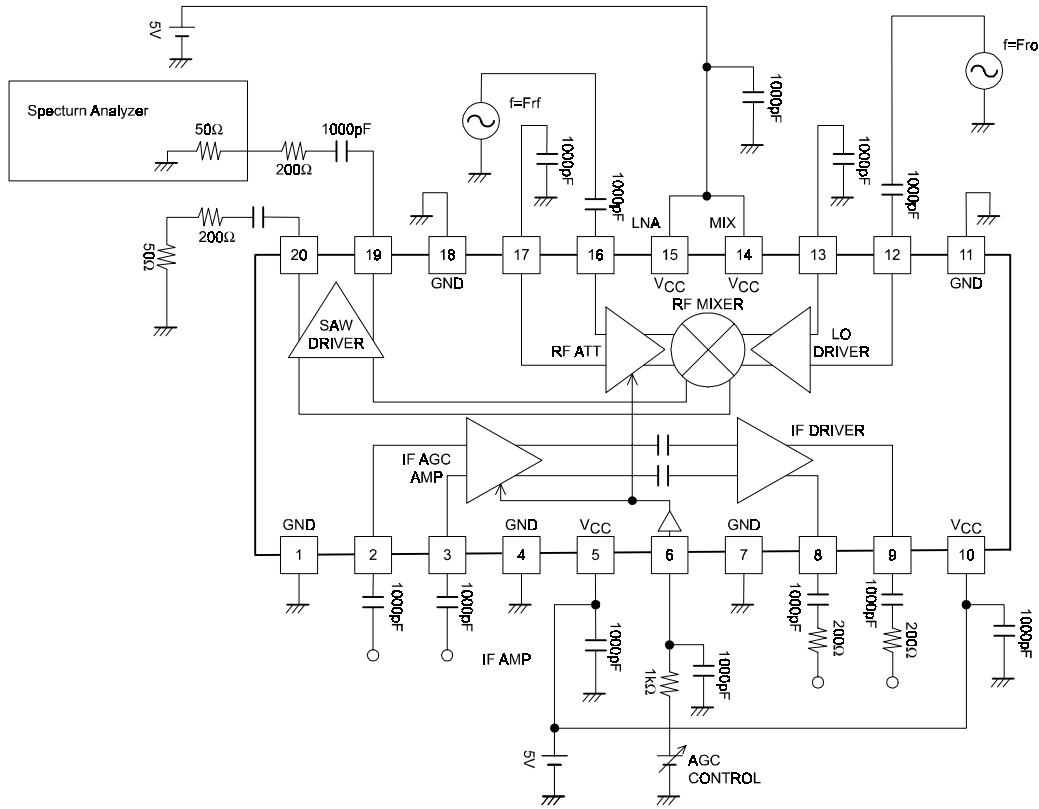
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(unit:  $\Omega$ )

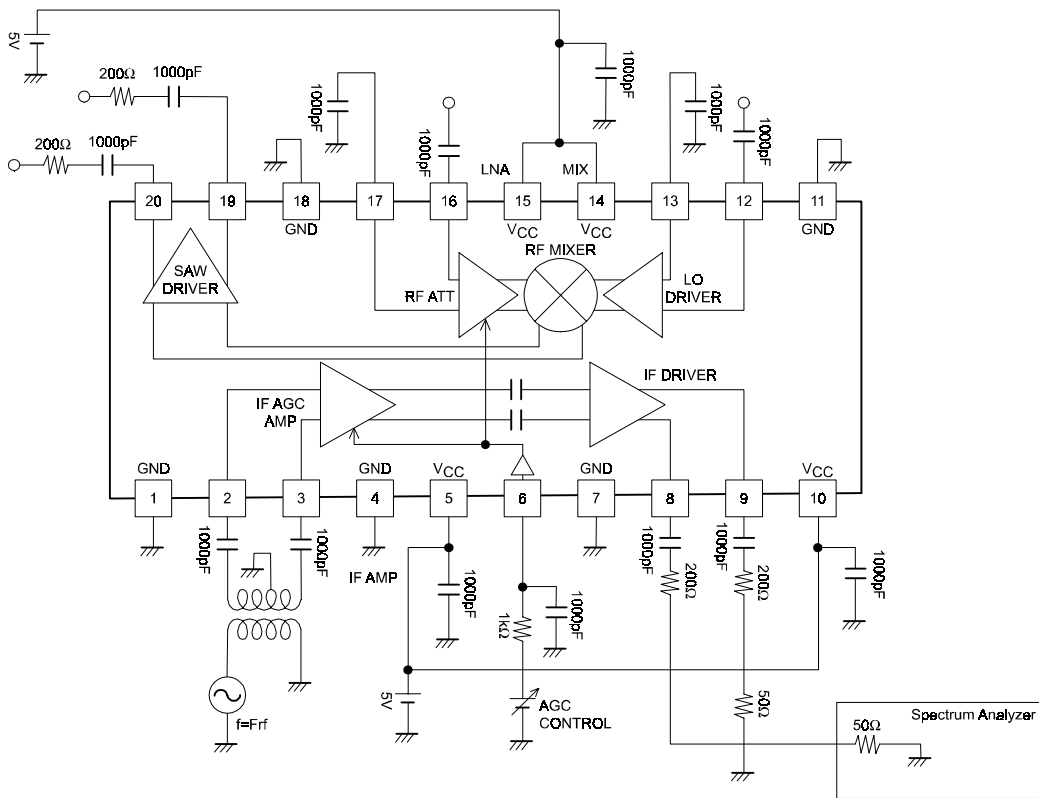
Pin Number	Description	Equivalent circuit
12 13	LO Input	 <p style="text-align: right;">OMP05099</p>
14 15	$V_{CC}$	 <p style="text-align: right;">OMP05100</p>
16 17	LNA Inputs	 <p style="text-align: right;">OMP05100</p>
18	GND	
19 20	Driver Outputs	 <p style="text-align: right;">OMP05101</p>

Measurement Circuit 1



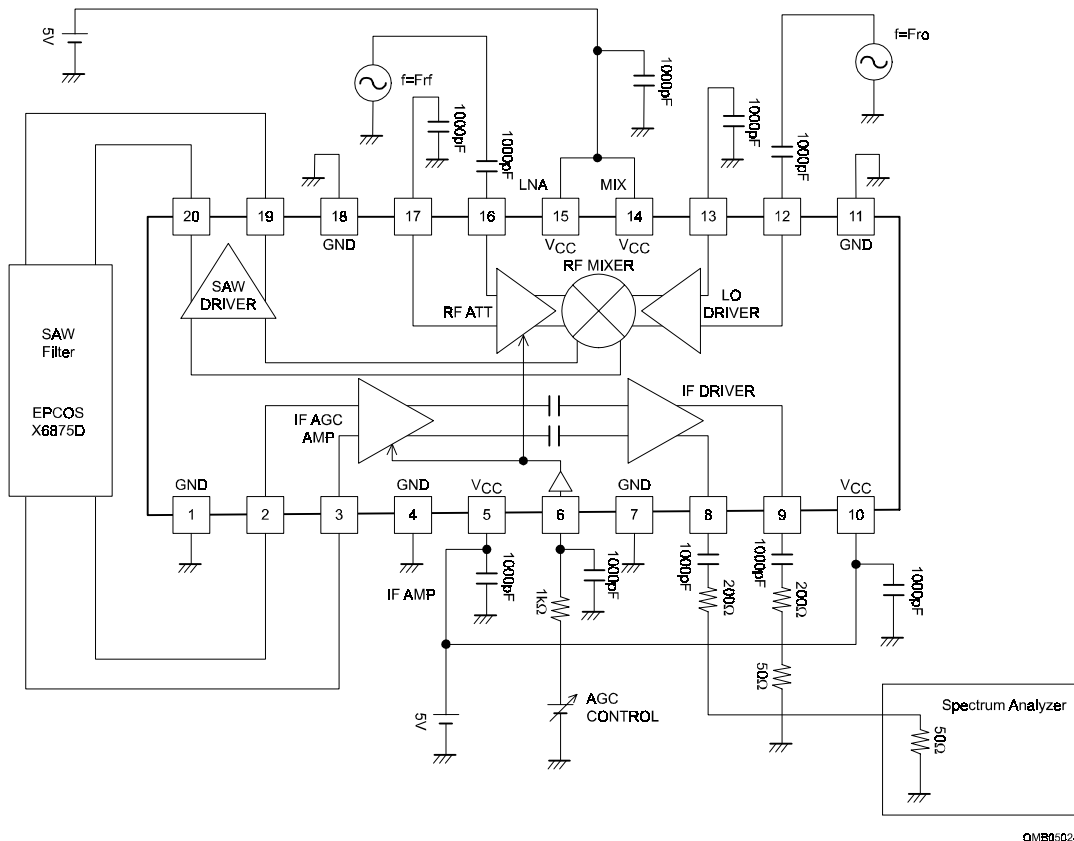
OMB05025

Measurement Circuit 2



OMB05026

## Application Circuit



OMB95024

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