

SANYO Semiconductors DATA SHEET

LB11966M — For Fan Motor 2-phase Half-Wave Driver

Overview

The LB11966M is a two-phase half-wave brushless motor driver for fan motor.

Functions

- 2-phase half-wave drive.
- RD (Rotation Detection) outputs incorporated.
- FG (Frequency Generation) outputs incorporated.
- Thermal shutdown circuit incorporated.
- Lock protection and automatic return function incorporated.
- Output protection zener diode incorporated.
- Hall input amplifier incorporated.

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		16	V
Output current	I _{OUT} ave		500	mA
	I _{OUT} peak	$t \le 1s$	1200	mA
Output withstand voltage	V _{OUT} max		Internal	V
FG/RD output current	I _{FG/RD} max		10	mA
FG/RD output withstand voltage	V _{FG/RD} max		16	V
Allowable power dissipation	Pd max	Mounted on a specified board *	800	mW
Operating temperature	Topr		-30 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

^{*} Specified board: 114.3mm × 76.1mm × 1.6mm, glass epoxy board.

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Recommended Operating Ranges at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	Vcc		3.0 to 15	V
Common-mode input voltage range	V _{COM}		0.2 to V _{CC} -2.3	V

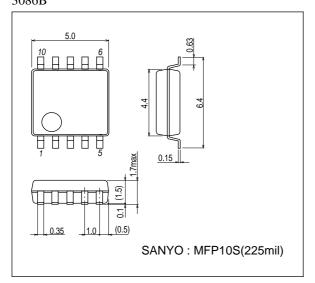
Electrical Characteristics at Ta = 25°C, $V_{CC}=12V$

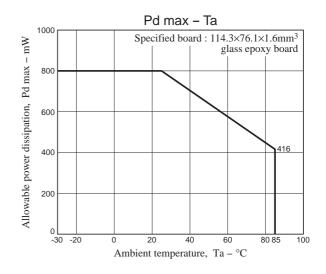
Parameter	Cumbal	Conditions	Ratings			Unit	
Faiametei	Symbol	Conditions	min	typ	max	Unit	
Circuit current I _{CC}		During driving (CT = L)		4	6	mA	
		During lock protection (CT = H)		3	5	mA	
CT capacitor charging current	I _{CT} 1	CT = 0.2	2.4	3.0	3.6	μΑ	
CT capacitor dis-charging current	I _{CT} 2	CT = 2.0	0.2	0.3	0.4	μА	
capacitor charging / dis-charging current ratio	RCT	R _{CT} = I _{CT} 1 / I _{CT} 2	8	10	12		
CT charging voltage	V _{CT} 1		1.4	1.6	1.8	V	
CT dis-charging voltage	V _{CT} ²		0.6	0.8	1.0	V	
Output limit withstand voltage	V _O LM	I _O = 10mA	23.5	25	26.5	V	
Output saturation voltage	V _{OL}	I _O = 500mA		0.95	1.3	V	
Hall input sensitivity	V _{HN}	Including offset and hysteresis		6	18	mV	
RD output saturation voltage	V_{RD}	I _{RD} = 5mA		0.2	0.5	V	
RD output leak current	I _{RL}	V _{RD} = 14V		0.1	10	μА	
RD output saturation voltage	V _{FG}	I _{FG} = 5mA		0.2	0.5	V	
RD output leak current	I _{FL}	V _{FG} = 14V		0.1	10	μА	
Thermal protection function operating temperature	VTH	Design target value *	150	180	200	°C	

^{*} Design target value and is not measured.

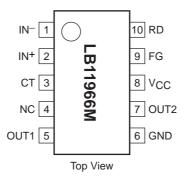
Package Dimensions

unit : mm (typ) 3086B





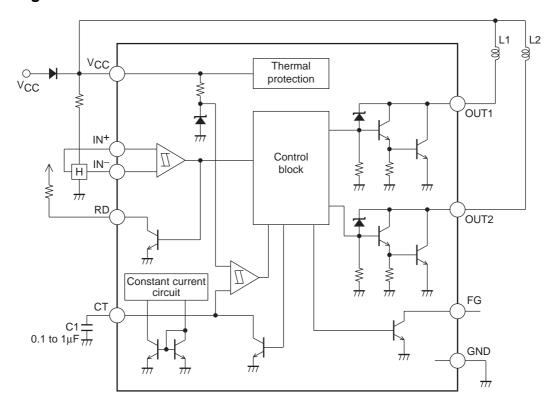
Pin Assignment



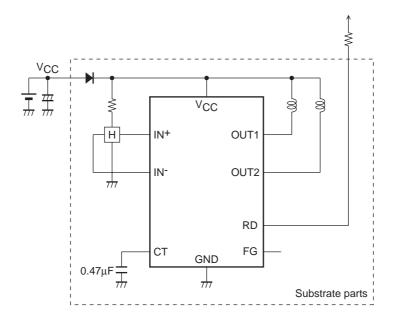
Truth table

IN-	IN+	СТ	OUT1	OUT2	RD	FG	Mode
Н	L	_	L	Н	L	L	Rotation
L	Н		Н	L	L	Н	Rotation
_	İ	Н	OFF	OFF	Н	-	Lock protection

Block Diagram



Application Circuit Example



Notice

- Take care not to cause interference due to wiring of IN- and OUT1.
- In application of connecting the CT pin to GND, lock protection and restart function are not effective.
- In a circuit configuration as shown above, a power supply/GND reverse connection will cause a current to flow as follows: GND → OUT → coil → power supply. The magnitude of this current is limited by the coil resistance. If it is less than 500mA, the IC will not be destroyed. If required, insert a diode between V_{CC} and the coil.
- The FG pin and RD pin are left open when not used.
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