Monolithic Digital IC

LB1978V



# Three-Phase Half-Wave Sensorless Motor Driver for Headphone Stereos

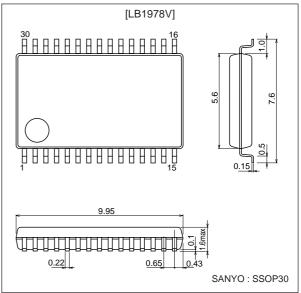
## **Functions and Features**

- Three-phase sensorless motor driver
- Built-in speed control
- Built-in reference voltage and forward/reverse switching pin
- Soft switching
- Built-in short brake drive pin

## **Package Dimensions**

unit: mm

#### 3191-SSOP30



## **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	Vcc max		2.0	V
Output transistor withstand voltage	Vsus		4	V
Maximum output current	lo max		0.6	А
Allowable power dissipation	Pd max	Tj = 125°C	0.4	W
Operating temperature	Topr		0 to 60	°C
Storage temperature	Tstg		-40 to +125	°C

#### Allowable Operating Ranges at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Power supply voltage	Vcc		1.0 to 1.7	V

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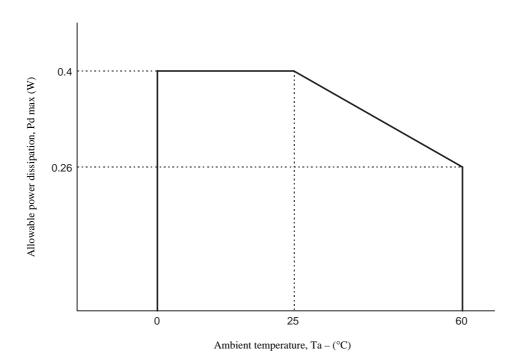
SANYO Electric Co., Ltd. Semiconductor Company TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

# Electrical Characteristics at Ta = 25 °C, Vcc = 1.2V, in the specified test circuit

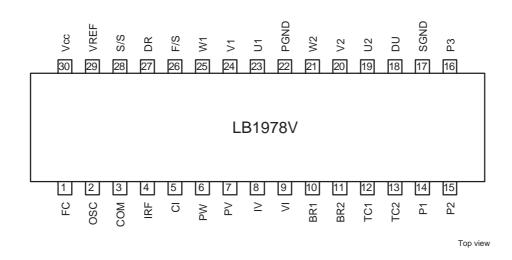
Devenenter	Symbol Conditions		Ratings			11.2
Parameter	Symbol	Conditions	min	typ	max	Unit
Power supply current	lcc	START pin H, RIRF = 180 k $\Omega$		6.8	10	mA
		START pin L, VBR = 0V		0	10	μΑ
Reference voltage	Vref		0.725	0.755	0.785	V
Reference voltage characteristics	$\frac{\Delta Vref}{\Delta Iref} / \Delta Vcc$	Vcc = 1.0 to 1.7V		1.0	2.0	%/V
Reference voltage load characteristics	ΔVref ΔIref	Iref = 0 to -50 μA	-0.2	-0.06		mV/μA
Oscillator cycle	T <sub>S</sub>	C <sub>S</sub> = 1500 pF	0.52	0.6	0.68	ms
Rotation switching load characteristics	T <sub>F/S</sub>	C <sub>S</sub> = 1500 pF, R <sub>IRF</sub> = 180 kΩ *Target	7.70		10.9	ms
Rotation detection accuracy	NF/S	T <sub>S</sub> = 0.60 ms *Target	-16		+16	%
COM voltage	V <sub>COM</sub> -	$R_{COM} = 1 k\Omega$	12	25	38	mV
F side output saturation voltage	Vsat1	Vcc = 1.0V, Im = 0.3A		0.17	0.30	V
S side output saturation voltage	Vsat2	Vcc = 1.0V, Im = 0.2A		0.12	0.25	V
S/S pin input High level voltage	V <sub>SSH</sub>		0.9			V
S/S pin input Low level voltage	V <sub>SSL</sub>				0.3	V
F/S DR pin ON voltage	V <sub>Lon</sub>				0.2	V
F/S DR pin OFF voltage	V <sub>Loff</sub>		Vcc-0.3			V
TC pin pull-in voltage	V <sub>TC</sub> 1	F/S = H	80	110	150	mV
	V <sub>TC</sub> 2	F/S = L	160	220	300	mV
BR1 ON voltage	V <sub>BRon</sub>		0.9			V
BR1 OFF voltage	V <sub>BRoff</sub>				0.3	V
BRsat voltage	V <sub>BRsat</sub>	lo = 6 mA, V <sub>BR1</sub> = 1.2V		0.15	0.3	V
CI rise voltage	V <sub>CI</sub>		0.620	0.650	0.680	V
CI hysteresis width	ΔV <sub>CI</sub>		60	80	100	mV
IRF voltage	V <sub>IRF</sub>	R <sub>IRF</sub> = 180 kΩ	0.725	0.755	0.785	V
VI output current	I <sub>VI</sub>	$V_{VI} = 0.3V, V_{IV} = GND$	26	30	34	μΑ
PV pin voltage	V <sub>PV</sub>		0.720	0.755	0.785	V
ΔIFC/ΔVVI ratio	$\Delta I_{FC} / \Delta V_{VI}$		150	210	250	μA/V
Output transistor OFF voltage	V <sub>OUT</sub> OFF		Vcc-0.3			V
Output transistor ON voltage	V <sub>OUT</sub> ON				0.3	V

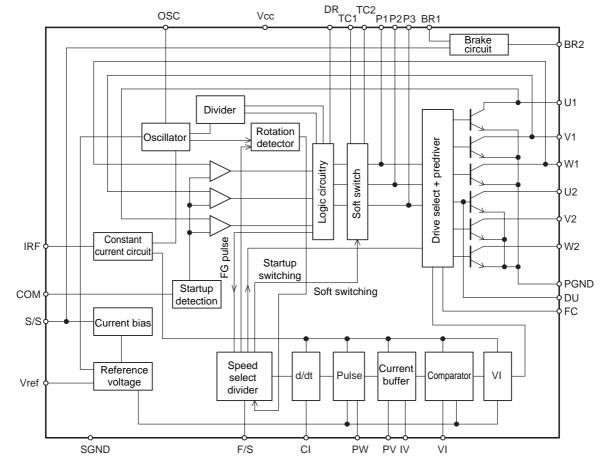
Note: Items shown to be "Target" are not measured.





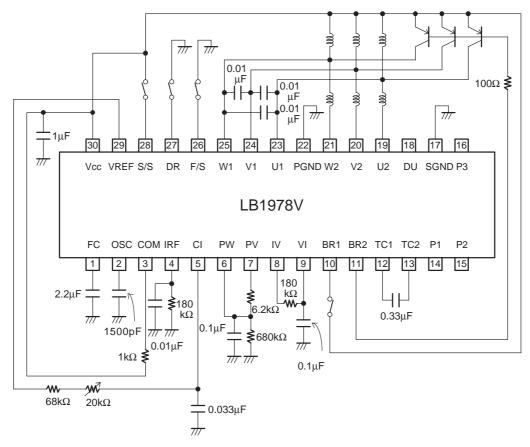






#### **Equivalent Circuit Block Diagram**





### **Pin Description**

Pin number	Pin name	Equivalent circuit	Pin function
1	FC	Vcc	Oscillator and ripple suppression pin.
		1 PGND	The higher the capacitance connected to FG, the more effectively will ripple components be suppressed.
2	OSC		Startup pulse cycle and drive switching cycle setting pin. Increased capacitance will result in higher startup pulse cycle and drive switching
			cycle.
3	СОМ	Vcc	Startup waveform detector offset setting pin. R <sub>COM</sub> = 1 kΩ results in approx. 25 mV offset at startup
4	IRF	Vcc (4) (4) (4) (25µA SGND	Oscillator circuit and F-V servo circuit internal current setting pin.
5	CI	Vcc 5 SGND	Speed adjustment pin using CR oscillation based on FG pulse edge detection.

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Pin number	Pin name	Equivalent circuit	Pin function
6	PW	Vcc 6 PGND	CI pin waveform and reference voltage comparator output pin.
7	PV	Vcc	Current buffer input/output pin.
8	IV	Vcc	Current-to-voltage converter comparator input pin.
9	VI	Vcc	Voltage-to-current converter input pin. Speed increases when VI pin voltage is higher than reference voltage and decreases when VI pin voltage is lower than reference voltage.
10	BRI	Vcc	Brake bias pin. When S/S pin is Low and BR1 pin is 0.9V or higher, brake drive pin BR2 goes ON.

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Pin number	Pin name	Equivalent circuit	Pin function
11	BR2	Vcc (11) SGND	Brake drive pin. When S/S pin is Low and BR1 pin is 0.9V or higher, brake drive is activated. This is an open-collector output.
12 13	TC1 TC2	Vcc 12 13 SGND	Motor current rise/fall slope setting pins. Setting value changes depending on the High or Low status of the F/S pin.
14 15 16	P1 P2 P3	Vcc (14) (14) (15) (16) SGND	Internal operation measurement pins which shape the current waveform. Must be left open for use.
17	SGND		Signal ground pin. Separate from power supply ground.
18 19 20 21	DU U2 V2 W2	Vcc 13 Vcc 13 Vcc Vcc 20 21 22 PGND	DU is base pin for U low-speed output transistor. U2, V2, and W2 are pins for connection to low-speed motor coils.
22	PGND		Power supply ground.

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Pin number	Pin name	Equivalent circuit	Pin function
23	U1		U1, V1, and W1 are pins for connection to
24 25	V1 W1	Vcc Vcc Vcc Vcc Vcc Vcc Vcc Vcc	high-speed motor coils.
26	F/S	26 SGND	High-speed/low-speed mode select pin. Vcc –1.0V or lower: high-speed (fast) Vcc –0.3V or higher: low-speed (slow)
27	DR	27 SGND	Rotation direction select pin. Vcc –0.3V or higher: phase sequence U -> V -> W Vcc –1.0V or lower: phase sequence U -> W -> V
28	S/S	28 Vcc SGND	Start/stop pin. 0.9V or higher: Start High active.
29	Vref	Vcc 29 SGND	Reference voltage pin. Reference voltage is 0.75V.
30	Vcc		Power supply pin.

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