LB1989



Three-Phase Sensorless VCR Drum Motor Driver

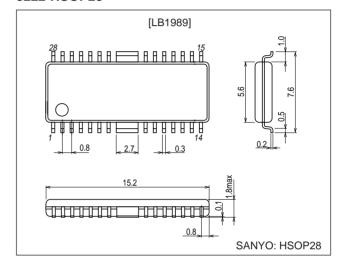
Functions and Features

- Soft switching drive
- No Hall sensors required.
- No FG sensors required.
- · Built-in PG amplifier
- Thermal shutdown circuit
- · Current limiter circuit

Package Dimensions

unit: mm

3222-HSOP28



Specifications

Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		14.5	V
Maximum output voltage	V _O max		14.5	V
Maximum input voltage	V _{I1} max		-0.3 to V _{CC} 1 + 0.3	V
Maximum cylinder current	I _O max		1.0	Α
Allowable power dissipation	Pdmax	Independent IC	0.6	W
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-55 to +150	°C

Allowable Operating Ranges at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V _{CC}		8 to 13.8	V

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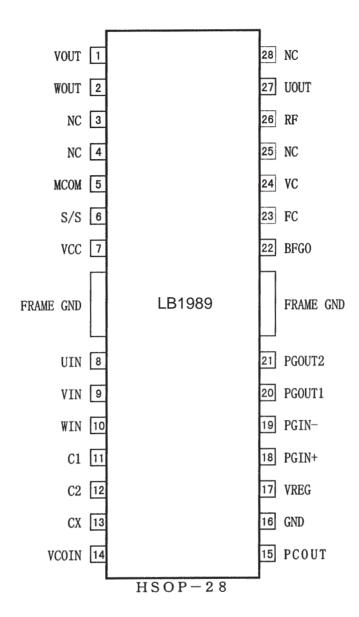
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Electrical Characteristics at $Ta=25^{\circ}C,\,V_{CC}$ = 12 V

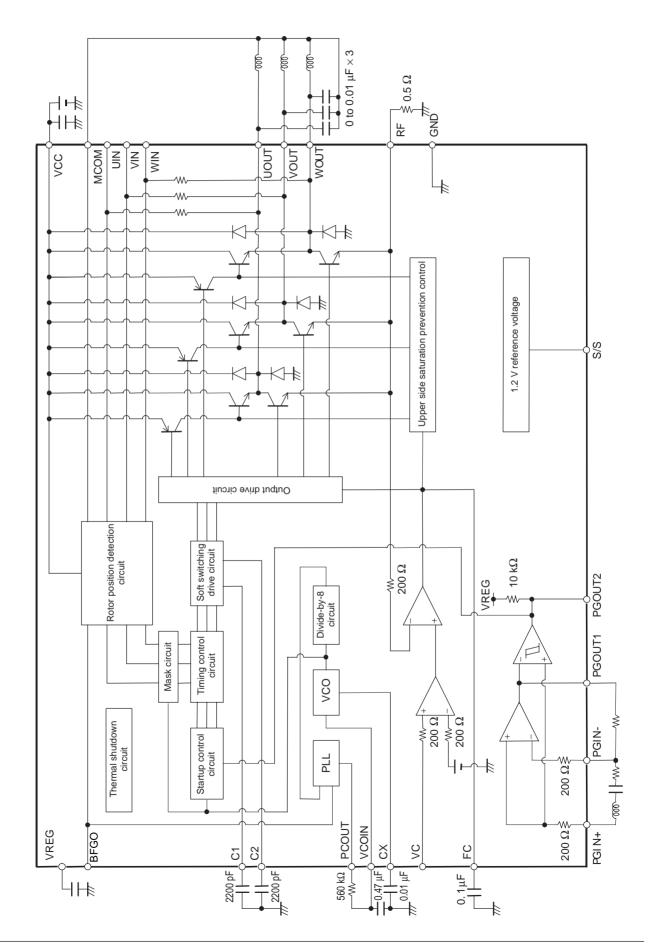
Parameter	Symbol	Conditions		Ratings			Toot oirquit
Faiailletei		Conditions	min	typ	max	Unit	Test circuit
Current drain	I _{CC}	V _C = 0 V		15	20	mA	1
Internal power supply	V _{REG}	V _C = 0 V	4.6	5.0	5.4	V	2
Output saturation voltage 1	V _{OSAT} 1	I _O = 0.4 A, Source + Sink		1.4	2.0	V	3
Output saturation voltage 2	V _{OU} 2	$I_O = 0.8 \text{ A}, \text{ RF} = 0 \Omega, \text{ Source} + \text{Sink}$		1.8	2.6	V	4
MC pin common-mode input voltage range	V _{IC}		0		V _{CC} - 2	V	5
VC pin input bias current	I _{VC}	V _C = 0 V	-2	-1		μΑ	6
Control start voltage	V _{THVC}		2.3	2.55	2.8	V	7
Closed-loop control gain	GMVC	RF = 0.5 Ω	0.75	0.95	1.15	A/V	8
PCOUT output current 1	I _{PCOU}	Source side		-90		μA	9
PCOUT output current 2	I _{PCOD}	Sink side		90		μA	10
VCOIN input current	I _{VCOIN}	V _{COIN} = 5 V		0.1	0.2	μA	11
Minimum VCO frequency	fV _{COMIN}	$Cx = 0.022 \mu F$, With V_{COIN} open		400		Hz	12
Maximum VCO frequency	fV _{COMAX}	Cx = 0.022 μF, V _{COIN} = 5 V		18.5		kHz	13
C1/C2 source current ratio	RSOURCE	IC1SOURCE / IC2SOURCE	-12		+12	%	14
C1/C2 sink current ratio	RSINK	IC1SINK / IC2SINK	-12		+12	%	15
C1 source/sink current ratio	RC1	IC1SOURCE / IC1SINK	-35		+15	%	16
C2 source/sink current ratio	RC2	IC2SOURCE / IC2SINK	-35		+15	%	17
S/S pin high level voltage	V _{SSH}		4			V	18
S/S pin low level voltage	V _{SSL}				0.7	V	19
S/S pin input current	I _{SSI}	V _{S/S} = 5 V			200	μA	20
Thermal shutdown circuit operating temperature	TTSD		150	180	210	°C	*
Thermal shutdown circuit hysteresis	ΔTTSD			15		°C	*
[FG/PG Amplifier Block]	•						•
Back EMF FG							
Output on voltage	V _{OL}				0.4	V	21
Output off voltage	V _{OH}		V _{REG} - 0.5			V	22
PG amplifier							•
Input offset voltage	V _{IO}		-8		+8	mV	23
Input bias current	I _{BIN} -		- 250			nA	24
Common-mode input voltage range	V _{ICOM}		0		V _{REG} - 1.5	V	*
Open-loop gain	GVPG	f = 1 kHz		55		dB	25
Output on voltage	V _{OL}				0.4	V	26
Output off voltage	V _{OH}		V _{REG} - 0.5			V	27
Schmitt amplifier hysteresis	V _{SHIS}		70	93	115	mV	28

Note *: These are design target values and are not measured.

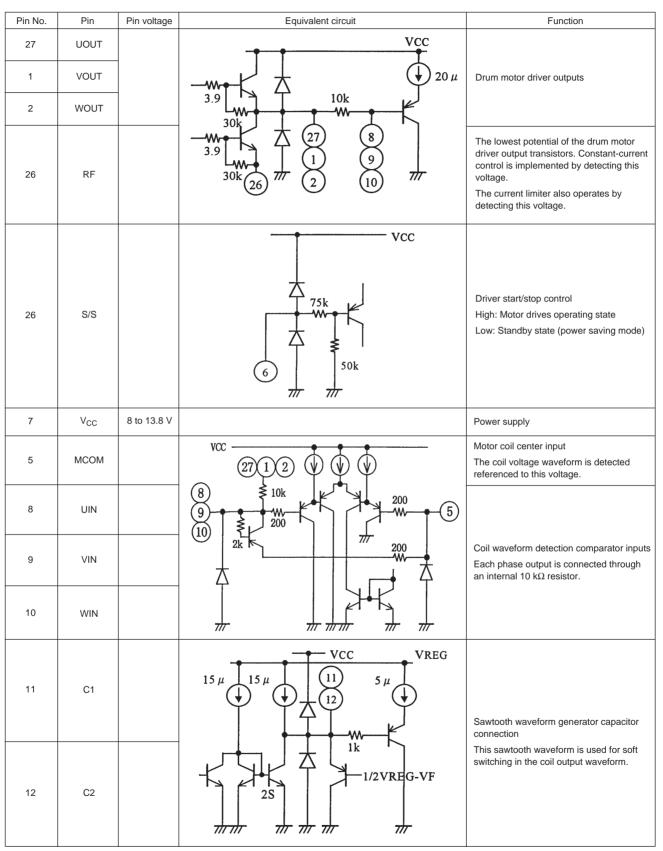
Pin Assignment



Block Diagram (Note that the external constants will vary depending on the motor used.)



Pin Functions



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Pin No.	Pin	Pin voltage	Equivalent circuit	Function
13	сх		VREG 100 μ 13 113	The value of the capacitor connected between this pin and ground determines the operating frequency range and the minimum operating frequency for the VCO circuit.
14	VCOIN		1.75V 14 50μ 50 μ	VCO circuit voltage input The PCOUT pin voltage is RC filtered and the result is input to this pin.
15	PCOUT		VREG VCC	VCO circuit PLL output
16	GND			Ground for all circuits other than the output transistor
17	VREG		VCC 17 39k 1.25V	Internal 5 V regulator This pin provides the control system power.

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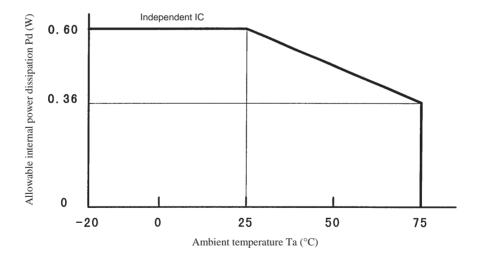
Pin No.	Pin	Pin voltage	Equivalent circuit	Function
18	PGIN+		VREG VCC 6 μ 6 μ 6 μ VCC \$10k	PG amplifier positive (+) input This pin is biased internally by 1/2 VREG.
19	PGIN-		200 200 10k	PG amplifier negative (–) input
20	PGOUT1		VREG 30 μ 38 ★ 5k 18	PG amplifier linear output
21	PGOUT2		VREG+VF VCC VREG VREG VREG	PG Schmitt amplifier output
22	BFGO		21)(22)	Motor back EMF detection FG output (synthesized from 3 phases)

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Pin No.	Pin	Pin voltage	Equivalent circuit	Function
23	FC		VREG VCC VCC 23	Frequency characteristics correction Current control system closed loop oscillation can be stopped by inserting a capacitor between this pin and ground.
24	VC	0 to V _{CC}	VCC 50 μ 50 μ 27k 40k 224k	Speed control This IC implements constant-current control by applying feedback from RF.

Allowable Internal Power Dissipation



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