

SANYO

No. ✕ 4972

LC9997G**1/5 Inch Optical Size NTSC Color
Solid-State Imaging Device****Preliminary****Overview**

The LC9997G is a 1/5 inch optical size frame transfer CCD (charge-coupled device) solid-state imaging device.

Features

- Effective number of pixels [total pixels]:
508 × 492 [532 × 500] (H × V)
- Number of optical blacks
Horizontal direction: Front: 2 pixels
Rear: 22 pixels
Vertical direction: Front: 5 pixels
Rear: 3 pixels
- High sensitivity and low noise
- Blooming is suppressed and smear is minimal.
- No residual images, burning in, or figure distortion
- Miniature size supports compact application designs.
- Superlative resistance to vibration, long life, and high reliability
- The LC9997G is provided in a 20-pin half-pitch completely transparent DIP package.
- Horizontal shift register, 5 V operation
- Can be used with a variable-speed electronic shutter

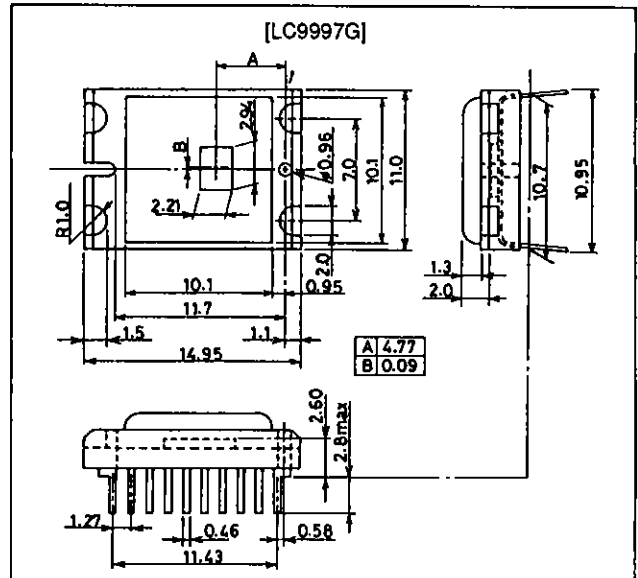
Device Structure

- 1/5 inch optical size frame transfer CCD image sensor
- Unit cell size: 5.8 μm (H) × 4.5 μm (V)
- Chip size: 4.180 mm (H) × 5.100 mm (V)
- Parallel gate CCD sensor
- Built-in high-sensitivity output amplifier
- Cy-G-Ye stripe color compensation filter

Note: Please contact your Sanyo sales representative in advance if you plan to design an optical system for this product.

Package Dimensions

unit: mm

3208A**SANYO Electric Co., Ltd. Semiconductor Business Headquarters**

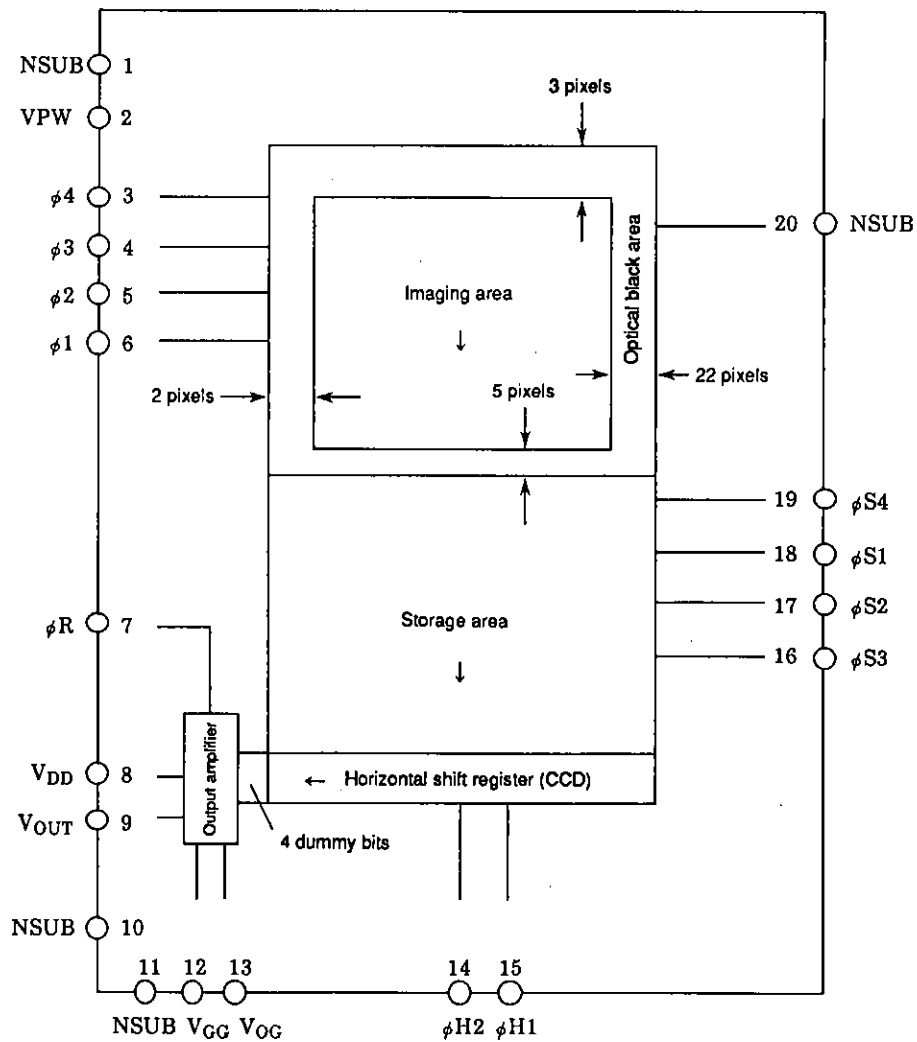
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110 JAPAN

Specifications

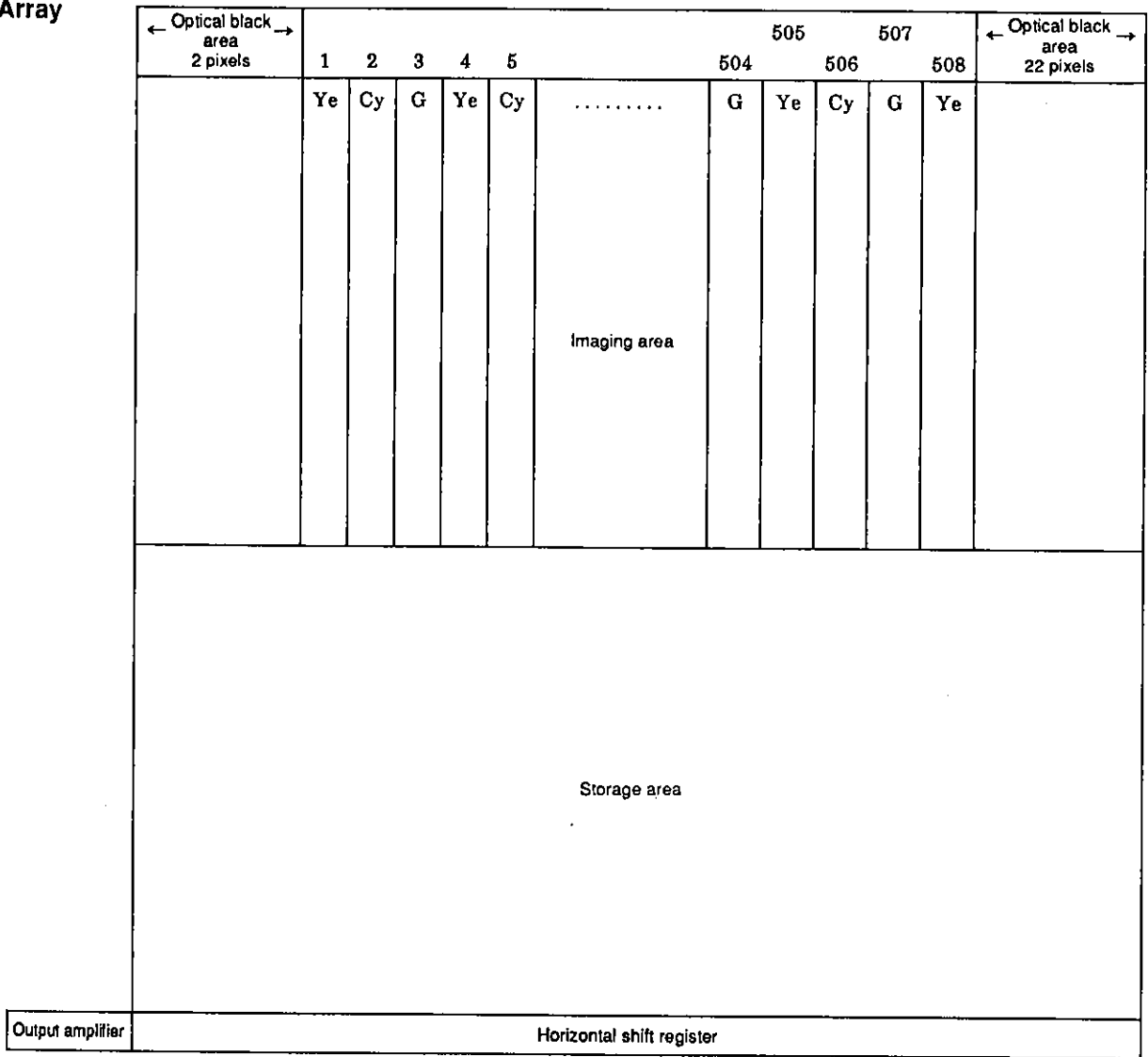
Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{DD}	V _{PW} = 0 V	-0.3 to +18	V
	NSUB	V _{PW} = 0 V	-0.3 to +50	V
	NSUB	ø1 to 4, øS1 to 4 = 0 V	-0.3 to +55	V
	V _{GG}	V _{PW} = 0 V	-0.3 to +5	V
Horizontal clock pin	øH1, 2, øR	V _{PW} = 0 V	-0.3 to +10	V
Other clock pins	—	V _{PW} = 0 V	-15 to +18	V
All other pins	—	V _{PW} = 0 V	-0.3 to +10	V
Operating temperature	T _{opr}		-10 to +60	°C
Storage temperature	T _{stg}		-30 to +80	°C

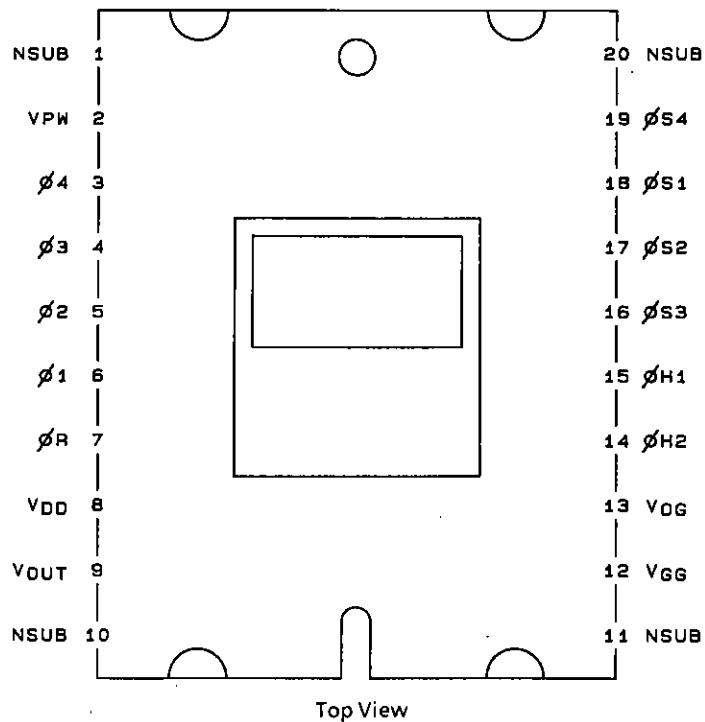
Block Diagram



Pixel Array



Pin Assignment



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Pin Functions

Pin No.	Symbol	Pin function	Pin No.	Symbol	Pin function
1	NSUB	N-substrate	20	NSUB	N-substrate
2	VPW	P-well	19	øS4	Storage area clock
3	ø4	Imaging area clock	18	øS1	Storage area clock
4	ø3	Imaging area clock	17	øS2	Storage area clock
5	ø2	Imaging area clock	16	øS3	Storage area clock
6	ø1	Imaging area clock	15	øH1	Horizontal shift register clock
7	øR	Reset gate	14	øH2	Horizontal shift register clock
8	V _{DD}	Power supply	13	V _{OG}	CCD output gate
9	V _{OUT}	CCD output	12	V _{GG}	Load gate
10	NSUB	N-substrate	11	NSUB	N-substrate

Clock Voltage/Frame Shift Frequency = 3.58 MHz

Parameter	Symbol	Conditions	min	typ	max	Unit
Imaging block pulses: ø1, ø2, ø3, ø4						
Pulse amplitude	V _{PIF}		14.0	14.5	15.0	V
Low level	V _{LIF}		-10.0	-9.5	-9.0	V
Storage block pulses: øS1, øS2						
Pulse amplitude	V _{PSL}		14.0	14.5	15.0	V
Low level	V _{LSL}		-7.0	-6.5	-6.0	V
Storage block pulses: øS3, øS4						
Pulse amplitude	V _{PSL}		14.0	14.5	15.0	V
Low level	V _{LSL}		-7.5	-7.0	-6.5	V
Horizontal transfer pulses: øH1, øH2						
Pulse amplitude	V _{PH}		4.5	5.0	5.5	V
Low level	V _{LH}		0	0	0.5	V
Reset gate: øR						
Pulse amplitude	V _{PR}		4.5	5.0	5.5	V
Low level	V _{LR}		4.2	4.5	4.8	V
Substrate pulse: øNSUB						
High level	V _{HNSUB} *	See Figure 1	30.0		40.0	V
Low level	V _{LNSUB}	See Figure 1	17.5	18.0	18.5	V

Note: Adjust the V_{HNSUB} saturation output to its maximum value without causing picture degradation.

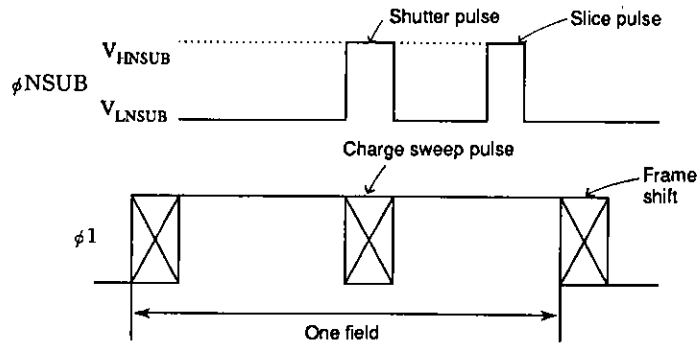


Fig. 1

Bias Conditions

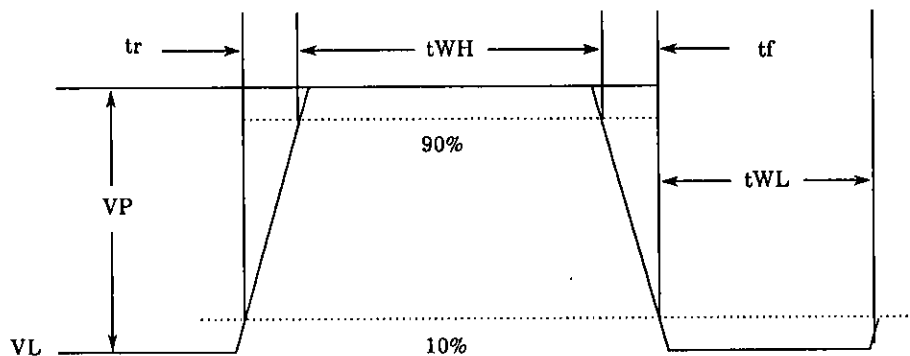
Parameter	Symbol	Conditions	min	typ	max	Unit
P-well	VPW			0		V
Output circuit voltage	V _{DD}	*1	14.5	15.0	15.5	V
	V _{GG}	*2	1.2	1.5	1.8	V
OG bias	V _{OG}	*2	4.5	5.0	5.5	V

Note: 1. This level must not become higher than the substrate pulse low level V_{LSUB}.
 2. These pins are high impedance inputs.

DC Characteristics

Parameter	Symbol	Conditions	min	typ	max	Unit
DC operating current	I _{DD}		4.5	7.0	11.0	mA

Drive Pulse Waveform Standards



Note: VP: Pulse amplitude
 VL: Low level

Symbol	tWH	tWL	tr	tf	Unit	Notes
	typ	typ	typ	typ		
ø1	110	110	30	30	ns	During frame transfer When the frame shift frequency is 3.58MHz
ø2	110	110	30	30		
ø3	110	110	30	30		
ø4	110	110	30	30		
øS1	110	110	30	30	ns	During frame transfer When the frame shift frequency is 3.58 MHz
øS2	110	110	30	30		
øS3	110	110	30	30		
øS4	110	110	30	30		
øS1	1.8	61.3	20	20	ns	During 1H line vertical transfer The figures enclosed in dark lines are in µs units.
øS2	1.8	61.3	20	20		
øS3	61.3	1.8	20	20		
øS4	61.3	1.8	20	20		
øR	17	80	4	4	ns	Reset pulse
øH1	48	49	4	4		During horizontal transfer
øH2	48	49	4	4		
øNSUB	7.6	16.6*	2	0.4	µs	Slice pulse. * The units are ms.
øNSUB	1.0	16.6	3	0.05	µs	Shutter pulse

Imaging Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	min	typ	max	Unit
Sensitivity	S	Test method 1	175	220	265	mV
Video signal non-uniformity	VF	Test method 2			15	%
Saturated signal level	Vsat	Test method 3*	600			mV
Smear	SM	Test method 4*		0.04		%
Dark signal	Vdrk	Test method 5, Ta = 55°C			7	mV
Color unevenness	Vcs	Test method 6			40	mV
γ characteristics	γ			1		—

Note: For a frame shift frequency of 3.58 MHz.

When the evaluation board pulse signal VHSUB level is adjusted to be 40.0 V, the minimum value of the saturation output level will be 500 mV.
If required, exchange the limiting sample.

Test Methods

The following tests are performed with the CCD device to be measured mounted on the Sanyo evaluation board.

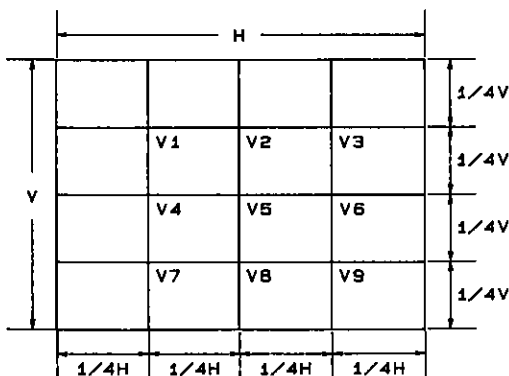
1. Sensitivity

Set up a CCV51F pattern box (Dai Nippon Printing Co., Ltd., intensity: 1500 NT, color temperature: 5100°K) with no pattern, and take an image with a 1 mm C-500 thickness infrared blocking filter mounted in front of a Fujinon HF16A lens. Set the lens aperture to f8, and set the separation between the lens and the pattern box to be 50 cm. Measure the CCD output Ye signal from the center of the image in this state.

$$VF = \frac{V_{max} - V_{min}}{(V1 + V2 + V3 + V4 + V5 + V6 + V7 + V8 + V9) / 9} \times 100$$

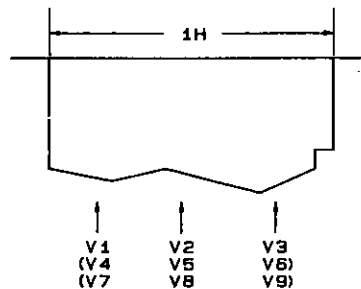
2. Video signal non-uniformity

Measure the CCD output signal for the nine points in the image indicated in Figure 2 in the test method 1 setup state. Derive the non-uniformity (VF) from formula 1.



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Fig. 2 Image Measurement Points



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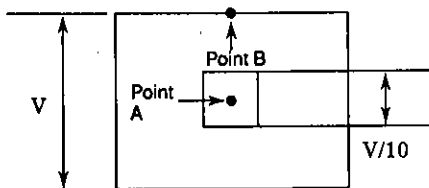
Fig. 3 1H Measurement Points

3. Saturated signal level

In the test method 1 setup state, remove the lens to saturate the output signal. Measure the CCD output signal from the center of the image at this time.

4. Smear

- Place a 1/10 V chart in front of a halogen light source as described below and take the image.
- Adjust the image (input) light intensity using ND filters so that the output signal at point A, i.e., the CCD output, becomes 250 mV.
- Remove the ND filter and measure the output value for the first line (at point B) in the CCD output signal.



$$SM = \frac{VB \times TND}{250} \times 100 (\%)$$

VB: The amount of smear (mV) at point B
 TND: Transmittance of the ND filter

Fig. 4

5. Dark signal

Block all light falling on the imaging element surface and measure the CCD output signal from the center of the image. At this time, do not take the difference between the signal level and the optical black segment level, but rather take the difference with the no signal level that has no pixel information. See figure 5.

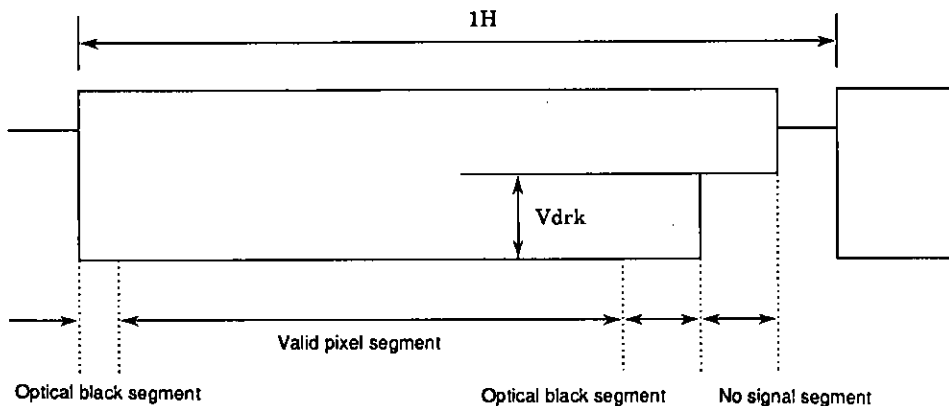


Fig. 5 Structure of the 1H Period

6. Color unevenness

Set up a CCV51F pattern box (Dai Nippon Printing Co., Ltd., intensity: 1500 NT, color temperature: 5100°K) and take an image with no pattern. Use the Sanyo LC9997-LE01 lens and attach a CM-500 infrared cut filter. With this setup, use ND filters to adjust the CCD output level to be 500 mV. Measure the chroma level and chroma phase at 16 points on the screen with this setup. Calculate the color unevenness level Vcs (mV) from these chroma level and chroma phase values.

$$BL = \frac{\sum_{i=1}^8 V_L(i) \cos \theta_L(i)}{8}$$

$$RL = \frac{\sum_{i=1}^8 V_L(i) \sin \theta_L(i)}{8}$$

$$BR = \frac{\sum_{i=1}^8 V_R(i) \cos \theta_R(i)}{8}$$

$$RR = \frac{\sum_{i=1}^8 V_R(i) \sin \theta_R(i)}{8}$$

$$V_{cs} = \sqrt{(BL - BR)^2 + (RL - RR)^2}$$

V_L, V_R: Chroma levels (mV)
 θ_L, θ_R: Chroma levels (°)

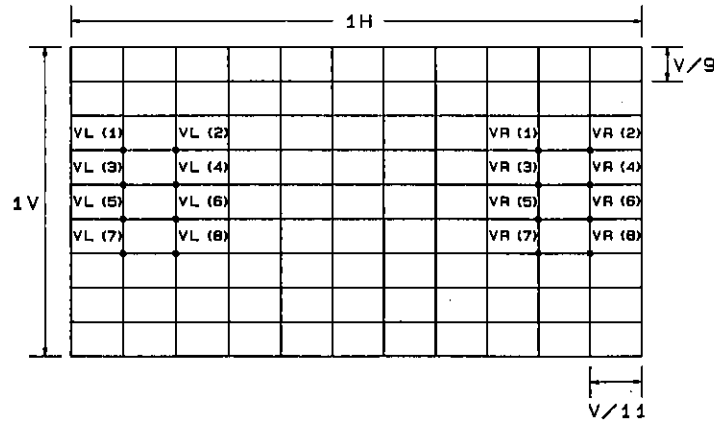


Fig. 6

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Notes on Handling and Mounting

1. Static discharge prevention

The following measure for static discharge prevention must be taken, since the sensor is easily destroyed by static discharges.

- Personnel and all equipment must be grounded when handling the sensor. For safety, personnel should be grounded through a resistor of about 1 M Ω . (The use of wrist straps is recommended.)
- Work should be performed either with bare hands or antistatic gloves. Antistatic work uniforms should be used. Also, conductive shoes should be worn.
- Spread conductive mats on the work place floors and on workbenches so that static charges do not arise.
- We recommend using an ionized air blower to remove static charges when handling CCD sensors.
- Use antistatic processed boxes to transport printed circuit boards with mounted CCD devices.

2. Soldering

- The package temperature must never exceed 80°C.
- In addition to static discharges, CCD sensors are also easily destroyed by thermal stress. The soldering iron tip temperature should be under 300°C when mounting on a printed circuit board, and 2 seconds per pin should be set as the target soldering time.
- Only use soldering irons that have a temperature controller that holds the soldering iron tip at a fixed temperature.
- Use extra care to avoid heating the element to over 80°C when re-soldering or removing a CCD element.

3. Dirt and contamination

- Work should be performed in a clean environment. (A class 1000 level is appropriate.)
- Do not touch the package surface or allow any objects to contact the surface. If dirt or other contamination gets on the package surface remove it with an air blower. (We recommend the use of an ionized air blower if possible.)
- Oily or greasy contamination can be removed with a cotton swab dipped in ethyl alcohol. Be extremely careful not to scratch the package surface.
- Store CCD devices in their special-purpose cases to avoid dirt and other contamination and to prevent condensation when transporting into a room with a radically differing temperature, warm or cool the device in advance.
- For devices supplied with protective tape, remove the tape immediately prior to use only after thorough antistatic measures have been implemented. Do not reuse the protective tape.

4. Storage methods

- Do not subject CCD products to intense light for extended periods.
- Since severe high temperature/high humidity conditions can adversely influence device characteristics, avoid storing CCD products in such environments.
- Since CCD sensors are high precision optical components, they must be protected from mechanical shocks.

5. Notes on mounting

- The use of a lens with an optical size of over 1/5 inch can result in flare. Consult your Sanyo sales representative before selecting a lens.
- This product is mounted in a fully clear plastic package, and is susceptible to light entering from the back of the printed circuit board that it is mounted on. Therefore end products must provide adequate protection from stray light.