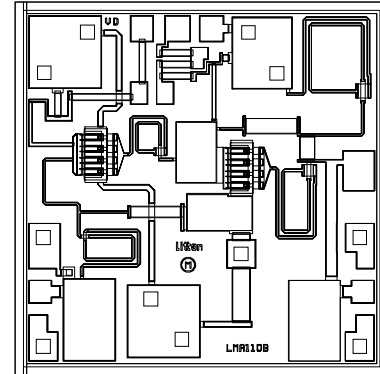


**Features**

- 3.5dB Typical Noise Figure
- 12.5dB Typical Gain
- 12dBm Saturated Output Power
- 12dB Input/Output Return Loss Typical
- 0.5-6GHz Frequency Bandwidth
- +8 Volts Single Bias Supply
- DC Decoupled RF Input and Output
- Chip Size : 1.62mmX1.62mm (.064"X.064")
- Chip Thickness : 100µm
- Pad Dimension : 100µm<sup>2</sup>



**Description**

The Filtronic LMA110B is a GaAs monolithic distributive amplifier which operates from 0.5 to 8 GHz. This amplifier produces a typical gain of 12.5dB with a noise figure of 3.5dB. The LMA110B is suitable for wide-band low noise gain block, EW and commercial PCN applications. DC decoupled input and output RF port. Ground is provided to the circuitry through vias to the backside metallization.

**Electrical Specifications at T<sub>a</sub>=25°C**

(V<sub>DD</sub>=+8.0V, Z<sub>in</sub>=Z<sub>out</sub>=50Ω)

Symbol	Parameter	Test Conditions	Min.	Limit Typ.	Max.	Units
BW	Operating Bandwidth		0.5		8	GHz
S21	Small Signal Gain	V <sub>D</sub> =8V, V <sub>g1</sub> =V <sub>g2</sub> =8V	11	12.5		dB
I <sub>ds</sub>	Drain Operating Current		60	85	110	mA
ΔS21	Small Signal Gain Flatness			±1	±1.5	dB
NF	Noise Figure	@ 50% I <sub>dss</sub>		3.5	4.5	dB
RL <sub>in</sub>	Input Return Loss			-10	-8	dB
RL <sub>out</sub>	Output Return Loss			-10	-8	dB
S12	Reverse Isolation			-30		dB
P-1dB	1-dB Gain Compression Power		8	10		dBm

**Absolute Maximum Ratings**

Symbol	Parameter/Conditions	Min.	Max.	Units
V <sub>dd</sub>	Drain Supply Voltage		13	Volts
I <sub>dd</sub>	Total Drain Current		110	mA
P <sub>in</sub>	RF Input Power		24	dBm
P <sub>t</sub>	Power Dissipation		1.5	W
T <sub>ch</sub>	Operating Channel Temperature		150	°C
T <sub>stg</sub>	Storage Temperature	-65	165	°C
T <sub>max</sub>	Max. Assembly Temp. (1 min. max.)		300	°C

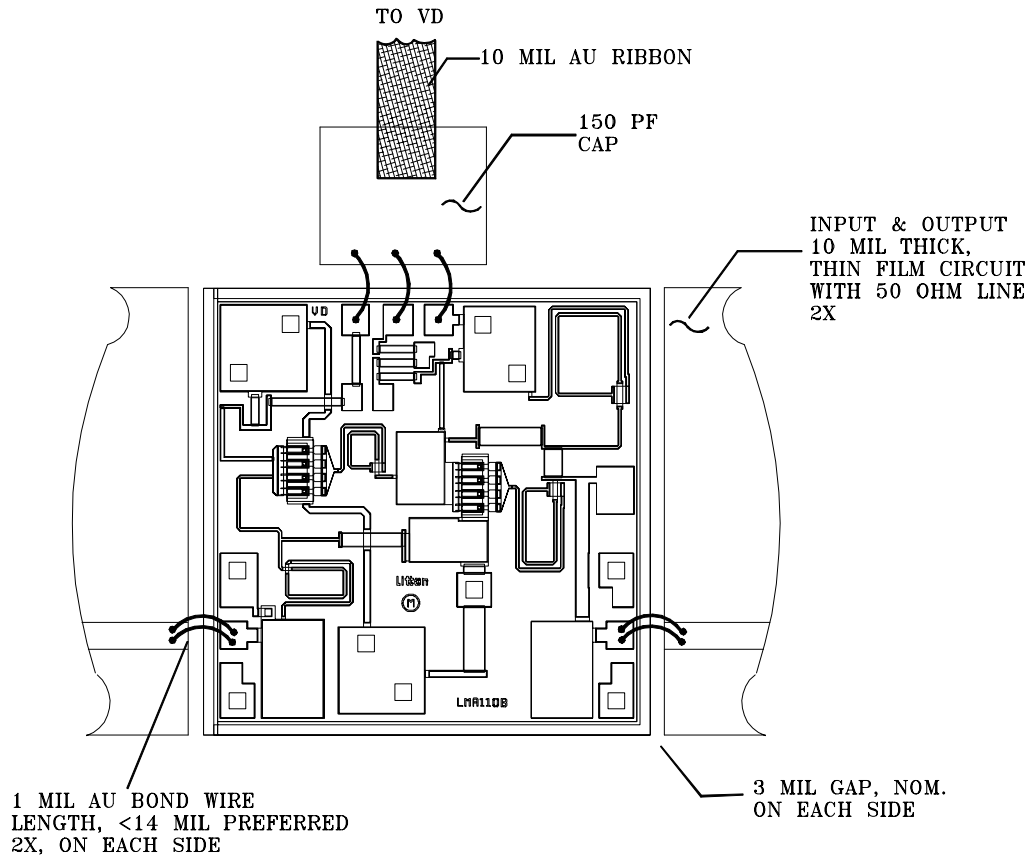
**Notes :**

1. This GaAs MMIC is susceptible to damage from Electrostatic Discharge. Proper precautions should be used when handling these devices.
2. Specifications subject to change without notice.

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**Assembly Diagram**

**SIMPLE BIAS SCHEME**



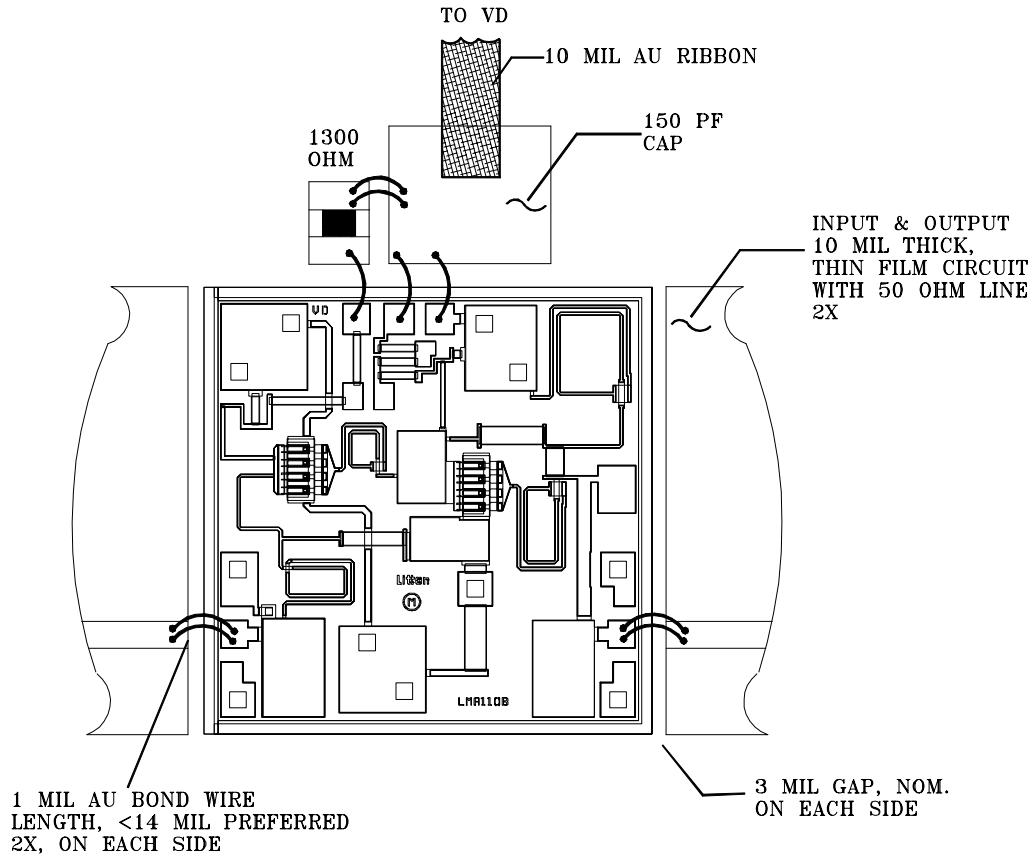
**Notes:**

- 1.) Recommended lead bond technique is thermo-compression wedge bonding with 0.001" (25µm) diameter wire. The bond tool force shall be 35-38 gram. Bonding stage temperature shall be 230-240°C, heated tool (150-160°C) is recommended. Ultrasonic bonding is not recommended.
- 2.) The recommended die attach is Ablebond silver epoxy, the stabilize bake temperature is set at 150°C for 45 minutes.
- 3.) Bond on bond or stitch bond acceptable.
- 4.) Conductor over conductor acceptable. Conductors must not short.

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**Assembly Diagram**

**OPTIMUM BIAS SCHEME**

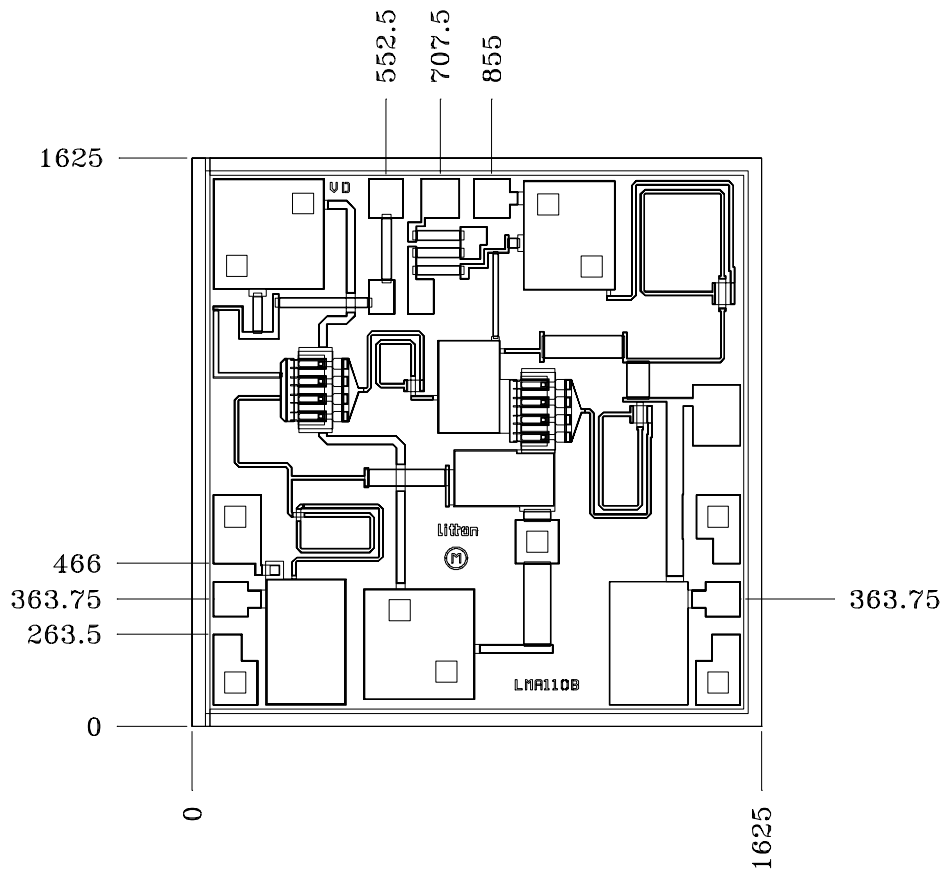


**Notes:**

- 1.) Recommended lead bond technique is thermo-compression wedge bonding with 0.001" (25µm) diameter wire. The bond tool force shall be 35-38 gram. Bonding stage temperature shall be 230-240°C, heated tool (150-160°C) is recommended. Ultrasonic bonding is not recommended.
- 2.) The recommended die attach is Ablebond silver epoxy, the stabilize bake temperature is set at 150°C for 45 minutes.
- 3.) Bond on bond or stitch bond acceptable.
- 4.) Conductor over conductor acceptable. Conductors must not short.

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**Mechanical Outline**



**Notes:**

- 1.) Unless Otherwise specified.
- 2.) All units are in micron ( $\mu\text{m}$ ).
- 3.) All bond pads are  $100 \times 100 \mu\text{m}^2$ .
- 4.) Bias pad ( $V_{DD}$ ) size is  $100 \times 121.5 \mu\text{m}^2$ .

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