



# M4N26

## 6-Pin DIP Optoisolators Transistor Output

The M4N26 device consists of a gallium arsenide infrared emitting diode optically coupled to a monolithic silicon phototransistor detector.

- Most Economical Optoisolator Choice for Medium Speed, Switching Applications
- Meets or Exceeds All JEDEC Registered Specifications

### Applications

- General Purpose Switching Circuits
- Interfacing and coupling systems of different potentials and impedances
- I/O Interfacing
- Solid State Relays

### MAXIMUM RATINGS (T<sub>A</sub> = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
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#### INPUT LED

Reverse Voltage	V <sub>R</sub>	3	Volts
Forward Current — Continuous	I <sub>F</sub>	60	mA
LED Power Dissipation @ T <sub>A</sub> = 25°C with Negligible Power in Output Detector Derate above 25°C	P <sub>D</sub>	100	mW
		1.41	mW/°C

#### OUTPUT TRANSISTOR

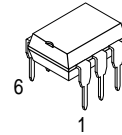
Collector–Emitter Voltage	V <sub>CEO</sub>	30	Volts
Emitter–Collector Voltage	V <sub>ECO</sub>	7	Volts
Collector–Base Voltage	V <sub>CBO</sub>	70	Volts
Collector Current — Continuous	I <sub>C</sub>	50	mA
Detector Power Dissipation @ T <sub>A</sub> = 25°C with Negligible Power in Input LED Derate above 25°C	P <sub>D</sub>	150	mW
		1.76	mW/°C

#### TOTAL DEVICE

Isolation Surge Voltage <sup>(1)</sup> (Peak ac Voltage, 60 Hz, 1 sec Duration)	V <sub>ISO</sub>	7500	Vac(pk)
Total Device Power Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	250 2.94	mW mW/°C
Ambient Operating Temperature Range <sup>(2)</sup>	T <sub>A</sub>	–55 to +100	°C
Storage Temperature Range <sup>(2)</sup>	T <sub>stg</sub>	–55 to +150	°C
Soldering Temperature (10 sec, 1/16" from case)	T <sub>L</sub>	260	°C

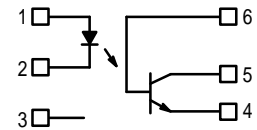
1. Isolation surge voltage is an internal device dielectric breakdown rating.  
For this test, Pins 1 and 2 are common, and Pins 4, 5 and 6 are common.
2. Refer to Quality and Reliability Section in Opto Data Book for information on test conditions.

### STYLE 1 PLASTIC



### STANDARD THRU HOLE

### SCHEMATIC



- PIN 1. LED ANODE  
2. LED CATHODE  
3. N.C.  
4. EMITTER  
5. COLLECTOR  
6. BASE

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## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)<sup>(1)</sup>

Characteristic	Symbol	Min	Typ <sup>(1)</sup>	Max	Unit	
<b>INPUT LED</b>						
Forward Voltage ( $I_F = 10\text{ mA}$ )	$V_F$	$T_A = 25^\circ\text{C}$	—	1.15	1.5	Volts
		$T_A = -55^\circ\text{C}$	—	1.3	—	
		$T_A = 100^\circ\text{C}$	—	1.05	—	
Reverse Leakage Current ( $V_R = 3\text{ V}$ )	$I_R$	—	—	100	$\mu\text{A}$	
Capacitance ( $V = 0\text{ V}$ , $f = 1\text{ MHz}$ )	$C_J$	—	18	—	$\text{pF}$	

## OUTPUT TRANSISTOR

Collector–Emitter Dark Current ( $V_{CE} = 10\text{ V}$ , $T_A = 25^\circ\text{C}$ )	$I_{CEO}$	—	1	50	$\text{nA}$
	$I_{CEO}$	—	1	—	$\mu\text{A}$
Collector–Base Dark Current ( $V_{CB} = 10\text{ V}$ )	$I_{CBO}$	—	0.2	—	$\text{nA}$
Collector–Emitter Breakdown Voltage ( $I_C = 1\text{ mA}$ )	$V_{(BR)CEO}$	30	45	—	Volts
Collector–Base Breakdown Voltage ( $I_C = 100\ \mu\text{A}$ )	$V_{(BR)CBO}$	70	100	—	Volts
Emitter–Collector Breakdown Voltage ( $I_E = 100\ \mu\text{A}$ )	$V_{(BR)ECO}$	7	7.8	—	Volts
Collector–Emitter Capacitance ( $f = 1\text{ MHz}$ , $V_{CE} = 0$ )	$C_{CE}$	—	7	—	$\text{pF}$
Collector–Base Capacitance ( $f = 1\text{ MHz}$ , $V_{CB} = 0$ )	$C_{CB}$	—	19	—	$\text{pF}$
Emitter–Base Capacitance ( $f = 1\text{ MHz}$ , $V_{EB} = 0$ )	$C_{EB}$	—	9	—	$\text{pF}$

## COUPLED

Output Collector Current ( $I_F = 10\text{ mA}$ , $V_{CE} = 10\text{ V}$ )	$I_C$ (CTR) <sup>(2)</sup>	2 (20)	7 (70)	—	$\text{mA} (\%)$
Collector–Emitter Saturation Voltage ( $I_C = 2\text{ mA}$ , $I_F = 50\text{ mA}$ )	$V_{CE(sat)}$	—	0.15	0.5	Volts
Turn–On Time ( $I_F = 10\text{ mA}$ , $V_{CC} = 10\text{ V}$ , $R_L = 100\ \Omega$ ) <sup>(3)</sup>	$t_{on}$	—	2.8	—	$\mu\text{s}$
Turn–Off Time ( $I_F = 10\text{ mA}$ , $V_{CC} = 10\text{ V}$ , $R_L = 100\ \Omega$ ) <sup>(3)</sup>	$t_{off}$	—	4.5	—	$\mu\text{s}$
Rise Time ( $I_F = 10\text{ mA}$ , $V_{CC} = 10\text{ V}$ , $R_L = 100\ \Omega$ ) <sup>(3)</sup>	$t_r$	—	2	—	$\mu\text{s}$
Fall Time ( $I_F = 10\text{ mA}$ , $V_{CC} = 10\text{ V}$ , $R_L = 100\ \Omega$ ) <sup>(3)</sup>	$t_f$	—	2	—	$\mu\text{s}$
Isolation Voltage ( $f = 60\text{ Hz}$ , $t = 1\text{ sec}$ ) <sup>(4)</sup>	$V_{ISO}$	7500	—	—	$\text{Vac(pk)}$
Isolation Resistance ( $V = 500\text{ V}$ ) <sup>(4)</sup>	$R_{ISO}$	$10^{11}$	—	—	$\Omega$
Isolation Capacitance ( $V = 0\text{ V}$ , $f = 1\text{ MHz}$ ) <sup>(4)</sup>	$C_{ISO}$	—	0.2	—	$\text{pF}$

1. Always design to the specified minimum/maximum electrical limits (where applicable).
2. Current Transfer Ratio (CTR) =  $I_C/I_F \times 100\%$ .
3. For test circuit setup and waveforms, refer to Figure 14.
4. For this test, Pins 1 and 2 are common, and Pins 4, 5 and 6 are common.

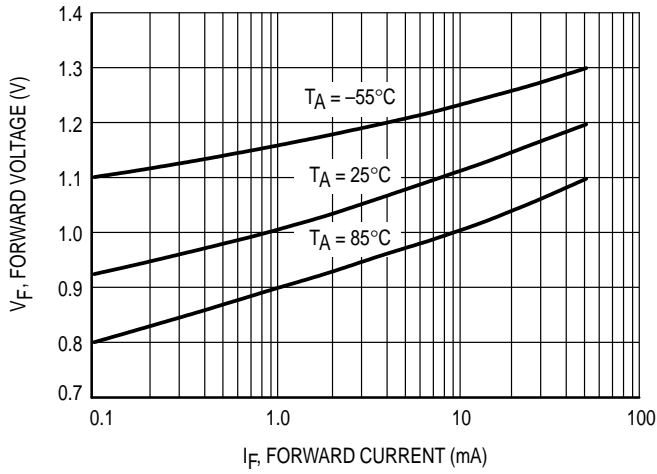


Figure 1. Forward Voltage vs. Forward Current

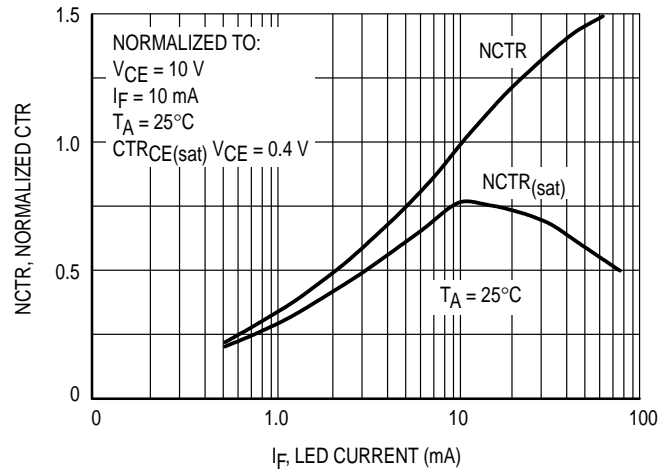


Figure 2. Normalized Non-Saturated and Saturated CTR,  $T_A = 25^\circ\text{C}$  vs. LED Current

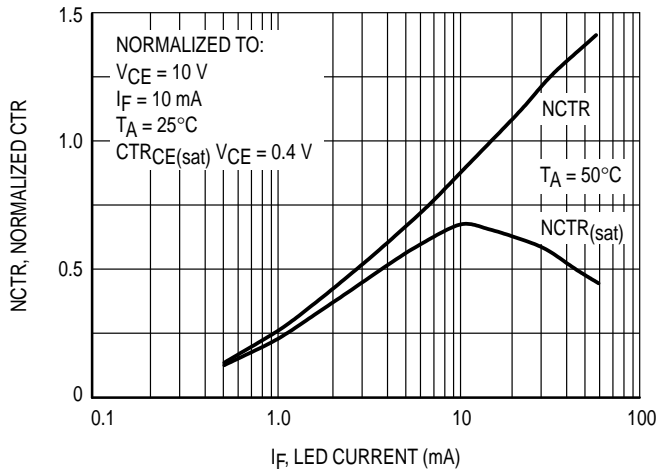


Figure 3. Normalized Non-Saturated and Saturated CTR,  $T_A = 50^\circ\text{C}$  vs. LED Current

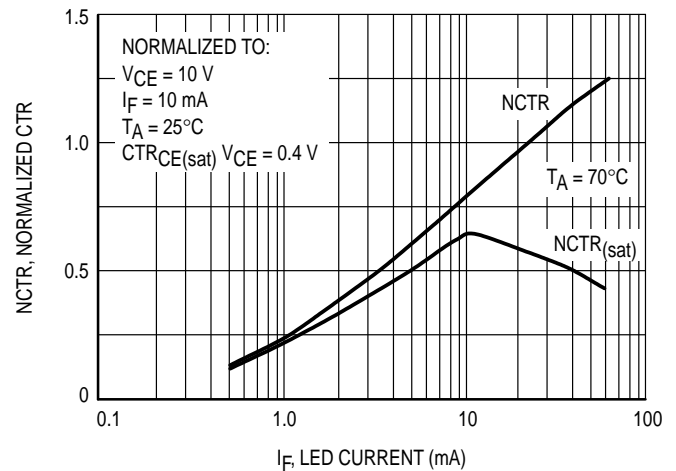


Figure 4. Normalized Non-Saturated and Saturated CTR,  $T_A = 70^\circ\text{C}$  vs. LED Current

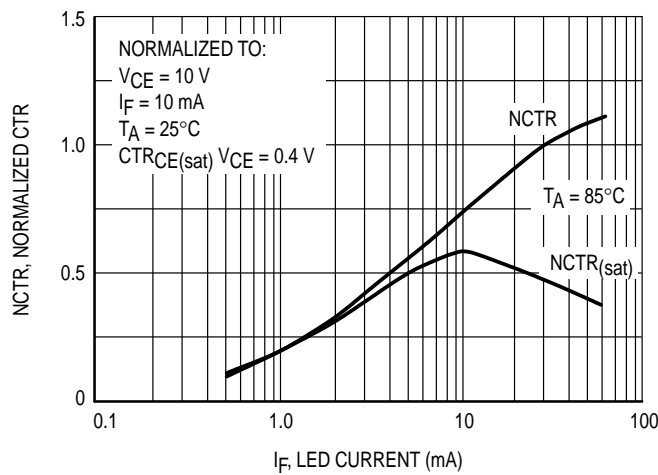
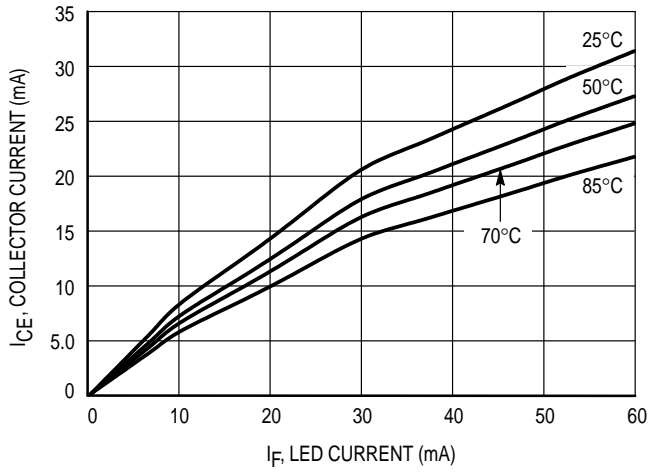
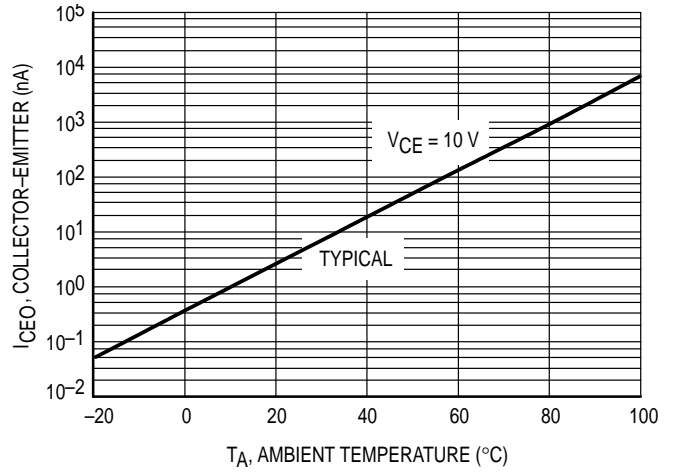


Figure 5. Normalized Non-Saturated and Saturated CTR,  $T_A = 85^\circ\text{C}$  vs. LED Current

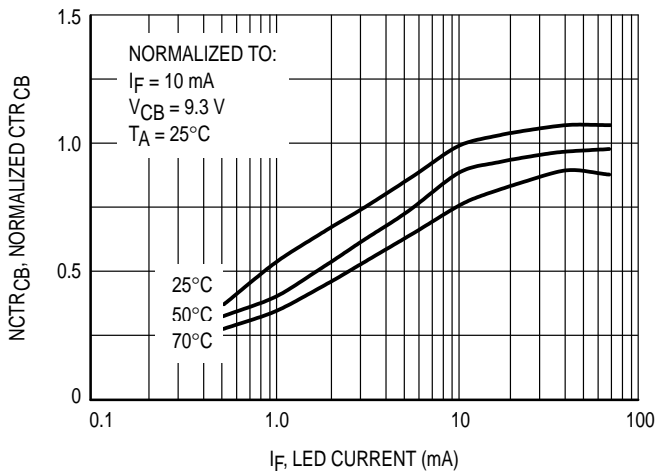
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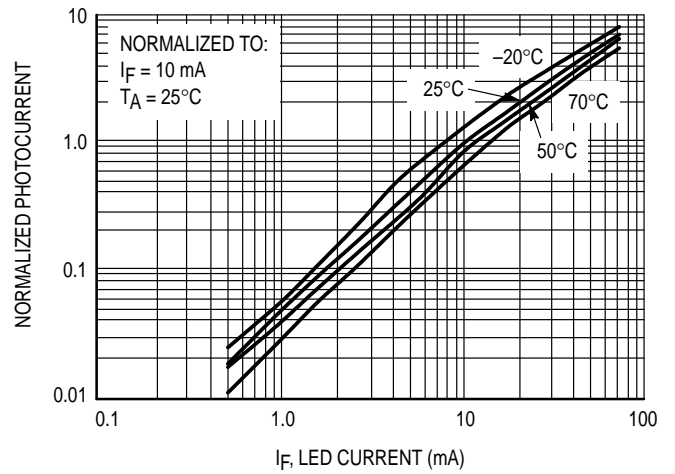
**Figure 6. Collector-Emitter Current vs. Temperature and LED Current**



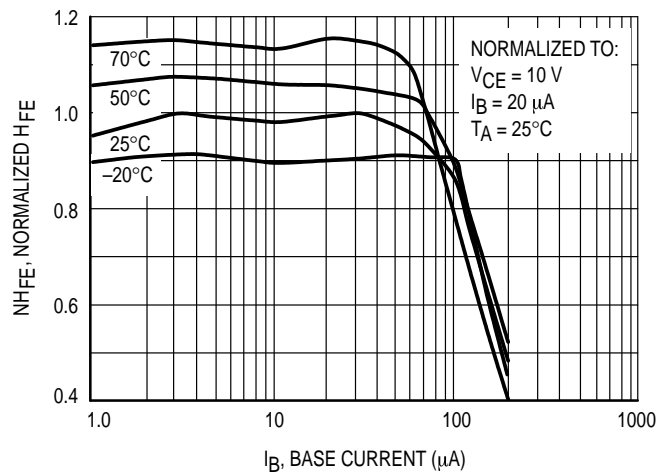
**Figure 7. Collector-Emitter Leakage Current vs. Temperature**



**Figure 8. Normalized CTRcb vs. LED Current and Temperature**



**Figure 9. Normalized Photocurrent vs.  $I_F$  and Temperature**



**Figure 10. Normalized Non-Saturated  $H_{FE}$  vs. Base Current and Temperature**

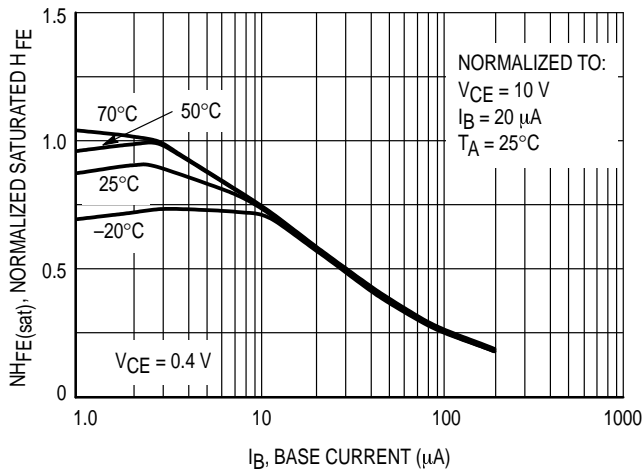


Figure 11. Normalized  $H_{FE}$  vs. Base Current and Temperature

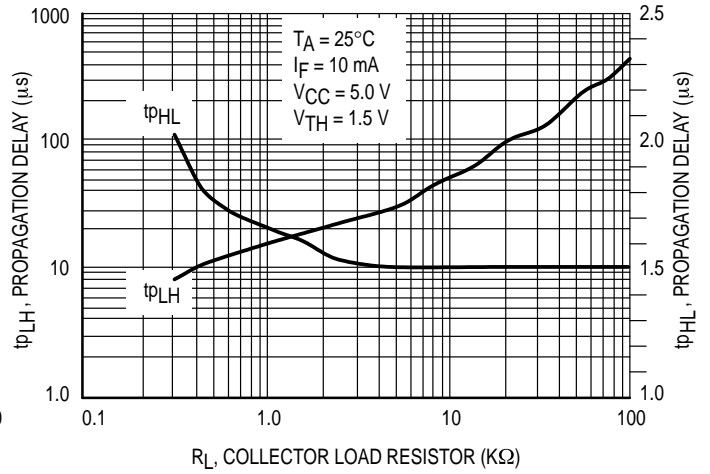


Figure 12. Propagation Delay vs. Collector Load Resistor

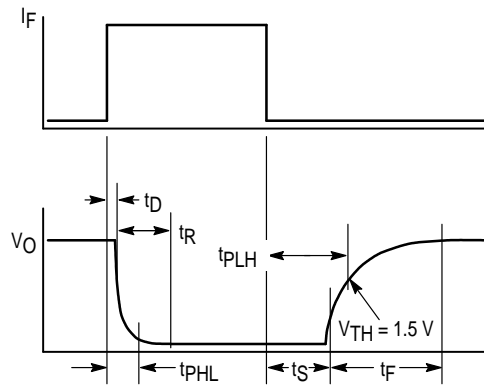


Figure 13. Switching Timing

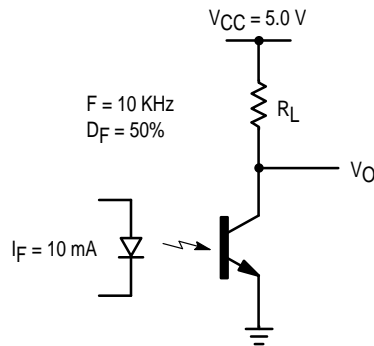
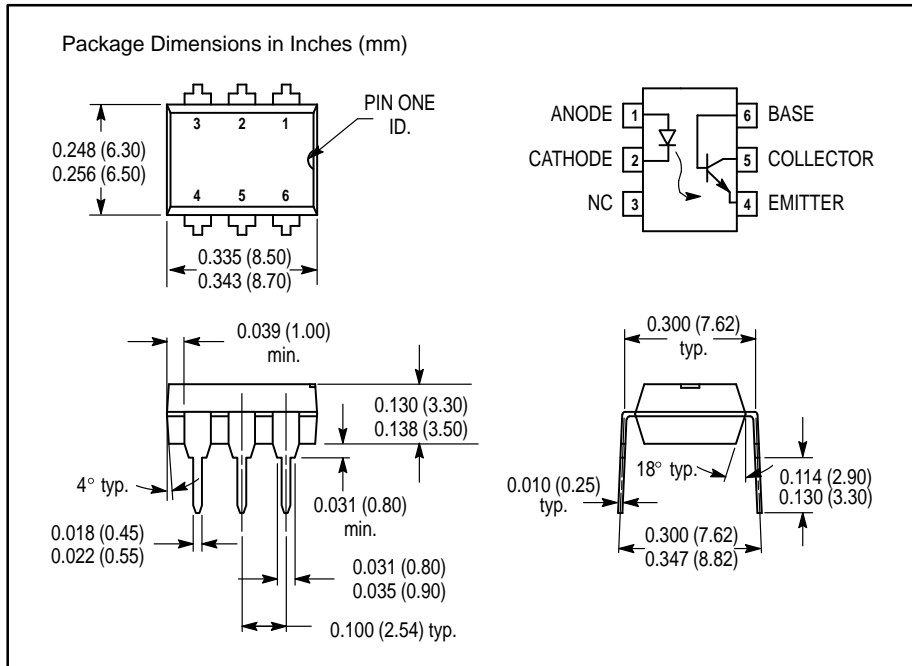



Figure 14. Switching Schematic

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