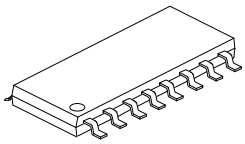
EARTH LEAKAGE CURRENT DETECTOR



SOP-16

■ DESCRIPTION

The UTC **M54133** is a semiconductor integrated circuit developed for use in high-speed earth leakage breakers incorporating functions to protect against voltage surges and inverter noise.

■ FEATURES

* Improvement of ability against unwanted tripping by lightning-surge and lightning impulse.
  Two times counting system adopted.
* Improvement of ability against unwanted tripping by inverter-noise.
  Built-in operational amplifier (of low current dissipation) for active low-pass filter.
  Improved high-frequency, high harmonic superposition performance
* Internal time delay function
* An external capacitor is used to set the delay time.
* High input sensitivity: V_T=11.5mVrms Typ.
* Low-current dissipation (at R_{IREF}=180kΩ)
  In stand-by condition: I_S=610μA Typ.
* High stabilities design
  Adopt the circuits that is not affected by fluctuations of supply voltage/ambient temperature.

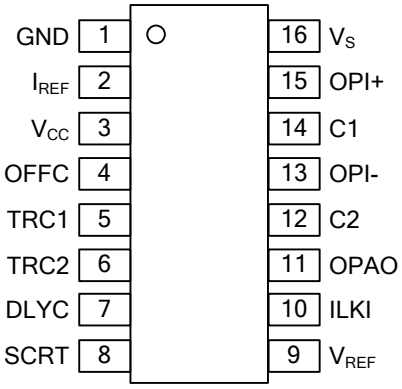
■ ORDERING INFORMATION

Ordering Number		Package	Packing
Lead Free	Halogen Free		
M54133L-S16-R	M54133G-S16-R	SOP-16	Tape Reel
M54133L-S16-T	M54133G-S16-T	SOP-16	Tube

M54133G-S16-R
(1)Packing Type
(2)Package Type
(3)Halogen Free

(1) R: Tape Reel, T: Tube
(2) S16: SOP-16
(3) G: Halogen Free, L: Lead Free

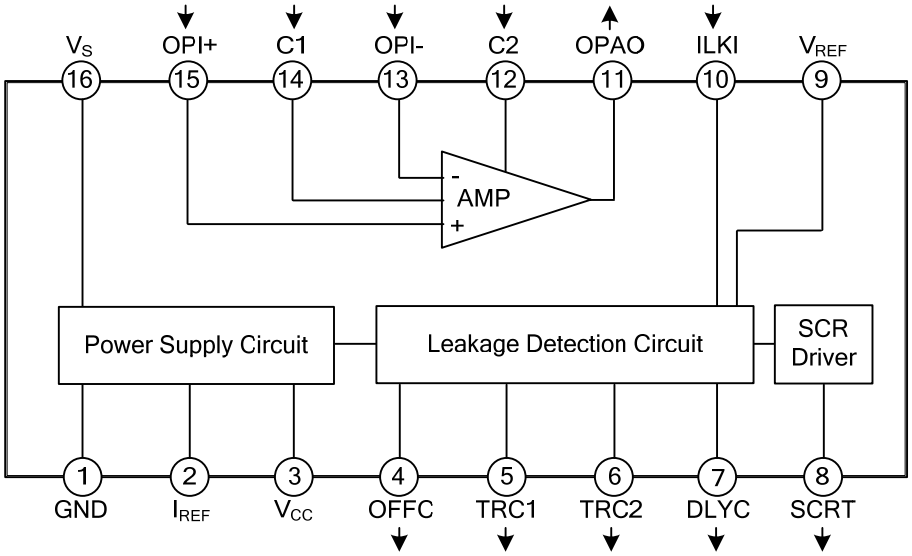
■ PIN CONFIGURATION



■ PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	GND	Grounding
2	I_{REF}	Pin for connecting resistor that sets constant current for internal circuits; approx. 1.3 V.
3	V_{CC}	Output pin of the internal constant-voltage circuit. Connect decoupling capacitor.
4	OFFC	Leakage input signal does not continue. Leakage is detected and SCR turn on. In these cases, this IC will be restored to the initial condition after a predetermined time. Connect capacitor that determines restore time.
5	TRC1	Pin for connecting capacitor that integrates signal output from discriminator of leak-signal input level.
6	TRC2	Pin for connecting capacitor to eliminate noise.
7	DLYC	Pin for connecting capacitor that sets delay time in case of using delay function.
8	SCRT	Output pin for driving a SCR.
9	V_{REF}	Pin for providing input reference level of leakage detection. About 2.4V appears.
10	ILKI	Other input pin of leakage detection.
11	OPAO	Output pin of operational amplifier.
12	C2	Pin for connecting capacitor that prevents abnormal oscillations. Connect capacitor across IC at pins 11 and 12.
13	OPI-	Negative input pins of operational amplifier
14	C1	Pin for connecting capacitor that prevents noise from causing malfunction. Connect capacitors across IC at pins 13 and 14 and across IC at pins 15 and 14.
15	OPI+	Positive input pins of operational amplifier
16	VS	Power supply

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING (T_A=25°C, unless otherwise noted)

PARAMETER		SYMBOL	RATINGS	UNIT
Maximum Supply Voltage		$V_{S(MAX)}$	15	V
Differential Input Voltage	OPI+ to OPI-	V_{ID}	-0.8~+0.8	V
Supply Current		I_S	4	mA
Differential Input Current	OPI+ to OPI-	I_{IOP}	-5~+5	mA
Input Current	V_{REF} to GND	I_{IG}	10	mA
Power Dissipation		P_D	200	mW
Operating Ambient Temperature		T_{OPR}	-20~+85	°C
Storage Temperature		T_{STG}	-55~+125	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage Range	V_S	7 ~ 12	V

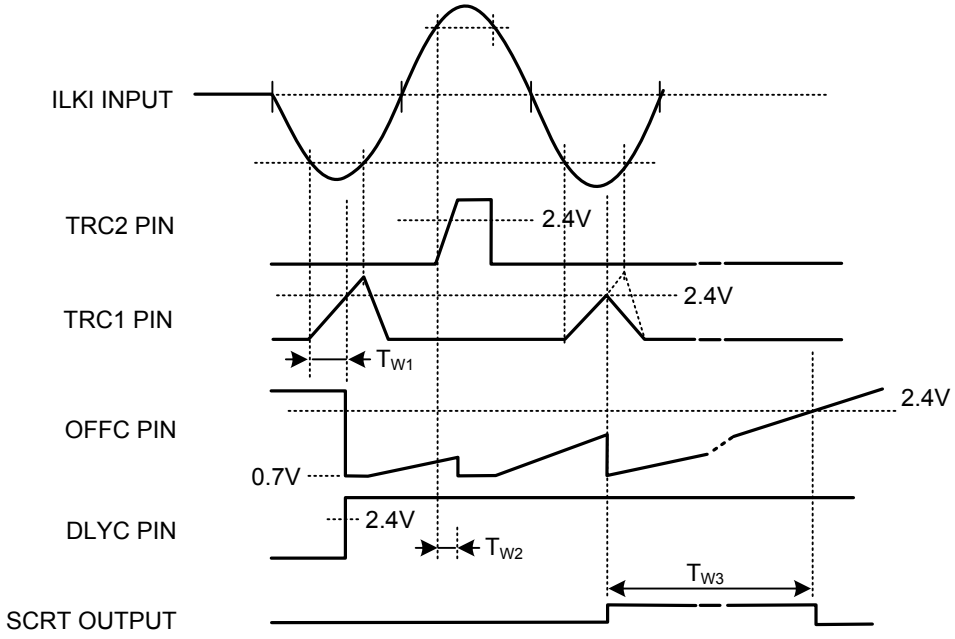
■ ELECTRICAL CHARACTERISTICS (T_A=25°C, V_S=9V, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Power Supply Circuit						
Maximum Supply Voltage	$V_{S(MAX)}$	I_S=4mA		13.9	15	V
V_{CC}-Pin Output Voltage	V_{CC}	I_{OH}=-1mA		5.2		V
Supply Current (In Standby)	I_{S0}		520	610	700	μA
Supply Current (While Detecting Leakage)	I_{S1}		560	650	740	μA
Supply Current (Immediately after Drive a SCR)	I_{S2}		480	570	660	μA
Ambient Temperature Dependence of I_{S0}		T_A=-25~+85°C		-0.2		%/°C
Operational Amplifier						
Differential Input Clamp Voltage	V_{IC}	I_{IDC}=± 4mA		±0.8		V
OPOA-Pin “H” Output Current	I_{OH}			2.8		mA
OPOA-Pin “L” Output Current	I_{OL}			0.8		mA
Input Bias Current	I_{IC}			125		nA
Voltage Gain	G_V	f=1kHz		40		dB
Frequency Band Width	B_W	-3dB		6		kHz
Maximum Output Voltage	V_O			3.5		V_{PP}
Output Offset Voltage	$V_{O(OFF)}$			0		mV
Leak Detector Circuit						
V_{REF}-Pin Output Voltage	V_O	I_{OH}=-200μA		2.4		V
V_{REF}-GND Clamp Voltage	V_{RCL}	I_{RCL}=5mA		4.7		V
DC Input Voltage of Leakage Detection	$V_{I(ON)}$	With respect to V_{REF}		±14.0		mVdc
ILKI-Pin Input Bias Current	I_{IH}	V_{IN}=V_{REF}		220		nA
3-ms Circuit						
TRC1 threshold voltage	V_{TH1}			2.4		V
Accuracy of TRC1-Pin “H” Output Current	E_{IOH1}	V_O=0V, I_{OH1}=-7.6μA	-20		+20	%
Accuracy of T_{W1} pulse width	E_{TW1}	C=0.01μf, T_{W1}=3ms	-15		+15	%
Ambient Temperature Dependence of T_{W1}		T_A=-20~+85°C		0		%/°C

■ ELECTRICAL CHARACTERISTICS (Cont..)

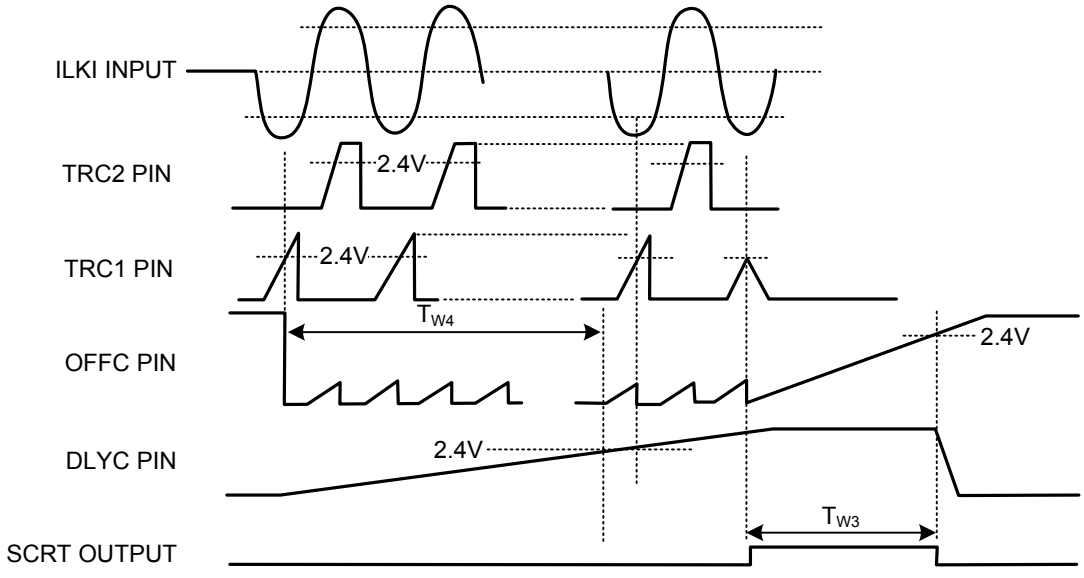
PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
1-ms Circuit							
TRC2 Threshold Voltage	V_{TH2}				2.4		V
Accuracy of TRC2-pin “H” Output Current	E_{IOH2}	V_O=0V, I_{OH2}=-7.6μA		-20		+20	%
Accuracy of TW2 Pulse Width	E_{TW2}	C=0.0047μF, T_{W2}=1.5ms		-15		+15	%
Ambient Temperature Dependence of V_T		T_A=+25°C~+85°C			-8.0		%
		T_A=+25°C~-20°C			+2.0		%
Ambient Temperature Dependence of Tw2		T_A=-20~+85°C			0		%/°C
Total AC Input Voltage of Leakage Detection	V_T	60Hz			11.5		mVrms
Reset Circuit							
OFFC Threshold Voltage	V_{TH}				2.4		V
Accuracy of OFFC-pin “H” Output Current	E_{IOH}	V_O=0V, I_{OH}=-7.6μA		-20		+20	%
Accuracy of Reset Time Pulse Width	E_{TW3}	C=0.33μF, T_{W3}=75ms		-30		+30	%
Delay Circuit							
DLYC Threshold Voltage	V_{TH}				2.4		V
Accuracy of DLYC-pin “H” Output Current	E_{IOH}	V_O=0V, I_{OH}=-7.6μA		-20		+20	%
Accuracy of Delay Timer Pulse Width	E_{TW4}	C=1.0μF, T_{W4}=300ms		-30		+30	%
SCR Driver Circuit							
SCRT-Pin “L” Output Voltage	V_{OL8}	I_{OL}=200μA			0.1	0.2	V
Supply Voltage for I_{OH} Hold	$V_{S(OFF)}$				3.0	4.0	V
SCRT-Pin “H” Output Current	I_{OHC}	V_O=8V	T_A=-20°C	-100	-160		μA
	I_{OHN}		T_A=+20°C	-50	-130		μA
	I_{OHH}		T_A=+85°C	-33	-100		μA

■ WITHOUT DELAY FUNCTION



Without Delay Function

■ USING DELAY FUNCTION



Using Delay Function

■ TYPICAL APPLICATION CIRCUIT