

M6007 Series

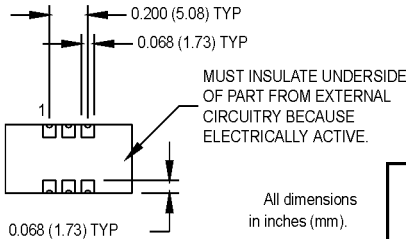
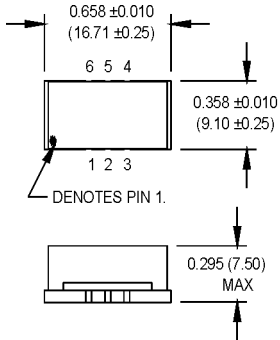
9x14 mm FR-4, 3.3 Volt, HCMOS/TTL, TCVCXO



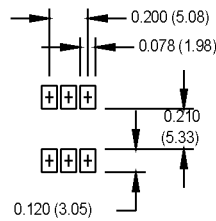
- Stratum 3 compliant stability and aging
- Ideal for WLL/DWDM/ATM, and SONET/SDH applications



Ordering Information		00.0000 MHz
Product Series	M6007	
Temperature Range	1: 0°C to +70°C 2: -40°C to +85°C 6: -20°C to +70°C 8: 0°C to +50°C	
Stability	L: ±5 ppm	
Frequency Control (Pin #1)	V: ±10 ppm	
Symmetry/Logic Compatibility	A: 40/60 CMOS/TTL B: 45/55 TTL C: 45/55 CMOS	
Package/Lead Configurations	D: DIP; Nickel Header K: FR-4, 6 Pad	
Frequency (customer specified)		



SUGGESTED SOLDER PAD LAYOUT



All dimensions in inches (mm).

Pin Connections

FUNCTION	SMT
N/C	1
Tristate	2
Ground/Case	3
Output	4
N/C	5
+Vdd	6

PARAMETER	Symbol	Min.	Typ.	Max.	Units	Condition/Notes	
Frequency Range	F	60		170	MHz		
Operating Temperature	T _A	(See Ordering Information)					
Storage Temperature	T _S	-55		+105	°C		
Frequency Stability	ΔF/F	(See Ordering Information)					See Note 1
Aging							
1st Year				1.5	ppm		
Thereafter (per year)				0.5	ppm		
Pullability/APR		(See Ordering Information)					
Control Voltage	V _c	0.5	1.5	2.5			
Tuning Range				10	ppm/V		
Modulation Bandwidth	f _m	10			kHz		
Input Impedance	Z _{in}	50k			Ohms		
Input Voltage	V _{dd}	3.15	3.3	3.45	V		
Input Current	I _{dd}			25	mA		
Output Type						CMOS/TTL	
Load		2 TTL or 15 pF max.					
Symmetry (Duty Cycle)		(See Ordering Information)					
Logic "1" Level	V _{oh}	2.5			V		
Logic "0" Level	V _{ol}			0.5	V		
Rise/Fall Time	T _r /T _f			10	ns		
Tristate Function		Input Logic "1": output active Input Logic "0": output disables					Opposite tristate logic available upon request.
Start up Time		10			ms		
Phase Noise (Typical) @155.52 MHz		100 Hz	1 kHz	10 kHz	100 kHz	Offset from carrier dBc/Hz	
		-60	-90	-110	-120		

1. Stability is inclusive of five year aging at 25°C.

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