on all channels.

match resistor.



MAXIM Three-Channel, Standard-Definition Video Filters

General Description

The MAX11501/MAX11502 integrated filters offer three

channels of 5th-order filters for standard-definition video and include +6dB output buffers on each chan-

nel. These video filters are ideal for anti-aliasing and

DAC smoothing in applications such as set-top boxes,

security systems, digital video recorders (DVRs), DVD

The MAX11501/MAX11502 video inputs feature a trans-

parent clamp compatible with AC- and DC-coupled

input signals and allow DAC outputs to be directly cou-

pled. The 5th-order filters provide a typical -3dB band-

width of 8.6MHz (MAX11501) and 8.9MHz (MAX11502) and offer either a flat passband response (MAX11501)

or a +0.8dB peaking passband response (MAX11502)

Each channel includes an output buffer with a gain of +6dB capable of driving a full 2VP-P video signal into

two standard 150Ω (75 Ω back-terminated) video loads. The buffers drive either AC- or DC-coupled loads and assure a blanking level of below 1V after the back-

The MAX11501/MAX11502 operate from a single +5V supply and are available in the upper commercial 0°C to +85°C temperature range. These devices are avail-

players, and personal video recorders.

Features

- Three-Channel 5th-Order 9MHz Filter for Standard-Definition Video
- +6dB Output Buffers
- Transparent Input Clamp
- AC- or DC-Coupled Inputs
- ♦ AC- or DC-Coupled Outputs
- ♦ Flat Passband Response (MAX11501)
- +0.8dB Peaking Passband Response (MAX11502) on All Channels
- ♦ 12kV HBM ESD Protection on Outputs
- Single +5V Power Supply
- Small 8-Pin SO Package

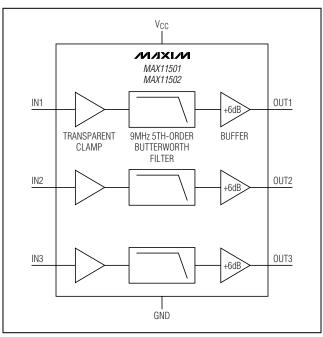
Ordering Information

PART	PIN- PACKAGE	FREQUENCY RESPONSE		
MAX11501USA+	8 SO	Flat		
MAX11502USA+	8 SO	HF Boost		

+Denotes a lead-free package.

Note: All devices are specified over the 0°C to +85°C operating temperature range.

Block Diagram



Typical Operating Circuit and Pin Configuration appear at end of data sheet.

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For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

Digital Video Recorders (DVRs) Security Video Systems

Applications

SDTV

DVD Players

able in small 8-pin SO packages.

Set-Top Box Receivers

- Personal Video Recorders
- Video On-Demand

ABSOLUTE MAXIMUM RATINGS

 Operating Temperature Range

MAX1150_USA+	0°C to +85°C
Storage Temperature Range	65°C to +150°C
Lead temperature (soldering, 10s)	+300°C
Junction Temperature	+150°C

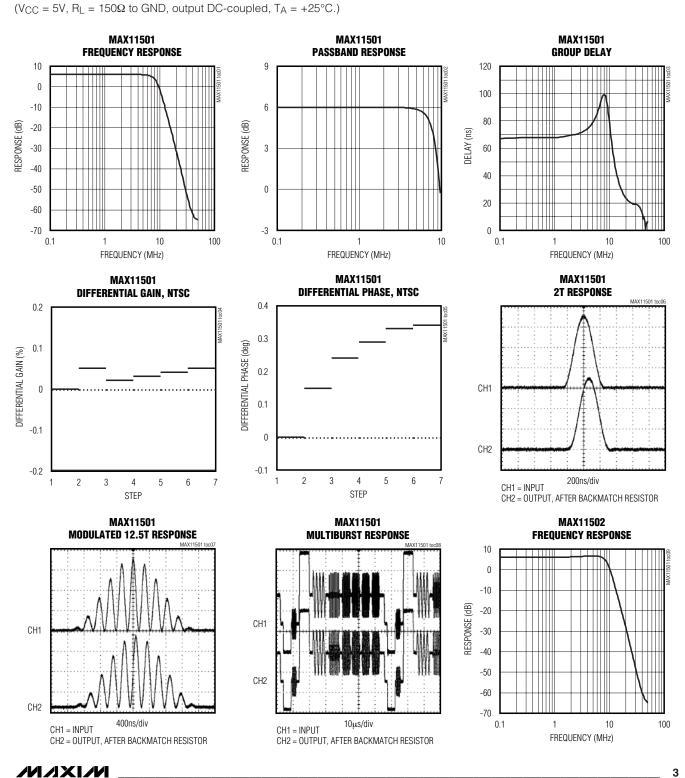
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

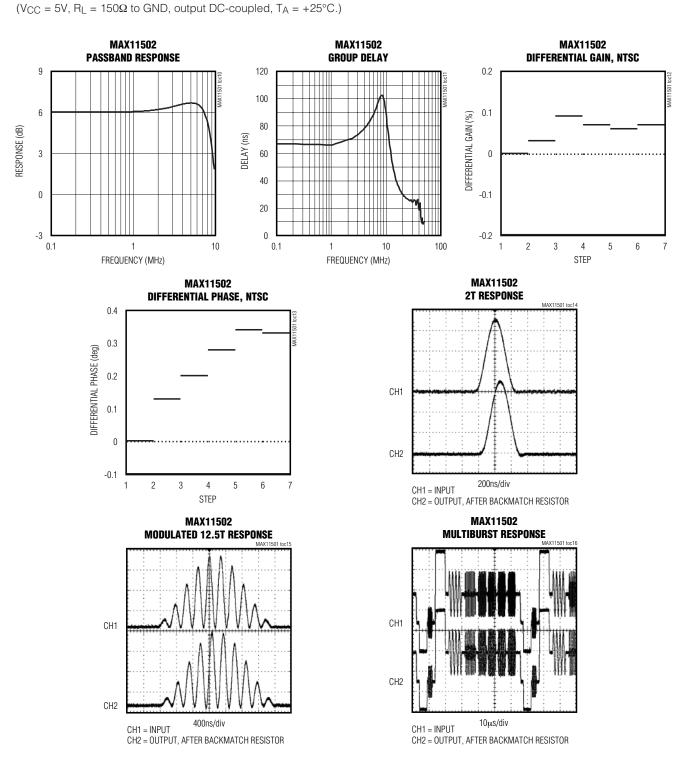
 $(V_{CC} = +5V, R_{LOAD} = 150\Omega \text{ to GND}, C_{IN} = 0.1\mu\text{F}, T_A = 0^{\circ}\text{C}$ to +85°C, frequency response is relative to 100kHz, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
-1dB Bandwidth	f _{1dB}	MAX11501	4.5	7.2		MHz
		MAX11502	5	7.8		
-3dB Bandwidth	f _{3dB}	MAX11501		8.6		MHz
		MAX11502		8.9		
Stopband Attenuation	A	MAX11501, f = 27MHz		50		dB
	A _{SB}	MAX11502, f = 27MHz		48		
Low-Frequency Gain	Av		5.8	6.0	6.2	dB
Low-Frequency Gain Match	AV(MATCH)			0.02		dB
Input Voltage Range	VIN	Referenced to GND if DC-coupled		1.4		V
Differential Gain	dG	All channels		0.1		%
Differential Phase	dø	All channels		0.3		Degrees
Total Harmonic Distortion	THD	$V_{OUT} = 1.8V_{P-P}$, f = 1MHz (all channels)		0.1		%
Channel-to-Channel Crosstalk	X _{TALK}	f = 1MHz		-80		dB
Signal-to-Noise Ratio	SNR	NTC-7 weighting, 100kHz, 4.2MHz		80		dB
Propagation Delay	tpD	f = 4.5MHz		76		ns
Supply Voltage Range	V _{DD}		4.75	5	5.25	V
Supply Current	ICC	No load		18	26	mA
Power-Supply Rejection Ratio	PSRR	DC (all channels)		60		dB





MAX11501/MAX11502



Typical Operating Characteristics (continued)

MAX11501/MAX11502

Pin Description

PIN	NAME	FUNCTION	
1	IN1	Video Input Channel 1	
2	IN2	Video Input Channel 2	
3	IN3	Video Input Channel 3	
4	V _{CC}	Power Supply	
5	GND	Ground	
6	OUT3	Video Output Channel 3	
7	OUT2	Video Output Channel 2	
8	OUT1	Video Output Channel 1	

Detailed Description

Each channel of the MAX11501/MAX11502 contains a transparent input clamp, an 8.6MHz (MAX11501) or 8.9MHz (MAX11502), 5th-order lowpass filter and an

output amplifier with a gain of two (see the *Typical Operating Circuit*). The MAX11501 provides a flat passband response and the MAX11502 features a +0.8dB high-frequency boost at 5MHz on all channels to help with system roll-off. Within the passband, each channel amplifies the signal by two and adds 280mV of offset.

$V_{OUT} = (2 \times V_{IN}) + 0.28V$

Typical voltage levels are shown in Figures 1 and 2.

Inputs

Transparent Clamps

All inputs feature transparent clamps to allow either AC or DC input coupling. The clamp remains inactive while the input signal is above ground, offering true DC input coupling. If the signal goes below ground, as when the signal is AC-coupled, the internal clamp sets the sync tip at slightly below the ground level.

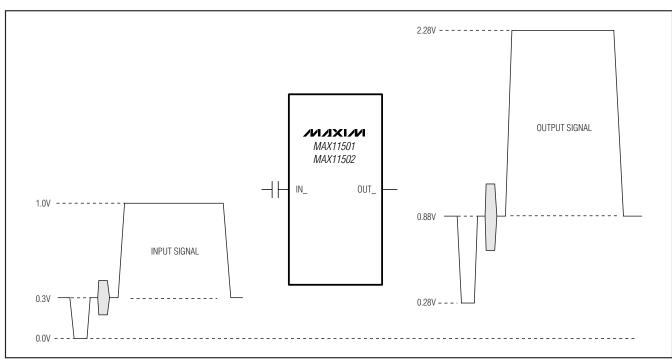


Figure 1. Typical AC-Coupled Signal

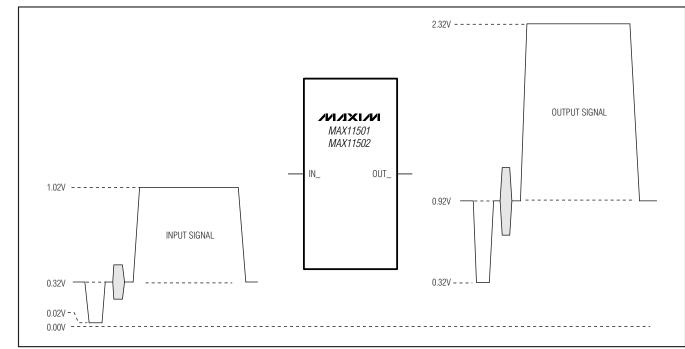


Figure 2. Typical DC-Coupled Signal

Input Coupling

The choice of AC- or DC-coupling the input depends on the video source. Many DACs provide a current output and are terminated to ground with a resistor. Such DAC outputs are conveniently DC-coupled to the MAX11501/MAX11502. Use AC-coupling when the DC level of the video signal is unknown or outside the specified input range of the MAX11501/MAX11502, such as SCART or V_{CC} terminated DAC outputs.

DC-Coupled Inputs

If the input is DC-coupled, the input voltage must remain above zero but not exceed the maximum input voltage of 1.4V (typical).

AC-Coupled Inputs

If the input is AC-coupled, the transparent clamps are active and set the lowest point of the signal at ground. This is appropriate for unipolar signals such as Y, R, G, or B, with or without sync pulses (Figure 3).

For bipolar signals such as Pb and Pr, bias the AC-coupled inputs to a fixed DC voltage, typically 0.59V, to ensure that the transparent clamp remains off. A suitable network is shown in Figure 4. Determine the bias voltage using:

$$V_{\rm B} = \frac{{\rm R2}}{{\rm R1} + {\rm R2}} \times \left(V_{\rm CC} - \left(I_{\rm L} \times {\rm R1}\right)\right),$$

where I_L is the input leakage current (typically 0.5μ A).

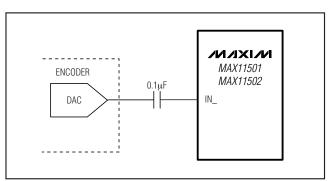


Figure 3. Simple AC-Coupling for Unipolar Signals (Y, R, G, B)

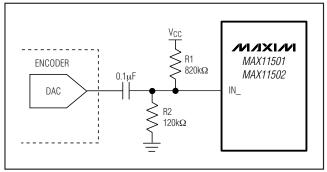


Figure 4. AC-Coupling for Bipolar Signals (Pb, Pr)



MAX11501/MAX11502

Standard-Definition Filters

The MAX11501/MAX11502 filters are optimized to deliver a flat (MAX11501) or high-frequency boosted (MAX11502) passband and high stopband attenuation. The filter characteristics have been chosen to provide excellent time domain response with low overshoot. The typical -3dB frequency of 8.6MHz (MAX11501) and 8.9MHz (MAX11502) guarantee minimal attenuation in the passband while at the same time offering a 27MHz attenuation of typically -50dB (MAX11501) and -48dB (MAX11502).

Output Buffer

The MAX11501/MAX11502 feature output buffers with +6dB of gain. A typical load (Figure 5(a)) is a 75 Ω backmatch resistor, an optional 220µF or larger AC-coupling capacitor, a transmission line, and a 75 Ω termination resistor. The MAX11501/MAX11502 clamp the signal, forcing the blanking level to less than 1V at the termination resistor. This allows direct drive of video loads at digital TV specifications without the need for costly AC-coupling capacitors. The MAX11501/MAX11502 drive two parallel loads per output (Figure 5(b)), but thermal considerations must be taken into account when doing so (see the *Junction-Temperature Calculations* section).

Applications Information

Output Configuration

The MAX11501/MAX11502 outputs may be either DC- or AC-coupled. When the outputs are AC-coupled, choose a capacitor that passes the lowest frequency content of the video signal, and keeps the line-time distortion within desired limits. The capacitor value is a function of the input leakage and impedance of the circuit being driven. The MAX11501/MAX11502 easily drive the industry common 220 μ F, or larger, coupling capacitor. If any or all outputs are driving two parallel loads, see the *Junction-Temperature Calculations* section.

The MAX11501/MAX11502 outputs are fully protected against short circuits to ground. The short-circuit protection circuitry limits the output current to 80mA (typical) per output. Shorting more than one output to ground simultaneously may exceed the maximum package power dissipation.

Junction-Temperature Calculations

Die temperature is a function of quiescent power dissipation and the power dissipation in the output drivers. Calculate the power dissipated, P_D , using:

$$P_D = P_{DS} + P_{DO1} + P_{DO2} + P_{DO3}$$

where $\mathsf{P}_{\mathsf{D}\mathsf{S}}$ is the quiescent power dissipated in the die, and given by:

and where $\mathsf{P}_{\mathsf{DOn}}$ is the power dissipated in the nth driver stage and given by:

$$P_{DOn} = \frac{(V_{CC} - V_{ORMSn}) \times V_{ORMSn}}{R_{Ln}}$$

where $V_{\mbox{ORMSn}}$ is the RMS output voltage and $R_{\mbox{Ln}}$ is the load resistance.

The following is an example of a junction-temperature calculation, assuming the following conditions:

- 1) Video standard = 525/60/2:1.
- 2) Video format = RGB with syncs on all.
- 3) Picture content = 100% white.
- 4) The input signal is AC-coupled.
- 5) The output signal is DC-coupled.
- 6) $V_{CC} = 5.0V.$
- 7) ICC = 26mA.

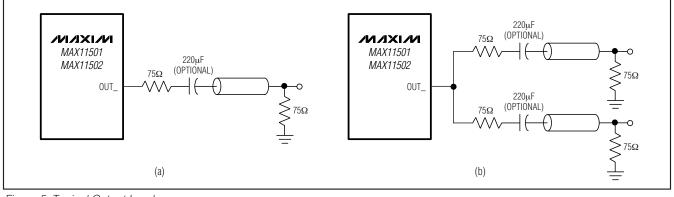


Figure 5. Typical Output Loads

A sync tip exists at 280mV and peak white exists at 2.28V. The RMS voltage is approximately 1.88V on each output (80% of the peak-to-peak voltage, plus the offset) giving:

$$P_{DS} = 5 \times 0.026 = 0.13W$$

$$P_{\text{DOn}} = \frac{(5 - 1.88) \times 1.88}{75} = 0.078W$$

and

$$P_D = 0.13 + 0.078 + 0.078 + 0.078 = 0.364W$$

The junction temperature is given by:

$$T_J = T_A + (R_{\theta JA} \times P_D)$$

where T_J is junction temperature, T_A is ambient temperature (assume +70°C), and $R_{\theta JA}$ is thermal resistance junction to ambient.

From the Absolute Maximum Ratings section of the data sheet, the derating factor is $5.9 \text{mW/}^{\circ}\text{C}$ above $+70^{\circ}\text{C}$. R_{0JA} = 1/(derating factor) = 1/($5.9 \text{mW/}^{\circ}\text{C}$) = 170°C/W (derating and maximum power dissipation are based on minimum PCB copper and indicate worst case). Therefore:

$T_J = 70 + (170 \times 0.364) = +132^{\circ}C$

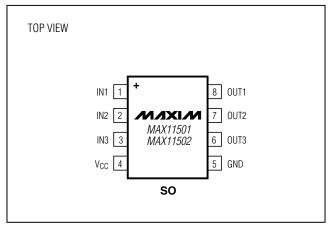
In this example, the die temperature is below the absolute maximum allowed temperature. It is unlikely under normal circumstances that the maximum die temperature will be reached, however it is possible if tolerances of V_{CC}, R_L, input voltage etc. are considered and the ambient temperature is high.

Changing the above example to a single video load on each output results in:

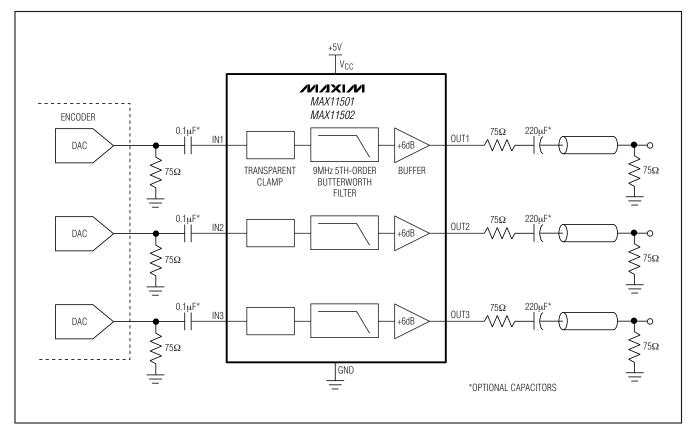
PCB Layout Recommendations

To help with heat dissipation, connect the power and ground traces to large copper areas. Bypass V_{CC} to GND with 0.1µF and 1.0µF capacitors. Surface-mount capacitors are recommended for their low inductance. Place traces carrying video signals appropriately to avoid mutual coupling. When AC-coupling the inputs, place the capacitors as close as possible to the device and keep traces short to minimize parasitic capacitance and inductance. For a recommended PCB layout, refer to the MAX11501/MAX11502 evaluation kit datasheet.

Pin Configuration



Typical Operating Circuit



Package Information

For the latest package outline information, go to www.maxim-ic.com/packages.				
PACKAGE TYPE	PACKAGE CODE DOCUMENT NO.			
8 SO	S8+2	<u>21-0041</u>		

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	12/07	Initial release	—
1	4/08	Updated Block Diagram	1

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