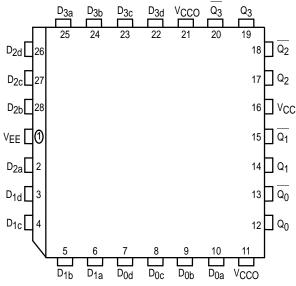
# **Quad 4-Input OR/NOR Gate**

The MC10E/100E101 is a quad 4-input OR/NOR gate.

- 500ps Max. Propagation Delay
- Extended 100E VEE Range of 4.2V to 5.46V
- 75kΩ Input Pulldown Resistors

## Pinout: 28-Lead PLCC (Top View)



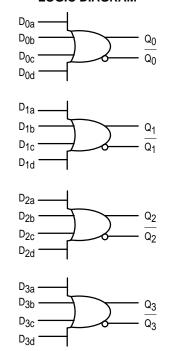
 $^{\star}$  All VCC and VCCO pins are tied together on the die.

# MC10E101 MC100E101

# QUAD 4-INPUT OR/NOR GATE



## LOGIC DIAGRAM



#### **PIN NAMES**

Pin	Function							
D <sub>0a</sub> – D <sub>3d</sub>	Data Inputs							
$Q_0 - Q_3$	True Outputs							
$\overline{Q_0} - \overline{Q_3}$	Inverting Outputs							

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## **DC CHARACTERISTICS** ( $V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$ ; $V_{CC} = V_{CCO} = GND$ )

		0°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
lН	Input HIGH Current			150			150			150	μΑ	
IEE	Power Supply Current										mA	
	10E		30	36		30	36		30	36		
	100E		30	36		30	36		35	42		

## **AC CHARACTERISTICS** ( $V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$ ; $V_{CC} = V_{CCO} = GND$ )

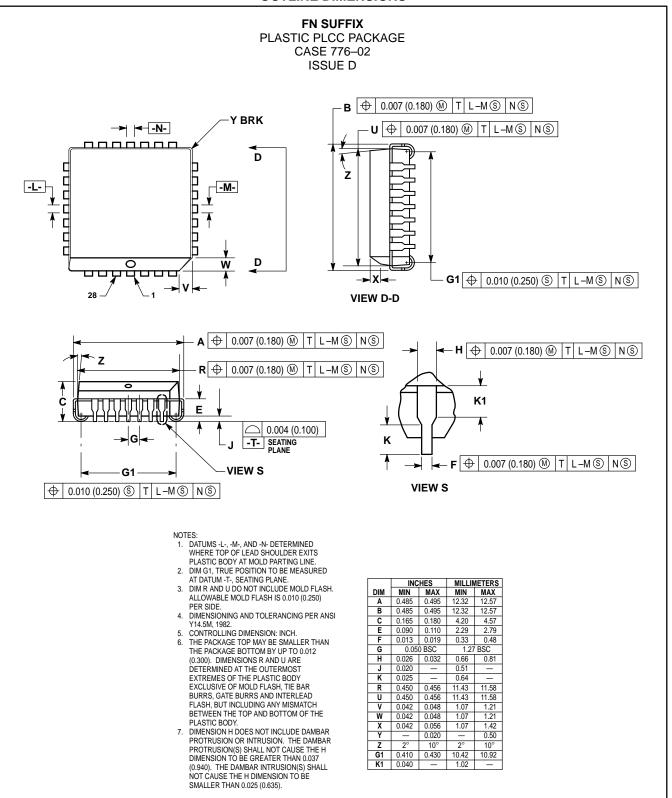
		0°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
<sup>t</sup> PLH <sup>t</sup> PHL	Propagation Delay to Output D to Q	200	350	500	200	350	500	200	350	500	ps	
<sup>t</sup> SKEW <sup>t</sup> SKEW	Within-Device Skew Within-Gate Skew		50 25			50 25			50 25		ps	1 2
t <sub>r</sub>	Rise/Fall Time 20 - 80%	300	380	575	300	380	575	300	380	575	ps	

<sup>1.</sup> Within-device skew is defined as identical transitions on similar paths through a device.

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<sup>2.</sup> Within-gate skew is defined as the variation in propagation delays of a gate when driven from its different inputs.

## **OUTLINE DIMENSIONS**



## MC10E101 MC100E101

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