



*Product Preview*

# Single Chip Voice Memory System

## Low power Silicon-Gate CMOS

The voice memory system is a SMOS integrated circuit specially designed for voice recording/reproduction application.  
e.g. Telephone Answering Machine.

This chip combines the MEMORY ADDRESS CONTROLLER and the ADAPTIVE DELTA MODULATOR which provides the A/D and D/A function. Together with the MIC amplifier and the demodulation sw. cap low pass filter, this chip provides a total solution to solid state voice recording and playback system design.

Features:

- + 5 volts power supply.
- 42 pins shrink DIP package.
- 4 different memory configurations:
  1. One 256K/1M DRAM
  2. Two 256K/1M DRAM
  3. Four 256K/1M DRAM
  4. Extended Memory
- Combination of Start, Reset, and Pause Control meet every need of voice recording application
- DRAM refreshing circuit provided
- 16K, or 32K bps clock rate can be selected, or variable clock rate
- Use low cost 3.58 MHz crystal or ceramic resonator
- Serial MCU interface provided
- Current address can be programmed by MCU
- Memory Compression/Voice Activated Recording feature is provided.

**PIN OUT DIAGRAM**

