

Dual EIA-423/EIA-232D Line Driver

The MC3488A dual is single–ended line driver has been designed to satisfy the requirements of EIA standards EIA–423 and EIA–232D, as well as CCITT X.26, X.28 and Federal Standard FIDS1030. It is suitable for use where signal wave shaping is desired and the output load resistance is greater than 450 ohms. Output slew rates are adjustable from 1.0 μs to 100 μs by a single external resistor. Output level and slew rate are insensitive to power supply variations. Input undershoot diodes limit transients below ground and output current limiting is provided in both output states.

The MC3488A has a standard 1.5 V input logic threshold for TTL or NMOS compatibility.

- PNP Buffered Inputs to Minimize Input Loading
- Short Circuit Protection
- Adjustable Slew Rate Limiting
- MC3488A Equivalent to 9636A
- Output Levels and Slew Rates are Insensitive to Power Supply Voltages
- No External Blocking Diode Required for V_{EE} Supply
- Second Source μA9636A

MC3488A

DUAL EIA–423/EIA–232D DRIVER

SEMICONDUCTOR TECHNICAL DATA

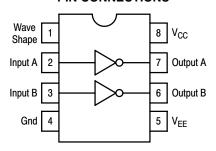


P1 SUFFIX PLASTIC PACKAGE CASE 626

D SUFFIXPLASTIC PACKAGE
CASE 751
(SO-8)



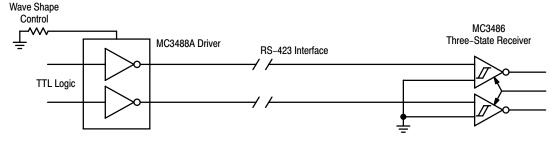
PIN CONNECTIONS



ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC3488AP1	$T_A = 0 \text{ to } +70^{\circ}\text{C}$	Plastic DIP
MC3488AD	1A = 0 10 +70 C	SO-8

Simplified Application



MAXIMUM RATINGS (Note 1)

Rating	Symbol	Value	Unit
Power Supply Voltages	V _{CC} V _{EE}	+ 15 – 15	V
Output Current Source Sink	I _{O+}	+ 150 - 150	mA
Operating Ambient Temperature	T _A	0 to + 70	°C
Junction Temperature Range	TJ	150	°C
Storage Temperature Range	T _{stg}	- 65 to + 150	°C

RECOMMENDED OPERATING CONDITIONS

Characteristic	Symbol	Min	Тур	Max	Unit
Power Supply Voltages	V _{CC} V _{EE}	10.8 - 13.2	12 - 12	13.2 - 10.8	V
Operating Temperature Range	T _A	0	25	70	°C
Wave Shaping Resistor	R _{WS}	10	-	1000	kΩ

TARGET ELECTRICAL CHARACTERISTICS (Unless otherwise noted, specifications apply over recommended operating conditions)

Characteristic	Symbol	Min	Тур	Max	Unit
Input Voltage – Low Logic State	V _{IL}	-	-	0.8	V
Input Voltage – High Logic State	V _{IH}	2.0	-	-	V
Input Current – Low Logic State (V _{IL} = 0.4 V)	I _{IL}	- 80	_	_	μΑ
Input Current – High Logic State (V _{IH} = 2.4 V) (V _{IH} = 5.5 V)	I _{IH1}	- -	- -	10 100	μА
Input Clamp Diode Voltage (I _{IK} = – 15 mA)	V _{IK}	- 1.5	_	_	V
Output Voltage – Low Logic State $ \begin{array}{ll} (R_L = \infty) & EIA423 \\ (R_L = 3.0 \ k\Omega) & EIA232D \\ (R_L = 450 \ \Omega) & EIA423 \end{array} $	V _{OL}	- 6.0 - 6.0 - 6.0	_ _ _	- 5.0 - 5.0 - 4.0	V
Output Voltage – High Logic State $ \begin{array}{ll} (R_L = \infty) & EIA423 \\ (R_L = 3.0 \ k\Omega) & EIA232D \\ (R_L = 450 \ \Omega) & EIA423 \end{array} $	V _{OH}	5.0 5.0 4.0	- - -	6.0 6.0 6.0	V
Output Resistance $(R_L \ge 450 \Omega)$	R _O	-	25	50	Ω
Output Short–Circuit Current (Note 2) $ (V_{in} = V_{out} = 0 \text{ V}) $ $ (V_{in} = V_{IH(Min)}, V_{out} = 0 \text{ V}) $	losh lost	– 150 + 15	_ _ _	- 15 + 150	mA
Output Leakage Current (Note 3) $ (V_{CC} = V_{EE} = 0 \text{ V}, -6.0 \text{ V} \leq V_0 \leq 6.0 \text{ V}) $	l _{ox}	- 100	_	100	μΑ
Power Supply Currents $(R_W = 100 \text{ k}\Omega, R_L = \infty, V_{IL} \leq V_{in} \leq V_{IH})$	I _{CC}	- - 18	_ _	+ 18 -	mA

NOTES: 1. Devices should not be operated at these values. The "Electrical Characteristics" provide conditions for actual device operation.
2. One output shorted at a time.
3. No V_{EE} diode required.

TRANSITION TIMES (Unless otherwise noted, C_L = 30 pF, f = 1.0 kHz, V_{CC} = - V_{EE} = 12.0 V \pm 10%, T_A = 25°C, R_L = 450 Ω . Transition times measured 10% to 90% and 90% to 10%)

Characteristic	Symbol	Min	Тур	Max	Unit
Transition Time, Low-to-High State Output	t _{TLH}				μs
$(R_W = 10 \text{ k}\Omega)$		0.8	-	1.4	
$(R_W = 100 \text{ k}\Omega)$		8.0	_	14	
$(R_W = 500 \text{ k}\Omega)$		40	_	70	
$(R_W = 1000 \text{ k}\Omega)$		80	_	140	
Transition Time, High-to-Low State Output	t _{THL}				μs
$(R_W = 10 \text{ k}\Omega)$		0.8	_	1.4	
$(R_W = 100 \text{ k}\Omega)$		8.0	_	14	
$(R_W = 500 \text{ k}\Omega)$		40	_	70	
$(R_W = 1000 \text{ k}\Omega)$		80	_	140	

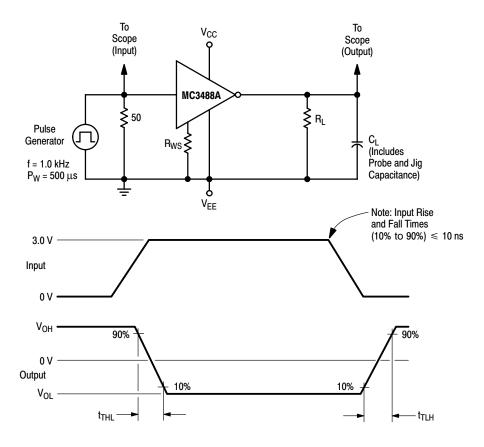


Figure 1. Test Circuit and Waveforms for Transition Times

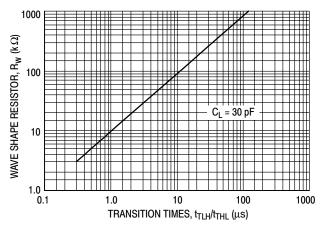


Figure 2. Output Transition Times versus Wave Shape Resistor Value

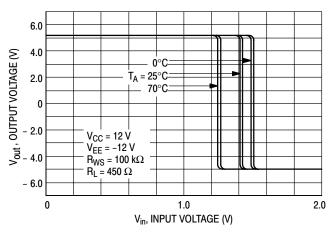
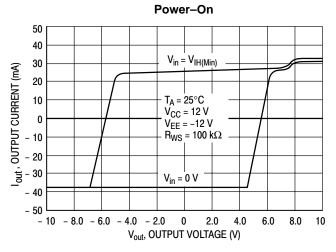


Figure 3. Input/Output Characteristics versus Temperature



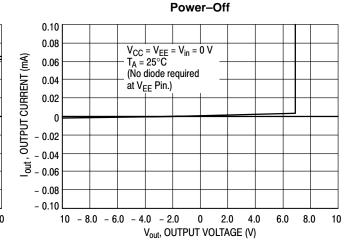


Figure 4. Output Current versus Output Voltage

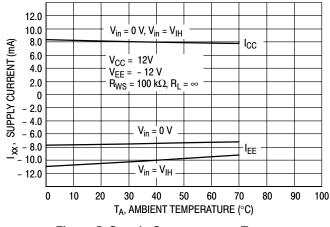


Figure 5. Supply Current versus Temperature

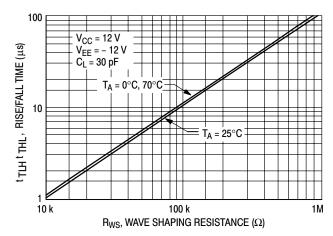
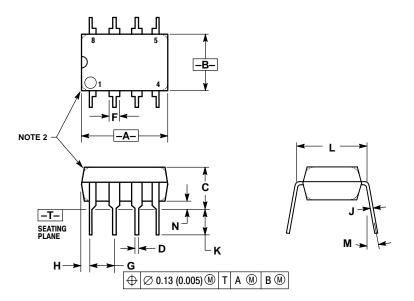


Figure 6. Rise/Fall Time versus Rws

PACKAGE DIMENSIONS

P1 SUFFIX

PLASTIC PACKAGE CASE 626-05 ISSUE L



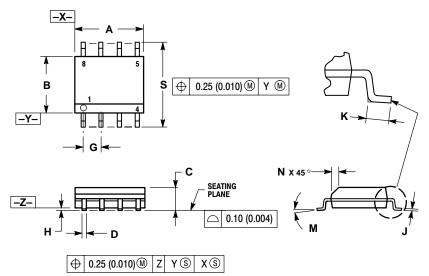
- NOTES:
 1. DIMENSION L TO CENTER OF LEAD WHEN FORMED PARALLEL.
 2. PACKAGE CONTOUR OPTIONAL (ROUND OR SQUARE CORNERS).
 3. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	9.40	10.16	0.370	0.400	
В	6.10	6.60	0.240	0.260	
С	3.94	4.45	0.155	0.175	
D	0.38	0.51	0.015	0.020	
F	1.02	1.78	0.040	0.070	
G	2.54 BSC		0.100 BSC		
Н	0.76	1.27	0.030	0.050	
_	0.20	0.30	0.008	0.012	
K	2.92	3.43	0.115	0.135	
L	7.62 BSC		0.300 BSC		
M		10°		10°	
N	0.76	1.01	0.030	0.040	

PACKAGE DIMENSIONS

D SUFFIX PLASTIC PACKAGE

CASE 751-07 **ISSUE W**



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: MILLIMETER.
 3. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
 4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE
- 4. MAXIMUM MULD FIRE TO SIDE.
 SIDE.
 5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIN	IETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	4.80	5.00	0.189	0.197
В	3.80	4.00	0.150	0.157
С	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
G	1.27 BSC		0.050 BSC	
Н	0.10	0.25	0.004	0.010
J	0.19	0.25	0.007	0.010
K	0.40	1.27	0.016	0.050
M	0 °	0 ° 8 °		8 °
N	0.25	0.50	0.010	0.020
S	5.80	6.20	0.228	0.244

Notes

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

PUBLICATION ORDERING INFORMATION

Literature Fulfillment:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada **Fax:** 303–675–2176 or 800–344–3867 Toll Free USA/Canada

Email: ONlit@hibbertco.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

JAPAN: ON Semiconductor, Japan Customer Focus Center 4–32–1 Nishi–Gotanda, Shinagawa–ku, Tokyo, Japan 141–0031

Phone: 81–3–5740–2700 Email: r14525@onsemi.com

ON Semiconductor Website: http://onsemi.com

For additional information, please contact your local

Sales Representative.