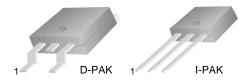


MJD2955

General Purpose Amplifier Low Speed Switching Applications D-PAK for Surface Mount Applications • Lead Formed for Surface Mount Applications (No Suffix) • Straight Lead (I-PAK, "-I " Suffix)

- Electrically Similar to Popular MJE2955T
- DC Current Gain Specified to 10A
- High Current Gain Bandwidth Product: $f_T = 2MHz (MIN), I_C = -500mA$



1.Base 2.Collector 3.Emitter

PNP Epitaxial Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

| Symbol | Parameter | Value | Units |
|------------------|--|------------|-------|
| V _{CBO} | Collector-Base Voltage | - 70 | V |
| V _{CEO} | Collector-Emitter Voltage | - 60 | V |
| V _{EBO} | Emitter-Base Voltage | - 5 | V |
| I _C | Collector Current | - 10 | Α |
| I _B | Base Current | - 6 | Α |
| P _C | Collector Dissipation (T _C =25°C) | 20 | W |
| | Collector Dissipation (T _a =25°C) | 1.75 | W |
| T _J | Junction Temperature | 150 | °C |
| T _{STG} | Storage Temperature | - 55 ~ 150 | °C |

Electrical Characteristics T_C=25°C unless otherwise noted

| Symbol | Parameter | Test Condition | Min. | Max. | Units |
|------------------------|--|---|---------|--------------|--------|
| V _{CEO} (sus) | * Collector-Emitter Sustaining Voltage | $I_C = -30 \text{mA}, I_B = 0$ | -60 | | V |
| I _{CEO} | Collector Cut-off Current | $V_{CE} = -30V, I_{E} = 0$ | | - 50 | μΑ |
| I _{CBO} | Collector Cut-off Current | $V_{CB} = -70V, I_{E} = 0$ | | - 2 | mA |
| I _{EBO} | Emitter Cut-off Current | $V_{EB} = -5V, I_{C} = 0$ | | - 0.5 | mA |
| h _{FE} | * DC Current Gain | $V_{CE} = -4V, I_{C} = -4A$ $V_{CE} = -4V, I_{C} = -10A$ | 20 5 | 100 | |
| V _{CE} (sat) | * Collector-Emitter Saturation Voltage | $I_C = -4A, I_B = -0.4A$ $I_C = -10A, I_B = -3.3A$ | | - 1.1 - 8 | V V |
| V _{BE} (on) | * Base-Emitter ON Voltage | V _{CE} = - 4V, I _C = - 4A | | -1.8 | V |
| f _T | Current Gain Bandwidth Product | V _{CE} = - 10V, I _C = - 500mA | 2 | | MHz |

^{*} Pulse Test: PW≤300ms, Duty Cycle≤2%

Typical Characteristics

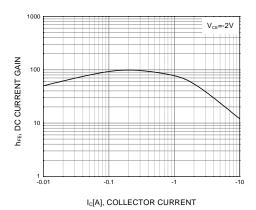


Figure 1. DC current Gain

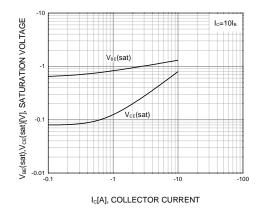


Figure 2. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

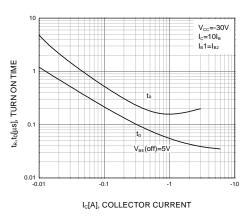


Figure 3. Turn On Time

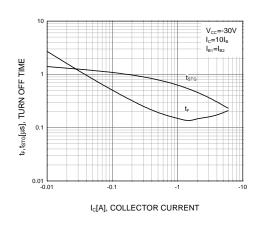


Figure 4. Turn Off Time

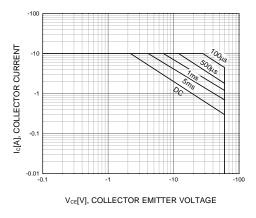


Figure 5. Safe Operating Area

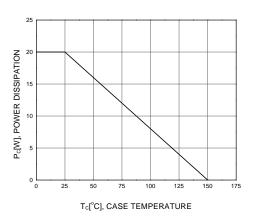
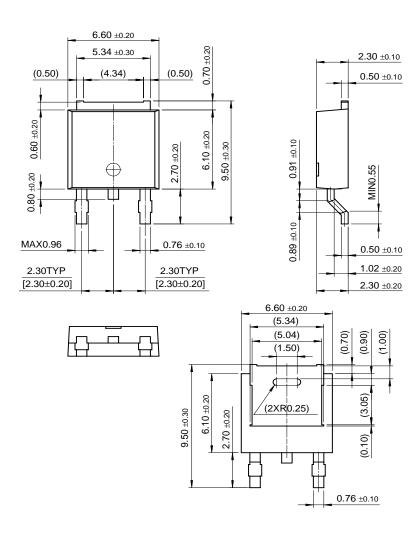


Figure 6. Power Derating

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Package Demensions

D-PAK



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