Preferred Device

General Purpose Transistor

PNP Silicon

Features

• Pb-Free Packages are Available

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	-40	Vdc
Collector - Base Voltage	V _{CBO}	-40	Vdc
Emitter – Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current – Continuous	Ι _C	-200	mAdc
Collector Current - Peak (Note 3)	I _{CM}	-800	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Total Device Dissipation FR-5 Board (Note 1) @ T _A = 25°C Derate above 25°C	P _D	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	556	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) @ T _A = 25°C Derate above 25°C	P _D	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	417	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

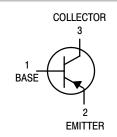
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

- 1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.
- 2. Alumina = $0.4 \times 0.3 \times 0.024$ in. 99.5% alumina.
- 3. Reference SOA curve.



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SOT-23 (TO-236) CASE 318 STYLE 6

MARKING DIAGRAM



2A = Specific Device Code

- M = Date Code*
- = Pb-Free Package

(Note: Microdot may be in either location) *Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBT3906LT1	SOT-23	3,000 / Tape & Reel
MMBT3906LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel
MMBT3906LT3	SOT-23	10,000 / Tape & Reel
MMBT3906LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel

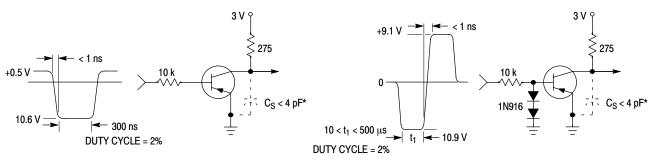
† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Charac	Symbol	Min	Max	Unit		
OFF CHARACTERISTICS						
Collector – Emitter Breakdown Voltage $(I_C = -1.0 \text{ mAdc}, I_B = 0)$	V _{(BR)CEO}	-40	-	Vdc		
Collector – Base Breakdown Voltage $(I_C = -10 \ \mu Adc, I_E = 0)$	V _{(BR)CBO}	-40	-	Vdc		
Emitter – Base Breakdown Voltage $(I_E = -10 \ \mu Adc, I_C = 0)$		V _{(BR)EBO}	-5.0	_	Vdc	
Base Cutoff Current (V _{CE} = -30 Vdc, V _{EB} = -3.0 Vdc)		I _{BL}	_	-50	nAdc	
Collector Cutoff Current (V _{CE} = -30 Vdc, V _{EB} = -3.0 Vdc)		I _{CEX}	-	-50	nAdc	
ON CHARACTERISTICS (Note 4)						
$\begin{array}{l} \text{DC Current Gain} \\ (I_{C}=-0.1 \text{ mAdc}, \text{ V}_{CE}=-1.0 \text{ Vdc}) \\ (I_{C}=-1.0 \text{ mAdc}, \text{ V}_{CE}=-1.0 \text{ Vdc}) \\ (I_{C}=-10 \text{ mAdc}, \text{ V}_{CE}=-1.0 \text{ Vdc}) \\ (I_{C}=-50 \text{ mAdc}, \text{ V}_{CE}=-1.0 \text{ Vdc}) \\ (I_{C}=-100 \text{ mAdc}, \text{ V}_{CE}=-1.0 \text{ Vdc}) \end{array}$)	H _{FE}	60 80 100 60 30	- 300 -	_	
	V _{CE(sat)}		-0.25 -0.4	Vdc		
$\begin{array}{l} \text{Base-Emitter Saturation Voltage} \\ (I_{C} = -10 \text{ mAdc}, I_{B} = -1.0 \text{ mAdc}) \\ (I_{C} = -50 \text{ mAdc}, I_{B} = -5.0 \text{ mAdc}) \end{array}$	V _{BE(sat)}	-0.65 -	-0.85 -0.95	Vdc		
SMALL-SIGNAL CHARACTERISTICS		·				
Current-Gain - Bandwidth Product (I _C = -10 mAdc, V _{CE} = -20 Vdc, f	= 100 MHz)	f _T	250	-	MHz	
Output Capacitance $(V_{CB} = -5.0 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MI})$	C _{obo}	-	4.5	pF		
Input Capacitance (V _{EB} = -0.5 Vdc, I_C = 0, f = 1.0 M	C _{ibo}	-	10	pF		
Input Impedance (I _C = -1.0 mAdc, V _{CE} = -10 Vdc, \sim	h _{ie}	2.0	12	kΩ		
Voltage Feedback Ratio ($I_C = -1.0 \text{ mAdc}$, $V_{CE} = -10 \text{ Vdc}$, $T_{CE} = -10 \text{ Vdc}$, T_{CE	h _{re}	0.1	10	X 10 ⁻⁴		
Small – Signal Current Gain ($I_C = -1.0 \text{ mAdc}$, $V_{CE} = -10 \text{ Vdc}$,	h _{fe}	100	400	-		
Output Admittance ($I_C = -1.0 \text{ mAdc}$, $V_{CE} = -10 \text{ Vdc}$,	h _{oe}	3.0	60	μmhos		
Noise Figure (I _C = -100 μ Adc, V _{CE} = -5.0 Vdc,	NF	-	4.0	dB		
SWITCHING CHARACTERISTICS						
Delay Time	(V _{CC} = -3.0 Vdc, V _{BE} = 0.5 Vdc,	t _d	-	35		
Rise Time	$I_{\rm C} = -10$ mAdc, $I_{\rm B1} = -1.0$ mAdc)	t _r	-	35	ns	
Storage Time	(V _{CC} = -3.0 Vdc, I _C = -10 mAdc,	t _s	-	225	200	
	$I_{B1} = I_{B2} = -1.0 \text{ mAdc}$			1	ns	

4. Pulse Test: Pulse Width $\,\leq\,$ 300 $\mu s,$ Duty Cycle $\,\leq\,$ 2.0%.

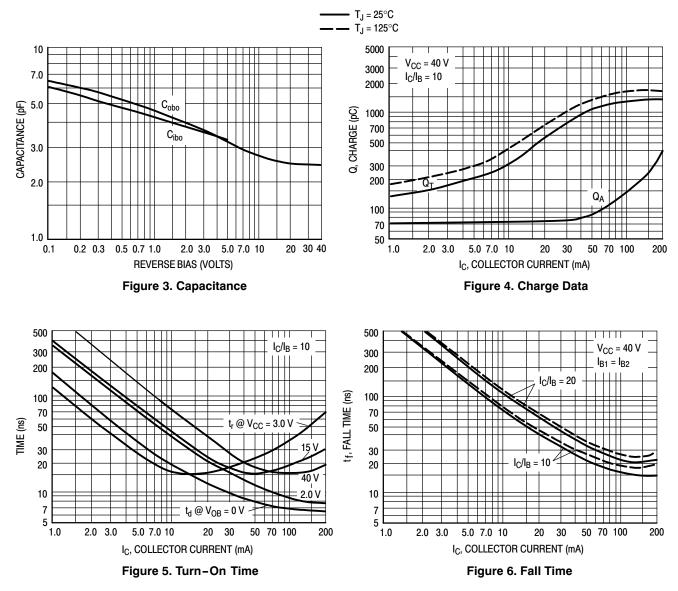


* Total shunt capacitance of test jig and connectors

Figure 1. Delay and Rise Time **Equivalent Test Circuit**

Figure 2. Storage and Fall Time **Equivalent Test Circuit**

TYPICAL TRANSIENT CHARACTERISTICS



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TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS NOISE FIGURE VARIATIONS

(V_{CE} = -5.0 Vdc, T_A = 25° C, Bandwidth = 1.0 Hz)

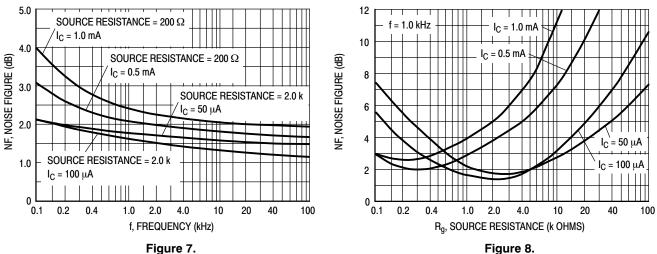
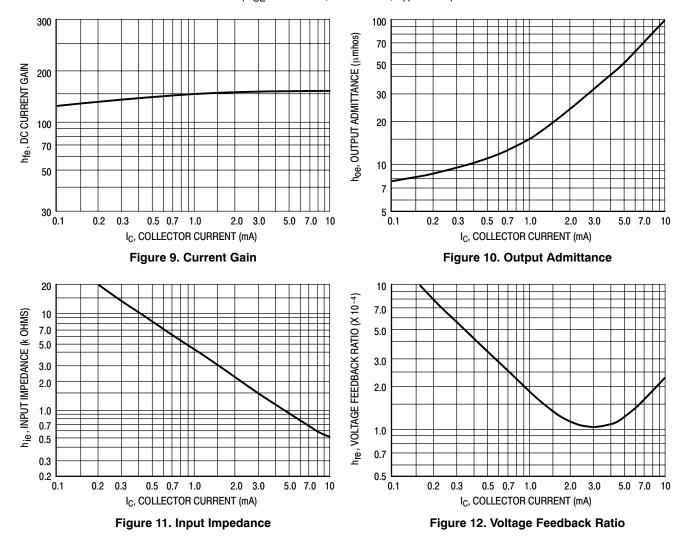
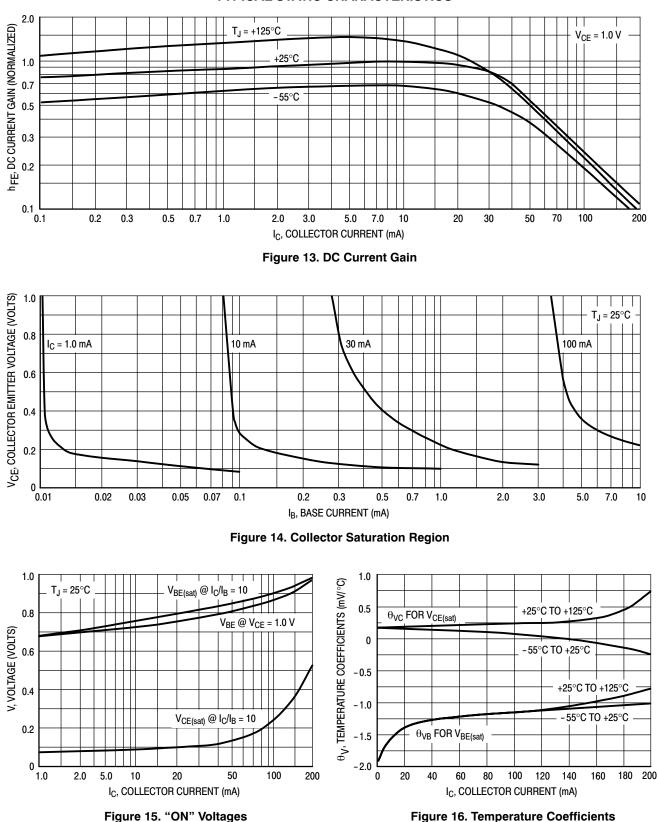


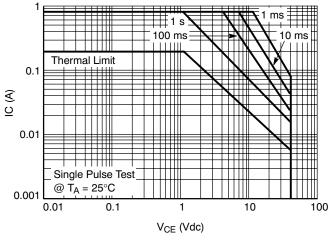
Figure 7.

h PARAMETERS (V_{CE} = -10 Vdc, f = 1.0 kHz, T_A = 25°C)





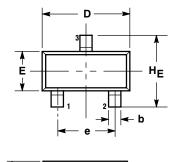
TYPICAL STATIC CHARACTERISTICS

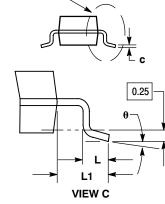




PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AN**





SEE VIEW C

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF 3 BASE MATERIAL. 318-01 THRU -07 AND -09 OBSOLETE, NEW
- 4 STANDARD 318-08

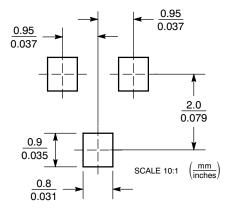
	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.89	1.00	1.11	0.035	0.040	0.044	
A1	0.01	0.06	0.10	0.001	0.002	0.004	
q	0.37	0.44	0.50	0.015	0.018	0.020	
С	0.09	0.13	0.18	0.003	0.005	0.007	
D	2.80	2.90	3.04	0.110	0.114	0.120	
Е	1.20	1.30	1.40	0.047	0.051	0.055	
e	1.78	1.90	2.04	0.070	0.075	0.081	
Г	0.10	0.20	0.30	0.004	0.008	0.012	
L1	0.35	0.54	0.69	0.014	0.021	0.029	
ΗE	2.10	2.40	2.64	0.083	0.094	0.104	

STYLE 6: PIN 1. BASE

EMITTER 2

COLLECTOR З.

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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