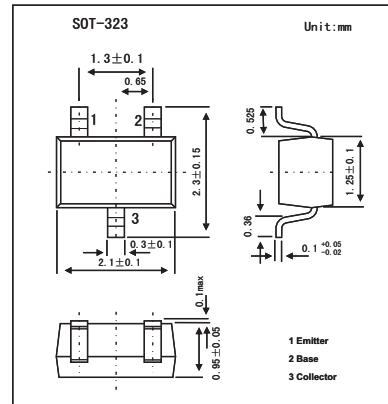


High Voltage PNP Transistors

MMST5401

■ Features

- Ultra-Small Surface Mount Package
- Ideal for Medium Power Amplification and
- Complementary NPN Type Available(MMST5551)



■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	-160	V
Collector-emitter voltage	V _{CEO}	-150	V
Emitter-base voltage	V _{EBO}	-5	V
Collector current-continuous	I _C	-0.6	A
Collector Power Dissipation	P _C	200	mW
Junction and storage temperature	T _J , T _{stg}	-55 to +150	°C

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Collector-base breakdown voltage	V _{(BR)CBO}	I _C = -100 μA, I _E = 0	-160			V
Collector-emitter breakdown voltage	V _{(BR)CEO}	I _C = -1.0 mA, I _B = 0	-150			V
Emitter-base breakdown voltage	V _{(BR)EBO}	I _E = -10 μA, I _C = 0	-5			V
Collector cutoff current	I _{CBO}	V _{CB} = -120 V, I _E = 0			-0.1	μA
Emitter cutoff current	I _{EBO}	V _{EB} = -4.0 V, I _C = 0			-0.1	μA
DC current gain *	h _{FE}	I _C = -1.0 mA, V _{CE} = -5 V	80			
		I _C = -10 mA, V _{CE} = -5 V	100		300	
		I _C = -50 mA, V _{CE} = -5 V	50			
Collector-emitter saturation voltage	V _{CE(sat)}	I _C = -50 mA, I _B = -5.0 mA			-0.5	V
Base-emitter saturation voltage	V _{BE(sat)}	I _C = -50 mA, I _B = -5.0 mA			-1.0	V
Transistor frequency	f _T	V _{CE} =-5V,I _C =-10mA,f=30MHz	100			MHz

* Pulse Test: Pulse Width = 300 μs, Duty Cycle=2.0%.

■ Marking

Marking	K4M
---------	-----