PNP general purpose transistor **SSTA56 / MMSTA56**

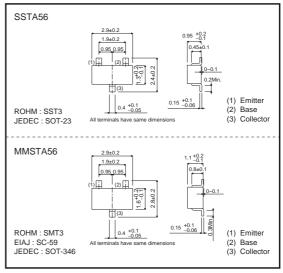
Features

- 1) BVCEO < -40V (Ic = 100µA)
- 2) Complements the SSTA06 / MMSTA06.

Package, marking and packaging specifications

Part No.	SSTA56	MMSTA56
Packaging type	SST3	SMT3
Marking	R2G	R2G
Code	T116	T146
Basic ordering unit (pieces)	3000	3000

•External dimensions (Unit : mm)



•Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit		
Collector-base voltage	Vсво	-80	V		
Collector-emitter voltage	VCEO	-80	V		
Emitter-base voltage	Vebo	-4	V		
Collector current	lc	-0.5	A		
Collector power dissipation	Pc	0.2	W		
		0.35	W *		
Junction temperature	Tj	150	°C		
Storage temperature	Tstg	-55 to +150	°C		

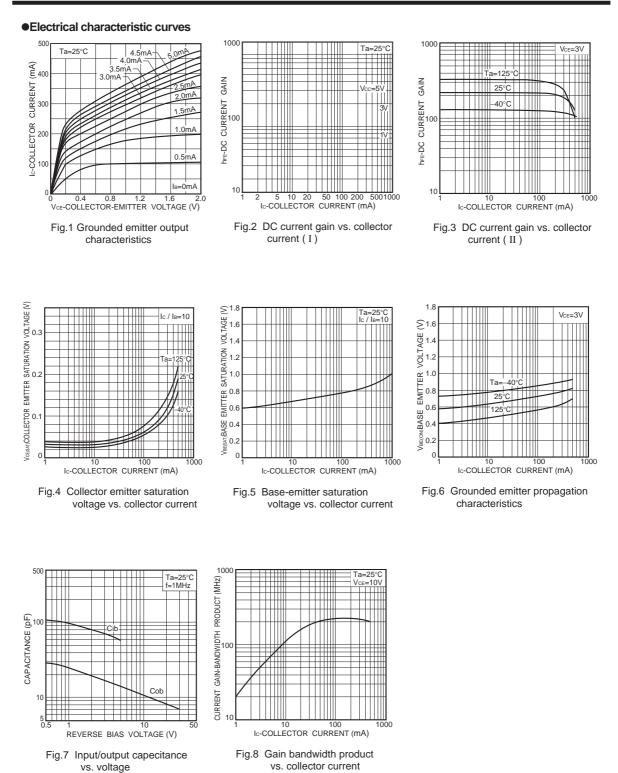
* Mounted on a 7×5×0.6mm CERAMIC SUBSTRATE

•Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Emitter-base breakdown voltage	ВVево	-4	-	-	V	Ic = -100mA
Collector-emitter breakdown voltage	BVCEO	-80	-	-	V	Ic = -1mA
Collector cutoff current	Ісво	-	-	-0.1	μΑ	Vcb=-80V
Collector cutoff current	ICEO	-	-	-1		VCE= -60V
Collector-emitter saturation voltage	VCE(sat)	-	-	0.25	V	Ic /IB= -100mA/-10mA
Base-emitter saturation voltage	VBE(on)	-	-	-1.2	V	Vce/IB= -1V/100mA
DC current transfer ratio	hFE 100	100	-	-		Vce=-1V, Ic=-10mA
		100	-	-		Vce= -1V , Ic = -100mA
Transition frequency	fT	50	-	-	MHz	Vce= -1V , Ie= 100mA , f=100MHz

SSTA56 / MMSTA56

Transistors



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