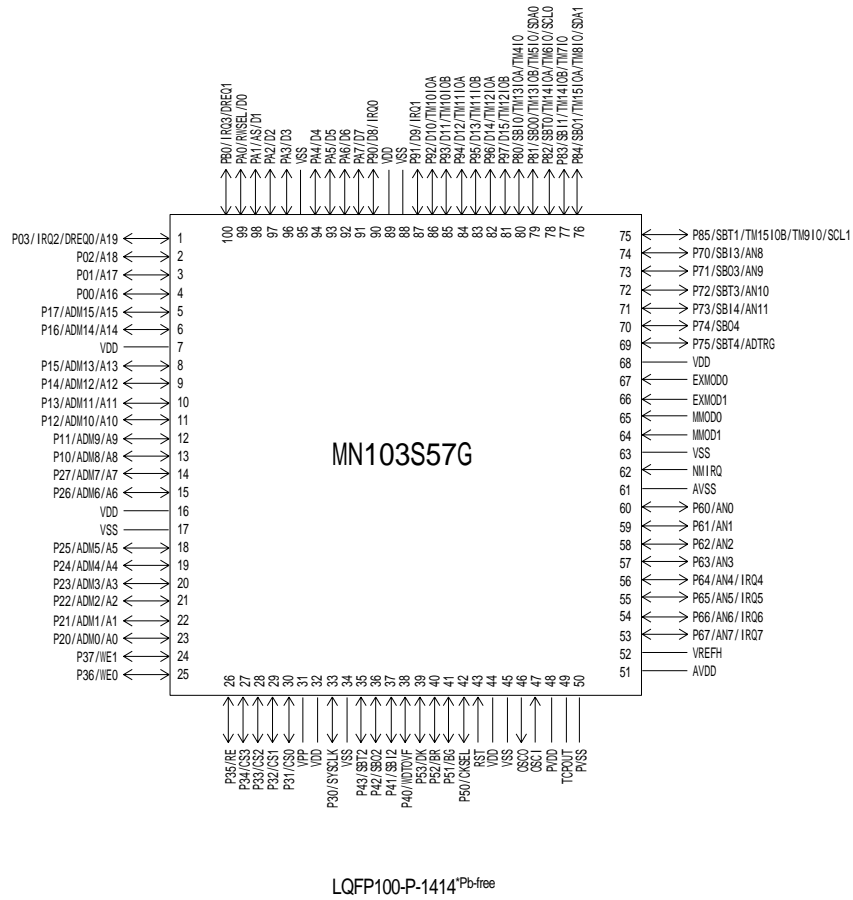


□ MN103S57G

Type		MN103S57G (under development)	
Command ROM (x64-bit)		128 K-byte	
Data RAM (x32-bit)		16 K-byte	
Package		LQFP100-P-1414 *Pb-free	
Minimum Instruction Execution Time		25.0 ns (at 3.0 V to 3.6 V, 40 MHz)	
Interrupts		<ul style="list-style-type: none"> • RESET • IRQ × 8 • NMI • Timer × 28 • SIF × 10 • I²C × 2 • DMA × 12 • WDT • A/D • System error 	
Timer Counter		<p>8-bit timer × 10</p> <p style="padding-left: 20px;">Reload-down count</p> <p style="padding-left: 20px;">Cascade connection possible (usable as a 16-bit to 32-bit timer)</p> <p>16-bit timer × 6</p> <p style="padding-left: 20px;">Up-down count</p> <p style="padding-left: 20px;">Input capture function</p> <p style="padding-left: 20px;">PWM generating function</p> <p style="padding-left: 20px;">Compare/capture register 2-ch.</p> <p>Watchdog timer × 1</p>	
DMA Controller		<p>Number of channels: 4</p> <p>Unit of transfer: 8/16/32 bits</p> <p>Max. Transfer cycles: 65535</p> <p>Starting factor: external interrupt, timer factor, serial transmission/reception factor, I²C transmission/reception factor, external transmission request factor, A/D conversion finish, software factor</p> <p>Transfer method: 2-bus cycle transfer</p> <p>Addressing modes: fixed, increment, decrement</p> <p>Transfer modes: word transfer, burst transfer, intermittent transfer</p>	
Serial Interface		<p>UART/synchronous/multi-master I²C interface selective: 2</p> <p>UART/synchronous interface selective: 3</p>	
I/O Pins	I/O	73	• Common use
A/D Inputs		10-bit × 12-ch.	
Electrical Characteristics		T.B.D.	

Pin Assignment



SupportTool

In-circuit Emulator	PX-ICE103S57	
On-boardDevelopmentTools	PX-ODB103S-O	
Flash Memory Built-in Type	Type	MN103SF57G (under development)
	Command ROM (× 64-bit)	128 K-byte
	Data RAM (× 32-bit)	16 K-byte
	Minimum instruction execution time	25.0 ns (at 3.0 V to 3.6 V, 40 MHz)
	Package	LQFP100-P-1414A *Pb-free

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