

Switching Transistors NPN Silicon

MPS2369 MPS2369A*

*ON Semiconductor Preferred Device

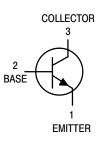
MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	VCEO	15	Vdc
Collector–Emitter Voltage	VCES	40	Vdc
Collector–Base Voltage	VCBO	40	Vdc
Emitter-Base Voltage	VEBO	4.5	Vdc
Collector Current — Continuous	IC	200	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	625 5.0	mW mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	200	°C/W



ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS						
Collector–Emitter Breakdown Voltage ⁽¹⁾ (I _C = 10 mAdc, I _B = 0)	MPS2369A	V(BR)CEO	15	_	_	Vdc
Collector–Emitter Breakdown Voltage (I _C = 10 μAdc, V _{BE} = 0)	MPS2369,A	V(BR)CES	40	_	_	Vdc
Collector–Base Breakdown Voltage (I _C = 10 μAdc, I _E = 0)	MPS2369,A	V(BR)CBO	40	_	_	Vdc
Emitter–Base Breakdown Voltage ($I_E = 10 \mu Adc, I_C = 0$)	MPS2369,A	V(BR)EBO	4.5	_	_	Vdc
Collector Cutoff Current (V _{CB} = 20 Vdc, I _E = 0) (V _{CB} = 20 Vdc, I _E = 0, T _A = 125°C)	MPS2369,A	ICBO			0.4 30	μAdc
Collector Cutoff Current (VCE = 20 Vdc, VBE = 0)	MPS2369,A	ICES	_	_	0.4	μAdc

^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

Preferred devices are ON Semiconductor recommended choices for future use and best overall value.

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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted) (Continued)

Characteristic		Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS						
DC Current Gain(1) $ \begin{aligned} &(I_{C}=10 \text{ mAdc}, V_{CE}=1.0 \text{ Vdc}) \\ &(I_{C}=10 \text{ mAdc}, V_{CE}=1.0 \text{ Vdc}, T_{A}=-55^{\circ}\text{C}) \\ &(I_{C}=10 \text{ mAdc}, V_{CE}=1.0 \text{ Vdc}) \\ &(I_{C}=10 \text{ mAdc}, V_{CE}=0.35 \text{ Vdc}) \\ &(I_{C}=10 \text{ mAdc}, V_{CE}=0.35 \text{ Vdc}, T_{A}=-55^{\circ}\text{C}) \\ &(I_{C}=30 \text{ mAdc}, V_{CE}=0.4 \text{ Vdc}) \\ &(I_{C}=100 \text{ mAdc}, V_{CE}=2.0 \text{ Vdc}) \\ &(I_{C}=100 \text{ mAdc}, V_{CE}=1.0 \text{ Vdc}) \end{aligned} $	MPS2369A MPS2369 MPS2369A MPS2369A MPS2369A MPS2369A MPS2369 MPS2369A	hFE		_ _ _ _ _ _	120 — 120 — — — — —	_
Collector–Emitter Saturation Voltage ⁽¹⁾ $(I_{C} = 10 \text{ mAdc}, I_{B} = 1.0 \text{ mAdc})$ $(I_{C} = 10 \text{ mAdc}, I_{B} = 1.0 \text{ mAdc})$ $(I_{C} = 10 \text{ mAdc}, I_{B} = 1.0 \text{ mAdc}, T_{A} = +125^{\circ}\text{C})$ $(I_{C} = 30 \text{ mAdc}, I_{B} = 3.0 \text{ mAdc})$ $(I_{C} = 100 \text{ mAdc}, I_{B} = 10 \text{ mAdc})$	MPS2369 MPS2369A MPS2369A MPS2369A MPS2369A	VCE(sat)	_ _ _ _	_ _ _ _	0.25 0.20 0.30 0.25 0.50	Vdc
$\label{eq:base-emitter} \begin{array}{l} \text{Base-Emitter Saturation Voltage}(1) \\ \text{(I}_{C} = 10 \text{ mAdc, I}_{B} = 1.0 \text{ mAdc)} \\ \text{(I}_{C} = 10 \text{ mAdc, I}_{B} = 1.0 \text{ mAdc, T}_{A} = +125^{\circ}\text{C)} \\ \text{(I}_{C} = 10 \text{ mAdc, I}_{B} = 1.0 \text{ mAdc, T}_{A} = -55^{\circ}\text{C)} \\ \text{(I}_{C} = 30 \text{ mAdc, I}_{B} = 3.0 \text{ mAdc)} \\ \text{(I}_{C} = 100 \text{ mAdc, I}_{B} = 10 \text{ mAdc)} \end{array}$	MPS2369 MPS2369A MPS2369A MPS2369A MPS2369A	VBE(sat)	0.7 0.5 — —	_ _ _ _ _	0.85 — 1.02 1.15 1.60	Vdc
SMALL-SIGNAL CHARACTERISTICS						
Output Capacitance (V _{CB} = 5.0 Vdc, I _E = 0, f = 1.0 MHz)	MPS2369,A	C _{obo}	_	_	4.0	pF
Small–Signal Current Gain (I _C = 10 mAdc, V _{CE} = 10 Vdc, f = 100 MHz)	MPS2369,A	h _{fe}	5.0	_	_	_
SWITCHING CHARACTERISTICS		·				
Storage Time $(I_{B1} = I_{B2} = I_C = 10 \text{ mAdc})$ (Figure 3)	MPS2369,A	t _S	_	5.0	13	ns
Turn–On Time $(V_{CC} = 3.0 \text{ Vdc}, I_C = 10 \text{ mAdc}, I_{B1} = 3.0 \text{ mAdc})$ (Figure 1)	MPS2369,A	t _{on}	_	8.0	12	ns
Turn–Off Time (V _{CC} = 3.0 Vdc, I _C = 10 mAdc, I _{B1} = 3.0 mAdc, I _{B2} = 1.5 mAdc) (Figure 2)	MPS2369,A	t _{Off}	_	10	18	ns

^{1.} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%.

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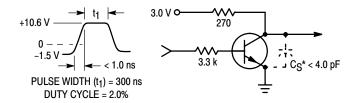


Figure 1. ton Circuit

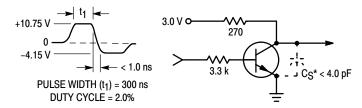


Figure 2. toff Circuit

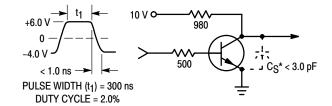


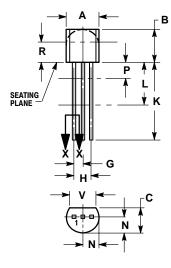
Figure 3. Storage Test Circuit

^{*}Total shunt capacitance of test jig and connectors.

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PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 **ISSUE AL**





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
7	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
P		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

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