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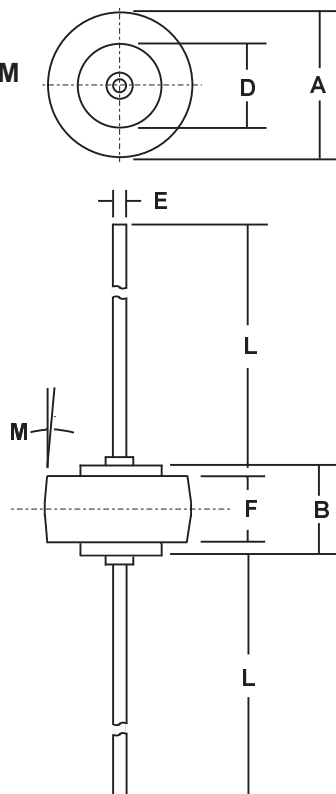
**Looking For: MR750, MR751, MR752,
 MR754, MR756 Replacements?**

25 AMP LEAD MOUNT BUTTON DIODES

FEATURES

- SUGGESTED REPLACEMENT FOR MR751, MR752, MR754, MR756 DIODES
- VOID FREE VACUUM DIE SOLDERING FOR MAXIMUM MECHANICAL STRENGTH AND HEAT DISSIPATION (Solder Voids: Typical $\leq 2\%$, Max. $\leq 10\%$ of Die Area)
- LARGE DIE FOR HIGH POWER HEAVY DUTY PERFORMANCE
- HIGH HEAT HANDLING CAPABILITY WITH VERY LOW THERMAL STRESS
- PROPRIETARY JUNCTION PASSIVATION FOR SUPERIOR RELIABILITY AND PERFORMANCE
- LOW FORWARD VOLTAGE DROP

MECHANICAL SPECIFICATION



Die Size:
 0.165" x 0.165"
 Square

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	8.43	8.69	0.332	0.342
B	5.94	6.25	0.234	0.246
D	5.46	1.35	0.050	0.053
E	1.27	5.71	0.215	0.225
F	4.19	4.45	0.165	0.175
L	25.15	25.65	0.990	1.010
M	5° NOM		5° NOM	

MECHANICAL DATA

- Case: Molded Epoxy (UL Flammability Rating 94V-0)
- Finish: All external surfaces are corrosion resistant and the contact areas are readily solderable
- Maximum Lead Soldering Temperature: 210 °C, 3/8" case for 10 seconds at 5 lbs tension
- Mounting Position: Any
- Polarity: Color band or diode symbol on case
- Weight: 0.09 Ounces (2.5 Grams)

RoHS COMPLIANT

MAXIMUM RATINGS & ELECTRICAL CHARACTERISTICS

Ratings at 25 °C ambient temperature unless otherwise specified.

PARAMETER (TEST CONDITIONS)	SYMBOL	RATINGS					UNITS
		DR750	DR751	DR752	DR754	DR756	
Series Number							
Maximum DC Blocking Voltage	VRRM	50	100	200	400	600	VOLTS
Maximum RMS Voltage	VRWM						
Maximum Peak Recurrent Reverse Voltage	VDC						
Non-repetitive Peak Reverse Voltage (Half wave, single phase, 60 Hz peak)	VRSM	60	120	240	480	720	
Average Forward Rectified Current @ Tc=150 °C	IO	25					
Peak Forward Surge Current (8.3mS single half sine wave superimposed on rated load)	IFSM	500					AMPS
Maximum Forward Voltage Drop at 25 Amp DC, 3/8" Leads	VF	1.1 (Typical 1.05)					VOLTS
Maximum Instantaneous Forward Voltage Drop at 100 Amp	VF	1.25					
Maximum Average DC Reverse Current @ TA = 25 °C	IRM	1					µA
At Rated DC Blocking Voltage @ TA = 100 °C		50					
Maximum Thermal Resistance, Junction to Case (Note 1)	RθJC	0.9					°C/W
Junction Operating and Storage Temperature Range	TJ, TSTG	-65 to +175					°C

Notes: 1) Both Leads to Heatsink, Equal Length