

# **OKI** Semiconductor

This version: Sep. 1998 Previous version: Mar. 1996

## MSM6545/6575/6545L/6575L

Operatable at 0.9 V and Built-in Buzzer Circuit 4-Bit Microcontroller

#### **GENERAL DESCRIPTION**

MSM6545/6575/6545L/6575L is a 4-bit, low-power microcontroller manufactured in a CMOS silicon gate process. The microcontroller can be initialized and operated at a low supply voltage of  $0.9~\rm V$ .

This single device contains a crystal oscillator circuit, voltage converter circuits, a time base counter, a ROM, a RAM, a stack RAM, I/O ports, interrupt function components, a serial I/O port, a buzzer output circuit, and an updown counter.

This IC is driven by one battery and is well suited to products that need to be operated under low power consumption.

#### **FEATURES**

• The IC can be initialized and operated even at a low voltage of 0.9 V.

• Low power consumption

• ROM : 4096 words × 17 bits (MSM6545/6545L)

2048 words × 17 bits (MSM6575/6575L)

• RAM : 256 words × 4 bits (MSM6545/6545L)

 $128 \text{ words} \times 4 \text{ bits (MSM}6575/6575L)$ 

• I/O port

Input-output port  $: 7 \text{ ports} \times 4 \text{ bits}$ Input port  $: 1 \text{ port} \times 4 \text{ bits}$ 

Interrupt functions (real-time interrupt, external interrupt, and serial interrupt)

• Serial I/O port : 8-bit sync communication

• Buzzer output circuit

• 4-bit decimal updown counter

• 73 instructions

• Minimum instruction execution time : 61 μs

Operation under single 1.5 V power supply (MSM6545/6575)

A mask option allows the 3 V power supply to be used (MSM6545L/6575L)

• Built-in 32.768 kHz crystal oscillator circuit

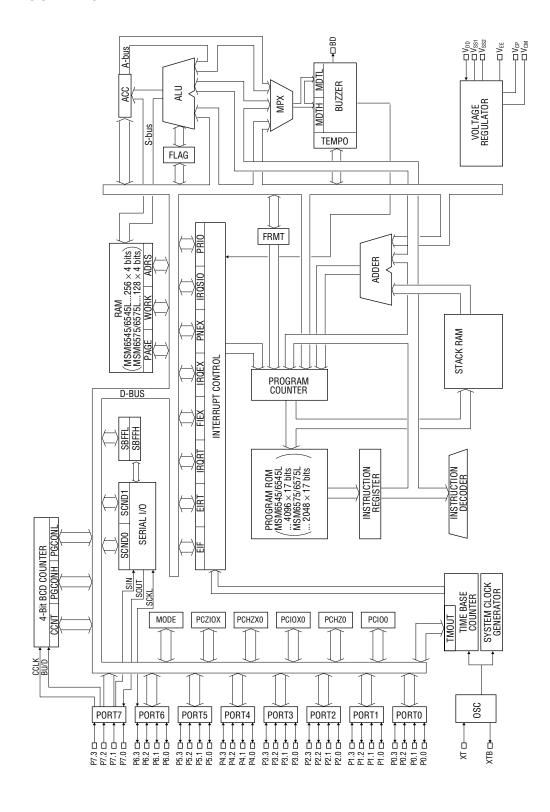
Package options:

44-pin plastic QFP (QFP44-P-910-0.80-K) : (Product name : MSM6545/6545L-××GS-K) 44-pin plastic QFP (QFP44-P-910-0.80-2K) : (Product name : MSM6545/6545L-××GS-2K) 44-pin plastic QFP (QFP44-P-910-0.80-K) : (Product name : MSM6575/6575L-××GS-K) 44-pin plastic QFP (QFP44-P-910-0.80-2K) : (Product name : MSM6575/6575L-××GS-2K)

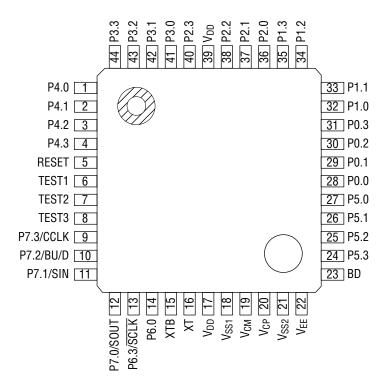
Chip

×× indicates a code number.

#### **BLOCK DIAGRAM**



#### PIN CONFIGURATION (TOP VIEW)



44-Pin Plastic QFP

Notes: 1. P6.3, P7.0 and P7.1 also function as serial port pins. P7.2, and P7.3 also function as updown counter pins.

2. P6.1 and P6.2 are not assigned pins.

## **PIN DESCRIPTIONS**

Symbol	Туре	Description						
PORT0	1/0	4-bit Input-output port, I/O switchable, with/without input pull-down resistor						
(P0.0 to P0.3)	1/0	4-bit input-output port, i/o switchable, with/without input pull-down resistor						
PORT1	1/0	4-bit Input-output port, I/O switchable, with/without input pull-down resistor						
(P1.1 to P1.3)	1/0							
PORT2	1/0	4-bit Input-output port, I/O switchable,	P2.0 to P2.3	External interrupt				
(P2.0 to P2.3)	1/0	with/without input pull-down resistor	F 2.0 t0 F 2.3	port				
PORT3	1/0	4-bit Input-output port, I/O switchable, with/w	ithout input pull.	down resistor				
(P3.0 to P3.3)	1/0	4-bit input-output port, i/O switchable, with/w	itilout iliput puli	-uowii iesisioi				
PORT4	1/0	4-bit Input-output port, I/O switchable, with/w	ithout input pull	down register				
(P4.0 to P4.3)	1/0	4-bit input-output port, i/O switchable, with/w	itilout iliput puli-	-uowii resistoi				
PORT5	1/0	4 hit lanut output part 1/0 quitabable with/w	ithout input pull	down register				
(P5.0 to P5.3)	1/0	4-bit Input-output port, I/O switchable, with/w	itiiout iiiput puii-	-down resistor				
PORT6	1/0	4-bit Input-output port, I/O switchable,						
(P6.0 to P6.3)	1/0	with/without input pull-down resistor	P6.3: SCLK	Chanad with a wial				
		4-bit input port	P7.0: SOUT	Shared with serial				
PORT7			P7.1: SIN	port				
(P7.0 to P7.3)	I	Tie to the negative pole of the battery when not used.	P7.2: BU/D	Shared with 4-bit				
		not useu.	P7.3: CCLK	up/down counter				
BD	0	Buzzer output pin						
RESET	I	Reset pin with input pull-down resistor						
TEST1		Tasting nine with input null down register						
TEST2	I	Testing pins with input pull-down resistor						
TEST3		Tie to the negative pole of the battery.						
XT	I	Connection nine for exectal applicator						
XTB	0	Connection pins for crystal oscillator						
$V_{DD}$	_	0 V power supply pin						
V <sub>SS1</sub>	_	$-1.5$ V supply pin (power supply pin for $-1.5$ \	/ operation)					
V <sub>SS2</sub>	_	-3.0  V supply pin (power supply pin for $-3.0  V$	/ operation)					
V <sub>CP</sub>		Connection wine for internal material development						
V <sub>CM</sub>	_	Connection pins for internal potential developr	nent capacitor					
V <sub>EE</sub>	_	Supply pin for internal logic (constant voltage	circuit output pi	n)				

#### ABSOLUTE MAXIMUM RATINGS (MSM6545/6575, 1.5 V, BUF = "0")

 $V_{DD} = 0 \text{ V } (V_{SS1} = \text{battery voltage})$ 

Parameter	Symbol	Condition	Rating	Unit
Power Supply Voltage	V <sub>SS1</sub>		-6.0 to +0.3	
Input Voltage	V <sub>IN</sub>	Ta = 25°C	V <sub>SS1</sub> – 0.3 to +0.3	V
Output Voltage	V <sub>OUT</sub>		V <sub>SS1</sub> – 0.3 to +0.3	
Storage Temperature	T <sub>STG</sub>	_	-55 to +125	°C

Note: The input of the constant voltage circuit is equal to the output of the voltage converter  $(V_{SS2})$ .

#### RECOMMENDED OPERATING CONDITIONS (MSM6545/6575, 1.5 V, BUF = "0")

 $V_{DD} = 0 V (V_{SS1} = battery voltage)$ 

			,	
Parameter	Symbol	Condition	Range	Unit
Operating Voltage	V <sub>op</sub>	_	−1.75 to −0.9	V
Operating Temperature	T <sub>op</sub>	_	-20 to +70	°C
Oscillation Frequency	fosc	_	32.768	kHz

Note: The input of the constant voltage circuit is equal to the output of the voltage converter  $(V_{SS2})$ .

#### ELECTRICAL CHARACTERISTICS (MSM6545/6575, 1.5 V, BUF = "0")

 $V_{DD} = 0 \text{ V}, V_{SS1} = -1.5 \text{ V}$  (battery voltage),  $V_{SS2} = -3.0 \text{ V}, f_{OSC} = 32.768 \text{ kHz}, C_X = 35 \text{ pF}, Ta = 25^{\circ}\text{C}$ 

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit	Applied Pin
Power Supply Current	I <sub>DD</sub>	*1	_	3	_	μΑ	_
Oscillation Start Voltage	-V <sub>OSC</sub>	Within 2 seconds	_	_	0.9	٧	V <sub>SS1</sub>
Output Current 1	-Іон1	$V_0 = -0.5 \text{ V}$	150		_		PORT0 to PORT6*2
Output Gurrent 1	I <sub>OL1</sub>	$V_0 = -1.0 \text{ V}$	150	_	_	μΑ	SOUT, SCLK
Output Current 2	-I <sub>0H2</sub>	V <sub>0</sub> = −0.5 V	20	_	_	μА	BD
Output Current 2	I <sub>OL2</sub>	V <sub>0</sub> = −1.0 V	20		_		טט
Input Current 1	l	$V_I = 0 \text{ V}$ , in the input state, 7 15	n the input state,	30		PORT0 to	
Input Current 1	l <sub>IH1</sub>	with pull-down resistor	'	10	30	μΑ	PORT6 *2
Input Lookaga Current	111	$V_I = 0 \text{ V}, -1.5 \text{ V}, \text{ in the input state,}$			4		PORT0 to PORT7*2
Input Leakage Current		without pull-down resistor	_	_	I	μΑ	SIN, SOUT, SCLK
Input Current 2		V <sub>I</sub> = 0 V, with pull-down	70	250	500		RESET
Input Current 3	I IH3	I <sub>IH3</sub> resistor 70	250	500	μΑ	TEST1 to TEST3	
lianist Valtana	-V <sub>IH</sub>		_	_	0.3	V	All input pine
IIIput voitage	put Voltage –V <sub>IL</sub>		1.2	_	_	V	All input pins

<sup>\*1</sup> Depends on the program. (Values in the above table are applied in the case where the software duty is about 5%.)

Note: The input of the constant voltage circuit is equal to the output of the voltage converter  $(V_{SS2})$ .

<sup>\*2</sup> PORT0 = P0.0 to P0.3, PORT1 = P1.0 to P1.3, PORT2 = P2.0 to P2.3, PORT3 = P3.0 to P3.3, PORT4 = P4.0 to P4.3, PORT5 = P5.0 to P5.3, PORT6 = P6.0 to P6.3, PORT7 = P7.0 to P7.3

## ABSOLUTE MAXIMUM RATINGS (MSM6545/6575, 1.5 V, BUF = "1")

 $V_{DD} = 0 V (V_{SS1} = battery voltage)$ 

			22 ( 88.	<del>,</del>
Parameter	Symbol	Condition	Rating	Unit
Power Supply Voltage	V <sub>SS1</sub>		-6.0 to +0.3	
Input Voltage	V <sub>IN</sub>	Ta = 25°C	V <sub>SS1</sub> – 0.3 to +0.3	V
Output Voltage	V <sub>OUT</sub>		V <sub>SS1</sub> – 0.3 to +0.3	
Storage Temperature	T <sub>STG</sub>	_	-55 to +125	°C

Note: The input of the constant voltage circuit is directly connected to the power supply  $(V_{SS1})$ .

## RECOMMENDED OPERATING CONDITIONS (MSM6545/6575, 1.5 V, BUF = "1")

 $V_{DD} = 0 \text{ V } (V_{SS1} = \text{battery voltage})$ 

Parameter	Symbol	Condition	Range	Unit
Operating Voltage	V <sub>op</sub>	_	−1.75 to −0.9	V
Operating Temperature	T <sub>op</sub>	_	−20 to +70	°C
Oscillation Frequency	fosc	_	32.768	kHz

Note: The input of the constant voltage circuit is directly connected to the power supply (V<sub>SS1</sub>).

## ELECTRICAL CHARACTERISTICS (MSM6545/6575, 1.5 V, BUF = "1")

 $V_{DD} = 0 \text{ V}, V_{SS1} = -1.5 \text{ V}$  (battery voltage),  $V_{SS2} = -3.0 \text{ V}, f_{OSC} = 32.768 \text{ kHz}, C_X = 35 \text{ pF}, Ta = 25^{\circ}\text{C}$ 

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit	Applied Pin	
Power Supply Current	I <sub>DD</sub>	*1	_	1.5	_	μΑ	_	
Oscillation Start Voltage	-V <sub>OSC</sub>	Within 2 seconds	_	_	0.9	V	V <sub>SS1</sub>	
Output Current 1	-I <sub>OH1</sub>	$V_0 = -0.5 \text{ V}$	150	_	_		PORTO to PORT6*2	
Output Guiteiit 1	I <sub>OL1</sub>	$V_0 = -1.0 \text{ V}$	150	_	—	μΑ	SOUT, SCLK	
Output Current 0	-I <sub>OH2</sub>	$V_0 = -0.5 \text{ V}$	20	_	—		BD	
Output Current 2	I <sub>OL2</sub>	$V_0 = -1.0 \text{ V}$	20	) <u> </u>	μΑ	עם		
Input Current 1	i	$V_I = 0 V$ , in the input state,	7	15	30		PORT0 to	
Input Current 1	I <sub>IH1</sub>	with pull-down resistor	′	13	30	μΑ	PORT6 *2	
Input Lookaga Current		$V_I = 0 \text{ V}, -1.5 \text{ V}, \text{ in the input state,}$				1		PORT0 to PORT7*2
Input Leakage Current		without pull-down resistor			'	μА	SIN, SOUT, SCLK	
Input Current 3		ı	$V_{I} = 0 \text{ V, without pull-down}$ 70 250	70 050	050	500	^	RESET
iliput Gurreilt 3	I <sub>IH3</sub>	resistor	/0	250	300	μΑ	TEST1 to TEST3	
Input Voltage	–V <sub>IH</sub>			_	0.3	V	All input pine	
	-V <sub>IL</sub>	_	1.2	_		V	All input pins	

<sup>\*1</sup> Depends on the program. (Values in the above table are applied in the case where the software duty is about 5%.)

Note: The input of the constant voltage circuit is directly connected to the power supply (V<sub>SS1</sub>).

<sup>\*2</sup> PORT0 = P0.0 to P0.3, PORT1 = P1.0 to P1.3, PORT2 = P2.0 to P2.3, PORT3 = P3.0 to P3.3, PORT4 = P4.0 to P4.3, PORT5 = P5.0 to P5.3, PORT6 = P6.0 to P6.3, PORT7 = P7.0 to P7.3

#### ABSOLUTE MAXIMUM RATINGS (MSM6545L/6575L, 3.0 V, BUF = "0")

 $V_{DD} = 0 \text{ V } (V_{SS2} = \text{battery voltage})$ 

			,	
Parameter	Symbol	Condition	Rating	Unit
Power Supply Voltage	V <sub>SS2</sub>		-6.0 to +0.3	
Input Voltage	V <sub>IN</sub>	Ta = 25°C	V <sub>SS2</sub> – 0.3 to +0.3	V
Output Voltage	V <sub>OUT</sub>		V <sub>SS2</sub> – 0.3 to +0.3	
Storage Temperature	T <sub>STG</sub>	_	−55 to +125	°C

Note: The input of the constant voltage circuit is equal to the output of the voltage converter  $(V_{SS1})$ .

## RECOMMENDED OPERATING CONDITIONS (MSM6545L/6575L, 3.0 V, BUF = "0")

 $V_{DD} = 0 V (V_{SS2} = battery voltage)$ 

Parameter	Symbol	Condition	Range	Unit
Operating Voltage	V <sub>op</sub>	_	−3.5 to −1.8	V
Operating Temperature	T <sub>op</sub>	_	−20 to +70	°C
Oscillation Frequency	f <sub>OSC</sub>	_	32.768	kHz

Note: The input of the constant voltage circuit is equal to the output of the voltage converter  $(V_{SS1})$ .

## ELECTRICAL CHARACTERISTICS (MSM6545L/6575L, 3.0 V, BUF = "0")

 $V_{DD} = 0 \text{ V}, V_{SS1} = -1.5 \text{ V}, V_{SS2} = -3.0 \text{ V}$  (battery voltage),  $f_{OSC} = 32.768 \text{ kHz}, C_X = 35 \text{ pF}, Ta = 25^{\circ}\text{C}$ 

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit	Applied Pin						
Power Supply Current	I <sub>DD</sub>	*1	_	0.75	_	μΑ	_						
Oscillation Start Voltage	-V <sub>OSC</sub>	Within 2 seconds	_	_	1.8	V	V <sub>SS2</sub>						
Output Current 1	-I <sub>OH1</sub>	$V_0 = -0.5 \text{ V}$	500	_	_		PORT0 to PORT6*2						
Output Gurrent 1	I <sub>OL1</sub>	$V_0 = -2.5 \text{ V}$	500	_	_	μΑ	SOUT, SCLK						
Output Current 0	-I <sub>0H2</sub>	$V_0 = -0.5 \text{ V}$	20	_	_		BD						
Output Current 2	I <sub>OL2</sub>	$V_0 = -2.5 \text{ V}$	20		_	μΑ	טס						
Input Current 1	i	$V_I = 0 V$ , in the input state,	50	100	100	100	100	200		PORT0 to			
Input Current 1	l <sub>IH1</sub>	with pull-down resistor	30		200	μΑ	PORT6 *2						
Input Lookaga Current	lı l	$V_I = 0 \text{ V}, -3 \text{ V}, \text{ in the input state,}$			4		PORT0 to PORT7*2						
Input Leakage Current		without pull-down resistor	_	_	I	μΑ	SIN, SOUT, SCLK						
Innut Current O	I <sub>IH3</sub> V <sub>I</sub> = 0 V, with pull-down resistor									750	1500	_	RESET
Input Current 3		200	750	1500	μΑ	TEST1 to TEST3							
Land Walterna	-V <sub>IH</sub>		_	_	0.5	V	All input nine						
Input Voltage	-V <sub>IL</sub>			_	_	V	All input pins						

<sup>\*1</sup> Depends on the program. (Values in the above table are applied in the case where the software duty is about 5%.)

Note: The input of the constant voltage circuit is equal to the output of the voltage converter  $(V_{SS1})$ .

<sup>\*2</sup> PORT0 = P0.0 to P0.3, PORT1 = P1.0 to P1.3, PORT2 = P2.0 to P2.3, PORT3 = P3.0 to P3.3, PORT4 = P4.0 to P4.3, PORT5 = P5.0 to P5.3, PORT6 = P6.0 to P6.3, PORT7 = P7.0 to P7.3

#### ABSOLUTE MAXIMUM RATINGS (MSM6545L/6575L, 3.0 V, BUF = "1")

 $V_{DD} = 0 \text{ V } (V_{SS2} = \text{battery voltage})$ 

Parameter	Symbol	Condition	Rating	Unit
Power Supply Voltage	V <sub>SS2</sub>		-6.0 to +0.3	
Input Voltage	V <sub>IN</sub>	Ta = 25°C	V <sub>SS2</sub> – 0.3 to +0.3	V
Output Voltage	V <sub>OUT</sub>		V <sub>SS2</sub> – 0.3 to +0.3	
Storage Temperature	T <sub>STG</sub>	_	-55 to +125	°C

Note: The input of the constant voltage circuit is directly connected to the power supply (V<sub>SS2</sub>).

## RECOMMENDED OPERATING CONDITIONS (MSM6545L/6575L, 3.0 V, BUF = "1")

 $V_{DD} = 0 V (V_{SS2} = battery voltage)$ 

Parameter	Symbol	Condition	Range	Unit
Operating Voltage	V <sub>op</sub>	_	−3.5 to −0.9	V
Operating Temperature	T <sub>op</sub>	_	−20 to +70	°C
Oscillation Frequency	f <sub>OSC</sub>	_	32.768	kHz

Note: The input of the constant voltage circuit is directly connected to the power supply (V<sub>SS2</sub>).

## ELECTRICAL CHARACTERISTICS (MSM6545L/6575L, 3.0 V, BUF = "1")

 $V_{DD} = 0 \text{ V}, V_{SS1} = -1.5 \text{ V}, V_{SS2} = -3.0 \text{ V}$  (battery voltage),  $f_{OSC} = 32.768 \text{ kHz}, C_X = 35 \text{ pF}, Ta = 25^{\circ}\text{C}$ 

Daware star	Complete	_		T	N.4	1 1 14	Annilia d Din
Parameter	Symbol	Condition	win.	тур.	wax.	Unit	Applied Pin
Power Supply Current	$I_{DD}$	*1	_	1.5	_	μΑ	_
Oscillation Start Voltage	-V <sub>OSC</sub>	Within 2 seconds	_	_	0.9	V	$V_{SS2}$
Output Current 1	-I <sub>0H1</sub>	$V_0 = -0.5 \text{ V}$	500		<b>A</b> P(	PORT0 to PORT6*2	
	I <sub>OL1</sub>	$V_0 = -2.5 \text{ V}$	500	_		μA	SOUT, SCLK
Output Current 2	-I <sub>0H2</sub>	$V_0 = -0.5 \text{ V}$	20	_	 	μА	DD.
	I <sub>OL2</sub>	$V_0 = -2.5 \text{ V}$	20	_			BD
Input Current 1	I <sub>IH1</sub>	$V_I = 0 V$ , in the input state,	50	100	200	μА	PORT0 to
		with pull-down resistor					PORT6 *2
Input Leakage Current		$V_I = 0 \text{ V}, -3 \text{ V}, \text{ in the input state,}$		_	1	μА	PORT0 to PORT7*2
		without pull-down resistor					SIN, SOUT, SCLK
Input Current 3	I <sub>IH3</sub>	V <sub>I</sub> = 0 V, with pull-down	200	750	1500	μA	RESET
		resistor					TEST1 to TEST3
Input Voltage	–V <sub>IH</sub>	_	_	_	0.5	V	All input nine
	-V <sub>IL</sub>		2.5	_	_		All input pins

<sup>\*1</sup> Depends on the program. (Values in the above table are applied in the case where the software duty is about 5%.)

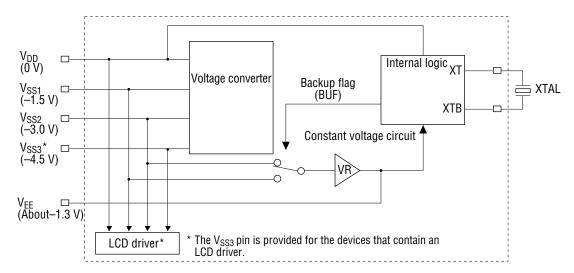
Note: The input of the constant voltage circuit is directly connected to the power supply (V<sub>SS2</sub>).

<sup>\*2</sup> PORT0 = P0.0 to P0.3, PORT1 = P1.0 to P1.3, PORT2 = P2.0 to P2.3, PORT3 = P3.0 to P3.3, PORT4 = P4.0 to P4.3, PORT5 = P5.0 to P5.3, PORT6 = P6.0 to P6.3, PORT7 = P7.0 to P7.3

#### **NOTES ON USE**

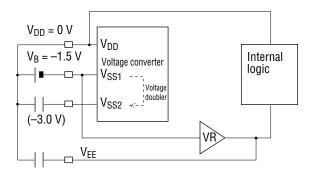
#### Power Supply for 0.9 V Microcontroller Series (Backup Flag and Constant-Voltage Circuit)

The 0.9 V devices have a built-in constant-voltage circuit. The output of this constant-voltage circuit powers the microcontroller's internal logic circuits. Setting a backup flag (BUF) allows the input of the constant voltage circuit to be switched to either the battery or the output generated in the voltage converter, based on the battery voltage. A battery voltage of 1.5 V or 3.0 V can be selected.



The output ( $V_{EE}$ ) of the constant-voltage circuit is set at approximately  $-1.3\,\mathrm{V}$ . This allows the current consumed by the internal logic to be limited, irrespective of the battery voltage. However, if the input of the constant voltage circuit is below this set value (approximately  $-1.3\,\mathrm{V}$ ), the output ( $V_{EE}$ ) is equal to the input. The  $0.9\,\mathrm{V}$  microcontroller can be operated even if the internal voltage (output from the constant voltage circuit) falls to  $0.9\,\mathrm{V}$ . Setting the backup flag allows a larger operating voltage margin despite changes in internal voltage due to noise. For example, for the  $1.5\,\mathrm{V}$  specification, setting the backup flag at "0" supplies twice the battery voltage to the constant voltage circuit. Thus, even if the battery voltage falls to  $0.9\,\mathrm{V}$ , the output voltage ( $V_{EE}$ ) is maintained at  $-1.3\,\mathrm{V}$ , providing a larger margin of operating voltage of the internal logic circuits, because  $1.8\,\mathrm{V}$  is applied to the input of the constant-voltage circuit. Figures 1 to 4 show the internal status depending on the backup flag settings for the battery, as well as status features.

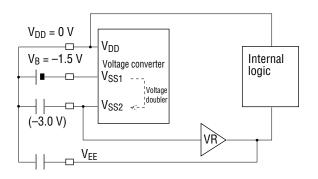
#### (Figure 1) 1.5 V Operation (Backup Flag = 1)



Internal status	The battery level V <sub>SS1</sub> is applied to the input of the constant voltage circuit.
Operating range	−0.9 to −1.75 V
Current consumption	1.5 μΑ*
Feature	When the battery level is powered down, the internal circuit is powered directly by the battery.

<sup>\*</sup> When the software duty is about 5%

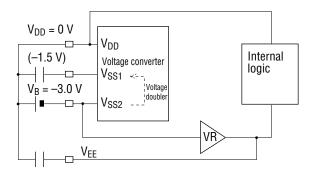
(Figure 2) 1.5 V Operation (Backup Flag = 0)



Internal status	A doubled level of $\ensuremath{V_{SS2}}$ is applied to the input of the constant voltage circuit.
Operating range	−0.9 to −1.75 V
Current consumption	3 μΑ*
Feature	When the battery level is powered down, a larger operating voltage margin is gained, compared to the case of Figure 1.

<sup>\*</sup> When the software duty is about 5%

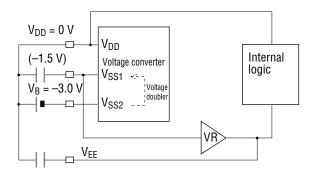
## (Figure 3) 3.0 V Operation (Backup Flag = 1)



Internal status	The battery level $V_{SS2}$ is applied to the input of the constant voltage circuit.
Operating range	−0.9 to −3.5 V
Current consumption	1.5 μΑ*
Feature	When the battery level is powered down, the internal circuit is powered directly by the battery.

<sup>\*</sup> When the software duty is about 5%

## (Figure 4) 3.0 V Operation (Backup Flag = 0)

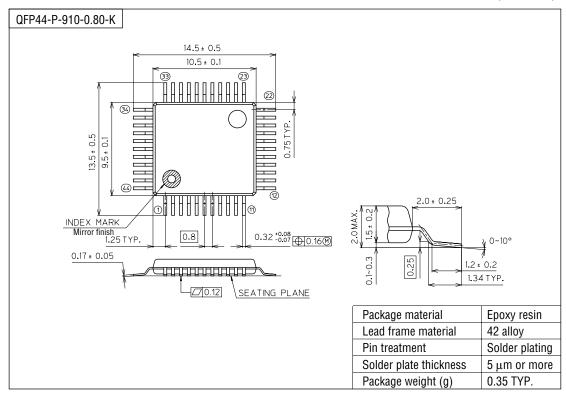


Internal status	A doubled level of $V_{SS1}$ is applied to the input of the constant voltage circuit.
Operating range	−1.8 to −3.5 V
Current consumption	0.75 μΑ*
Feature When the battery level is powered do smaller operating voltage margin is good compared to the case of Figure 3.	

<sup>\*</sup> When the software duty is about 5%

#### **PACKAGE DIMENSIONS**

(Unit: mm)

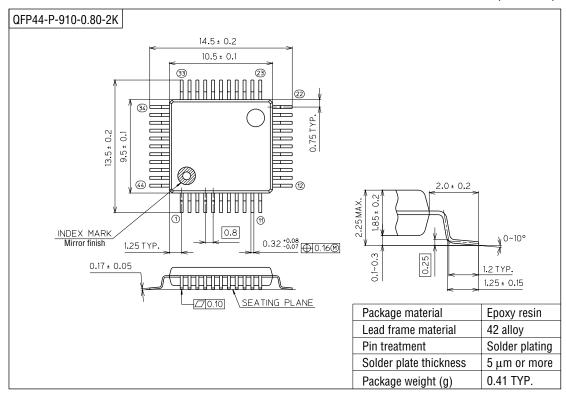


Notes for Mounting the Surface Mount Type Package

The SOP, QFP, TSOP, SOJ, QFJ (PLCC), SHP and BGA are surface mount type packages, which are very susceptible to heat in reflow mounting and humidity absorbed in storage.

Therefore, before you perform reflow mounting, contact Oki's responsible sales person for the product name, package name, pin number, package code and desired mounting conditions (reflow method, temperature and times).

(Unit: mm)



Notes for Mounting the Surface Mount Type Package

The SOP, QFP, TSOP, SOJ, QFJ (PLCC), SHP and BGA are surface mount type packages, which are very susceptible to heat in reflow mounting and humidity absorbed in storage. Therefore, before you perform reflow mounting, contact Oki's responsible sales person for the product name, package name, pin number, package code and desired mounting conditions (reflow method, temperature and times).