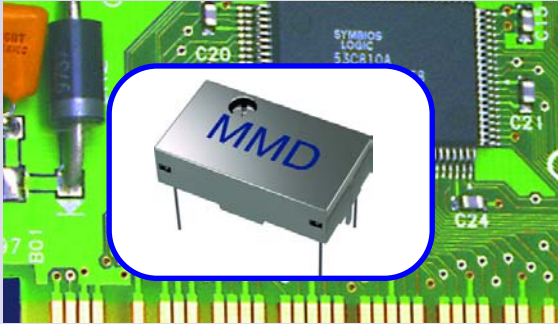


MTA and MTB Series

MMD
COMPONENTS

Full-Size (7.3mm or 4.7mm height)



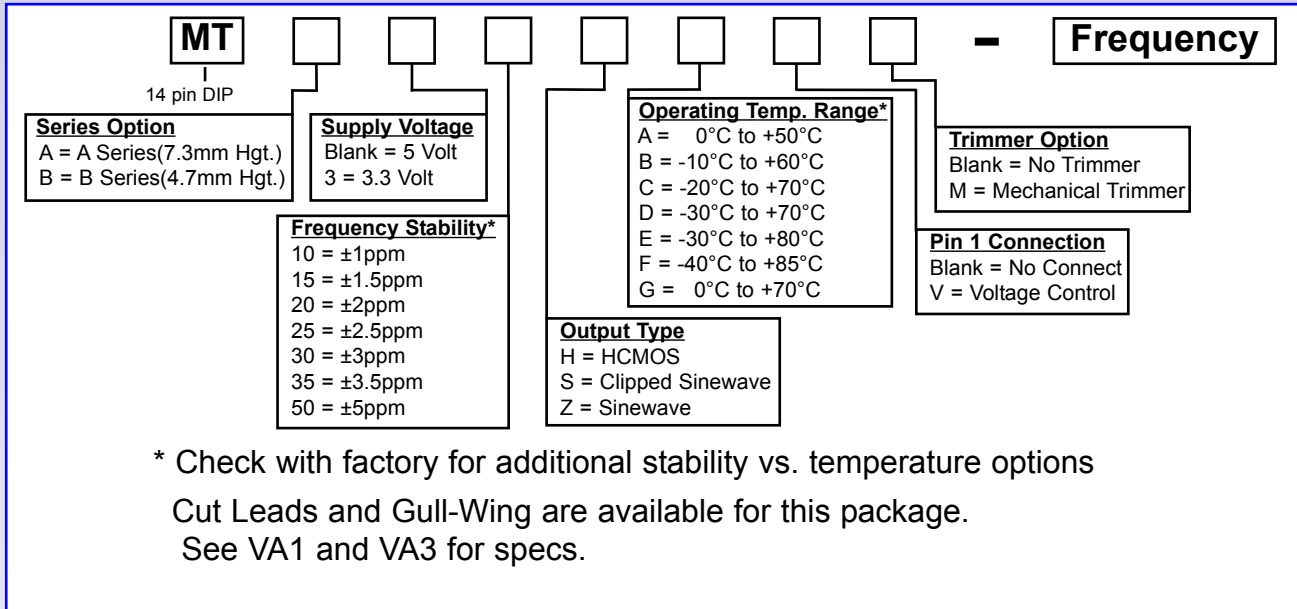
- **Industry Standard Package**
- **5.0 or 3.3 Volt**
- **HCMOS, Sinewave, Clipped Sine**
- **1.000MHz to 1.000GHz**
- **Stability Down to ± 1 ppm**

Electrical Specifications

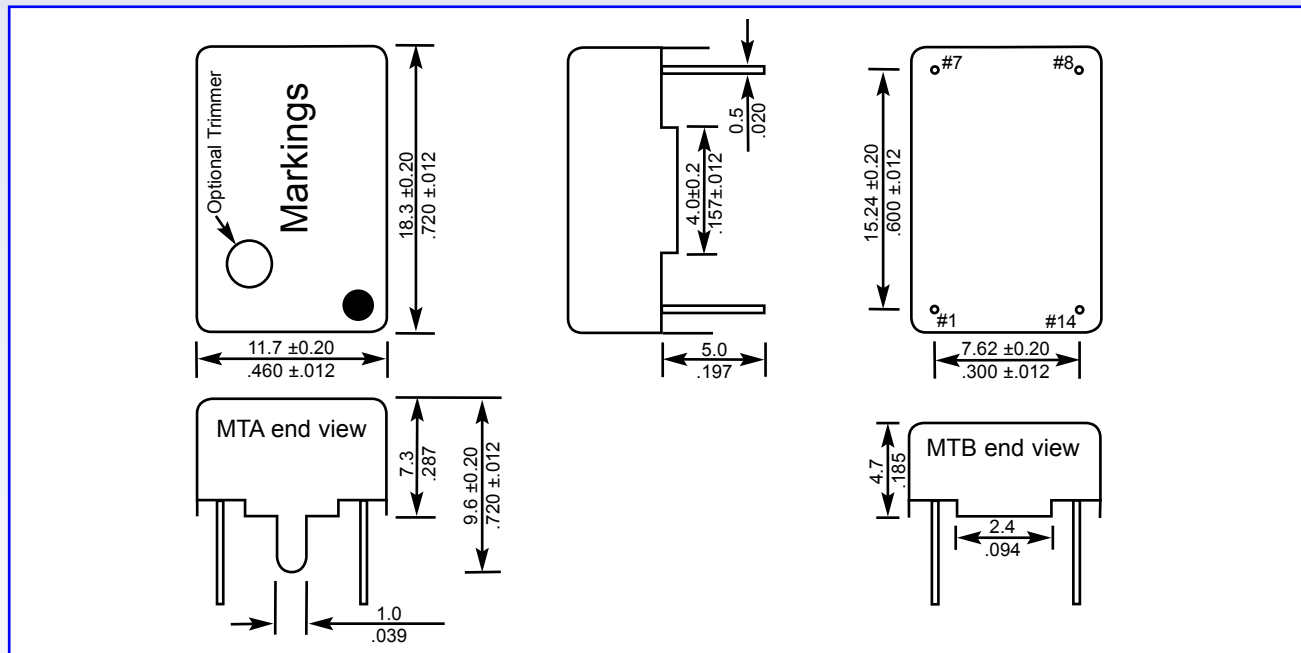
H Option = HCMOS Output	Frequency Range	1.000MHz to 160.000MHz
	Frequency Stability	Down to ± 1 ppm
	Load	10K Ohms // 15pF
	Supply Current	35mA max
S Option = Clipped Sine Output	Output	Logic"1" Level = 0.9Vdd min. Logic"0" Level = 0.1Vdd max.
	Frequency Range	8.000MHz to 300.000MHz
	Frequency Stability	Down to ± 1 ppm
	Load	10K Ohms // 15pF
Z Option = Sinewave Output	Supply Current	3mA max.
	Output	1.0V p-p min.
	Sinewave Output	8.000MHz to 1.000GHz
	Frequency Stability	Down to ± 1 ppm
Operating Temperature Range	Load	50 Ohms
	Supply Current	5mA max.
	Output	7dBm min.
	See Part Numbering Guide	
Storage Temperature Range		-40°C to +85°C
Supply Voltage (Vdd)	Vdd = 5V	5.0Vdc $\pm 5\%$
	Vdd = 3.3V	3.3Vdc $\pm 5\%$
Internal Trim (Top of can)		± 3 ppm min.
Control Voltage	Vdd = 5V	2.5Vdc ± 2.0 Vdc Positive Slope
	Vdd = 3.3V	1.65Vdc ± 1.5 Vdc Positive Slope
Pin 1 Connection	Blank	No Connect
	V Option	± 10 ppm min.
Frequency Stability	vs. Aging	± 1 ppm per year max.
	vs. Voltage (with a 5% change)	± 0.3 ppm
	vs. Load (with a 10% change)	± 0.3 ppm
Symmetry	@50% of waveform w/CMOS load	40/60%

Notes

Part Numbering Guide



Mechanical Dimensions



Pin Connections

Pin 1: Control Voltage or N/C
Pin 7: Case Ground
Pin 8: Output
Pin 14: Supply Voltage

Markings

Line 1: MMD
Line 2: Part Number
Line 3: Frequency
Line 4: Date Code