DOLBY PRO LOGIC SURROUND DECODER

GENERAL DESCRIPTION

The NJM2177 is a higher level integration and high quality audio performance monolithic IC designed for use in Dolby Pro Logic Surround System. The NJM2177 provides all the necessary function for a complete Pro Logic processor except time delay; Automatic input balance, noise sepuencer, adaptibve matrix, center mode control, and modified B-type noise reduction all on chip.

In addition to Dolby Pro Logic function including Dolby 3-stereo, this device provides two channel bypass mode and two special outputs used for other surround conbeniently.

At two channel by pass mode, noise and distortion of NJM2177A are lower than that of NJM2177

(note) Dolby and the double-D symbol are trademarks of Dolby Laboratories Licensing Corporation. San Francisco, CA94103-4813, USA.

This device available only to licensees of Dolby Lab.

Licensing and application information may be obtained from Dolby Lab.

9 to 13V

300mVrms

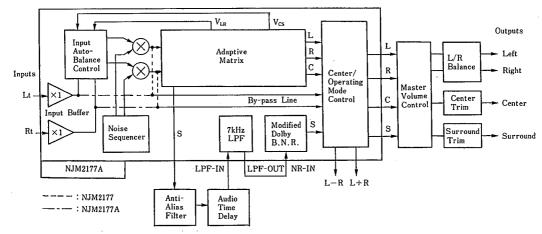
FEATURES

- Operating Voltage
- Dolby operating level
- Lower Operating Current 34mA typ.
- Internal mode control switches
- Package SDIP-56, QFP-64

FUNCTIONS

- Auto input balance and buffer
- Noise sequencer; a Noise generator, a sequencer controlled by external two bits
- Adaptive Matrix
- Center mode control; ON/OFF, Normal/Phantom/Wideband
- Modified Dolby B Type Noise Reduction and OP amp. for 7kHz low-pass filter
- Operating mode control; 4ch(L,C,R), 3ch(L,C,R), 2ch(no processing)
- L+R and L-R output

ACTIVE SURROUND DECODER BLOCK DIAGRAM



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NJM2177L/2177AL



NJM2177FB3/2177AFB3

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ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT	
Supply Voltage	V* 1	15	v	
Power Dissipation	PD	(SDIP-56) 700	mW	
		(QFP-64) 500	mW	
Operating Temperature Range	Topr	-20~+75		
Storage Temperature Range	Tstg	-40~+125		

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ELECTRICAL CHARACTERISTICS

(Ta=25°C, V+=12V, 0dB Reference is $300mV/1kH_Z$ at C-OUT. Unless otherwise specified.)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNI
Overall						
Operating Voltage Range	Vop		9.0		13.0	v
Operating Current	lcc	No signal	<u> </u>	34.0	40.0	mA
Reference Voltage	V _{ref}	No signal		4.0		v
Control SW input voltage						
2ch Mode	Vc-2ch	MODE-CNT PIN	0.0		0.8	v
3ch	V _C -3ch	MODE-CNT PIN	- I	Open	—	
4ch	V _C -4ch	MODE-CNT PIN	3.8		7.0	v
Center on	V _C -con	CENTER-CNT PIN	2.4	_	7.0	v
Center off	V _C -coff	CENTER-CNT PIN	0.0	_	0.8	v
Noise Seq. on	V _C -nson	NOISE-CNT-E PIN	0.0		0.8	v
Noise Seq. off	V _C -nsoff	NOISE-CNT-E PIN	3.2		7.0	v
Noise Seq. channel select H	V _C -nssH	NOISE-CNT-A and NOISE-CNT-B PIN	3.2	_	7.0	v v
Noise Seq. channel select L	V _C -nssL	NOISE-CNT-A and NOISE-CNT-B PIN	0.0		0.8	v .
					0.0	Ľ
Modified B Noise Reduction (0dBd Reference					r	
Voltage Gain	GV-BNR	$V_{in} = 0 dBd$, f=100Hz	-	9.0	-	dB
Decode Responce 1	$D_{\alpha c I}$	$V_{in} = 0 dBd$, f=1.0kHz	-1.6	-0.1	1.4	dB
2	D _{ec2}	$V_{in} = -15 \text{dBd}, f = 1.4 \text{kHz}$	-3.0	-1.5	0.0	dB
3	Dec3	$V_{in} = -20 dB$, f=1.4kHz	-4.9	-3.4	-1.9	dB
4	D _{cc4}	V_{in} =40dBd, f=5.0kHz	-6.8	-5.3	-3.8	dB
T.H.D	THD-NR	$V_{in} = 0 dBd$, f = 1.0kHz	—	0.07	—	%
Headroom	HR-NR	V+=9V AT T.H.D.=1%	15.0	17.0		dB
SN Ratio	SN-NR	Rg=0, weighted CCIR/ARM	76	82	-	dB
Noise sequencer		· · · ·				
OUTPUT Noise level	Vno		-15	-12.5	-10	dB
Output Noise Level Accuracy relative to Cch Lch	ΔV_{no}		-0.5	0.0	0.5	dB
Rch S'ch			· ·			
Adaptive Matrix		L				
·						
Output Level Accuracy relative to Cch						
L,R,S'ch out	∆Vol		-0.5	0.0	0.5	dB
Matrix Rejection relative L,R,C,S'ch out	Mr		25.0	40.0		dB
T.H.D L,R,C,S'ch out	THD-AM		-	0.02		%
Headroom L,R,C,S'ch out	HR-AM	V+=9V at T.H.D=1%	15.0	15.7	-	dB
Signal to Noise Ratio L,R,C,S' ch out	SN-AM	Rg=0, weighted CCIR/ARM	78	83		dB
Auto Balance						
Capture Range	CPR		-	±5	-	dB
Error collection	CER		-	±4	-	dB
T.H.D LI, RI OUT	THD-AB		-	0.03		%
S/N _. Lt, Rt OUT	SN-AB	Rg=0, weighted CCIR/ARM	78	83	_	dB
Headroom Lt,Rt OUT	HR-AB	V*=9V at T.H.D=1%	15.0	17.0	-	dB
L+R & L-R OUTPUT		· · · · · · · · · · · · · · · · · · ·	i			
						Ι
Output Level Accuracy relative to Cch		1		0.0	I _	dB
Output Level Accuracy relative to Cch $L+R$, $L-R$ ch	∆Vol-OP			1 0.0		
Utput Level Accuracy relative to Cch L+R, L-R ch T.H.D	∆Vol-OP THD-OP		_	0.02	_	%
L+R, $L-R$ ch		Rg=0, weighted CCIR/ARM	-	1	-	% dB

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MEMO

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