

## HALL AMPLIFIER IC

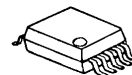
### ■GENERAL DESCRIPTION

The NJM2650 is a dual hall amplifier IC for converting signals from hall sensors to digital signals.

It is suitable for speed and position sensing of motor control system using DSP or micro controllers.

The NJM2650 can turn off the amplifier block and bias supply of hall device at the standby. Therefore, the consumption current of the application can be reduced.

### ■PACKAGE OUTLINE



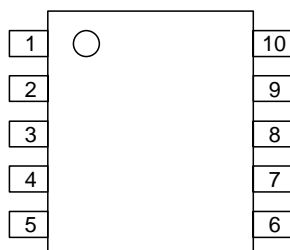
**NJM2650V**

### ■FEATURES

- Position Sensing Output
- Chip Enable
- Hall Bias Stand-By Function
- Open Collector Output
- Bipolar Technology
- Package Outline

SSOP10

### ■PIN CONFIGURATION



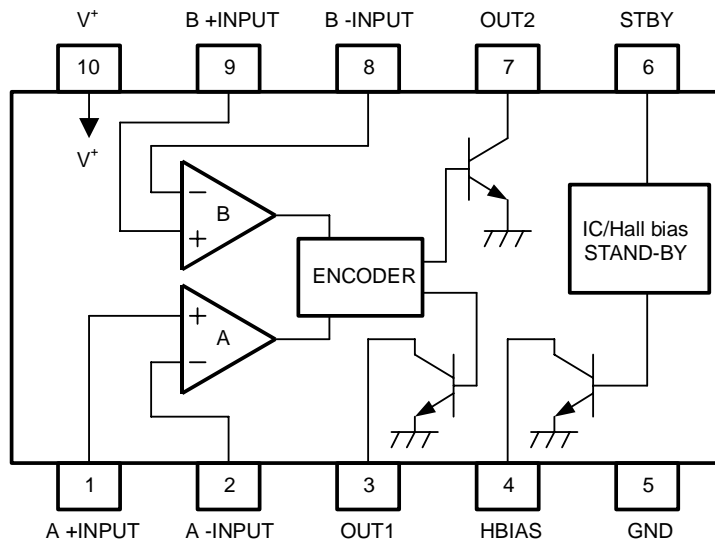
**NJM2650V**

#### PIN Function

1. A +INPUT
2. A -INPUT
3. OUT1
4. HBIAS
5. GND
6. STBY
7. OUT2
8. B -INPUT
9. B +INPUT
10. V<sup>+</sup>

# NJM2650

## ■BLOCK DIAGRAM



## ■ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Maximum Supply Voltage	V <sup>+</sup>	20	V
Output Voltage	V <sub>OUT</sub>	V <sup>+</sup>	V
Output Current	I <sub>OUT</sub>	7	mA
Hall Bias Current	I <sub>OBIAS</sub>	20	mA
Stand-By Terminal	V <sub>OUT</sub>	V <sup>+</sup>	V
Differential Input Voltage	V <sub>ID</sub>	V <sup>+</sup>	V
Input Voltage	V <sub>IC</sub>	-0.3 ~ V <sup>+</sup> -1.5	V
Power Dissipation	P <sub>D</sub>	SSOP10 250	mW
Operating Temperature Range	T <sub>opr</sub>	-40 ~ +85	°C
Storage Temperature Range	T <sub>stg</sub>	-50 ~ +150	°C

Output to GND in internal diode for ESD.

Output voltage is below to IC supply voltage.

■ELECTRICAL CHARACTERISTICS (V<sup>+</sup>=5V, V<sub>STBY</sub>=5V, Ta=25°C)

Total Device

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V <sup>+</sup>		4.5	–	18	V
Supply Current 1	I <sub>CC1</sub>	R <sub>L</sub> =∞	–	2	4	mA
Supply Current 2	I <sub>CC2</sub>	V <sub>STBY</sub> =0V	–	30	60	μA

Hall Amplifier Section

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V <sub>IO</sub>		-4.2	–	4.2	mV
Input Common Mode Voltage Range	V <sub>ICM</sub>		1.5	–	3.5	V
Input Bias Current	I <sub>B</sub>		–	–	600	nA

Hall Bias Block Section

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Low Level Output Voltage (HBIAS Pin)	V <sub>OLBIAS</sub>	I <sub>SINK</sub> =10mA	–	0.1	0.3	V
Output Leak Current (HBIAS Pin)	I <sub>LEAK(HBIAS)</sub>	V <sub>OUT</sub> =5V	–	–	5	μA

Output Section

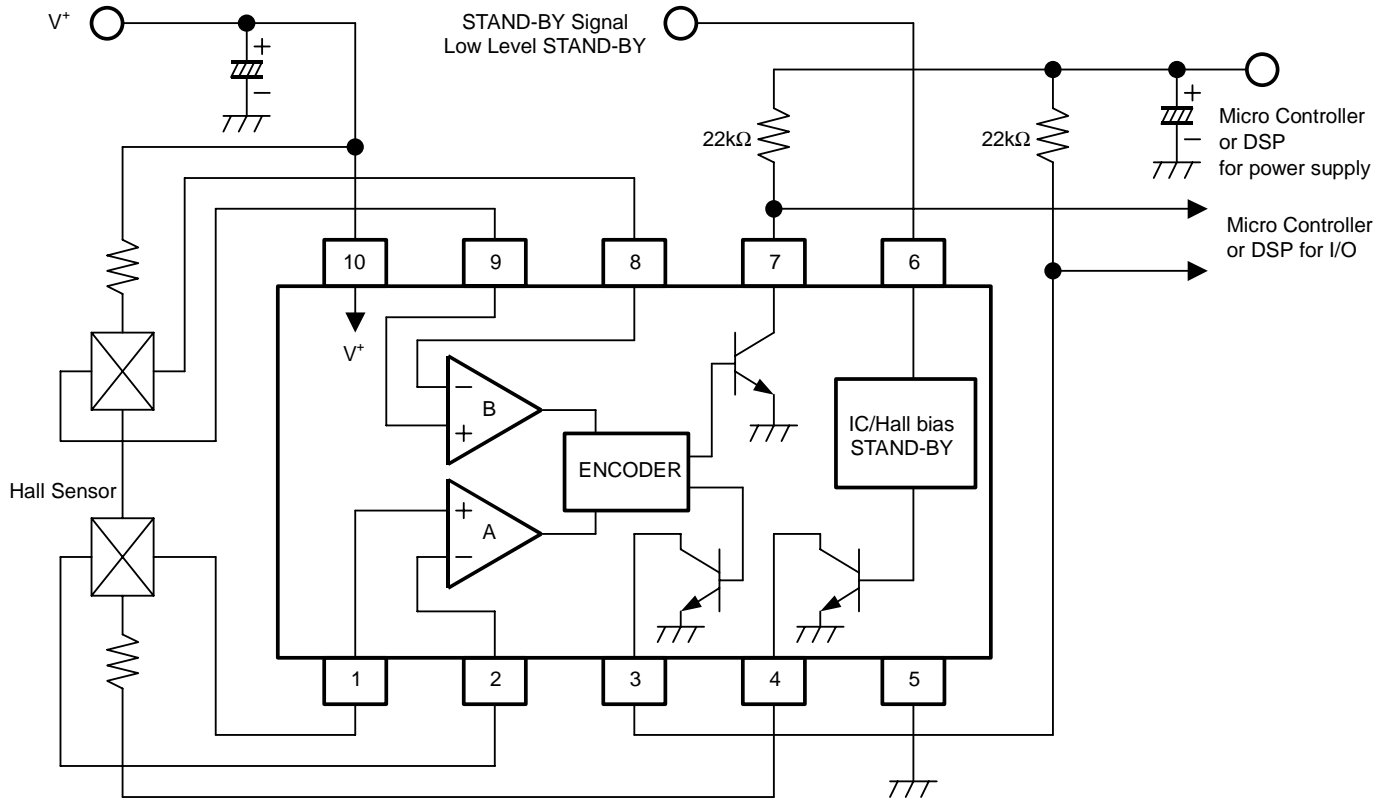
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Low Level Output Voltage (OUT1,OUT2 Pin)	V <sub>OL1</sub> V <sub>OL2</sub>	I <sub>SINK</sub> =5mA	–	0.2	0.5	V
Output Leak Current (OUT1,OUT2 Pin)	I <sub>LEAK(OUT)</sub>	V <sub>OUT</sub> =5V	–	–	5	μA

Stand-By Section (Low Level Stand-By)

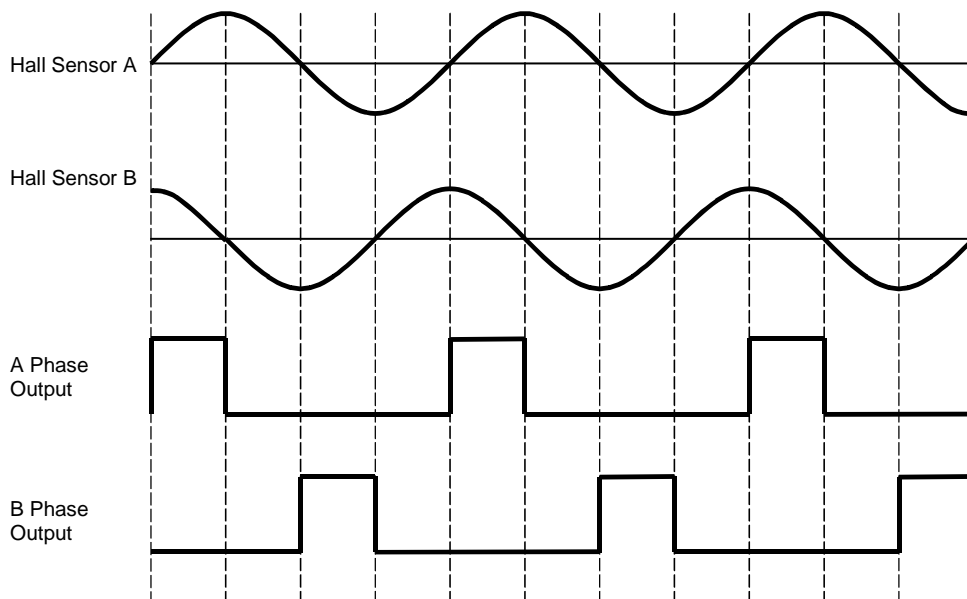
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Low Level Voltage	V <sub>IL(STBY)</sub>		–	–	0.4	V
Input High Level Voltage	V <sub>IH(STBY)</sub>		2.4	–	–	V
Input Bias Current	I <sub>STBY</sub>		–	1	20	μA

# NJM2650

## TYPICAL APPLICATIONS

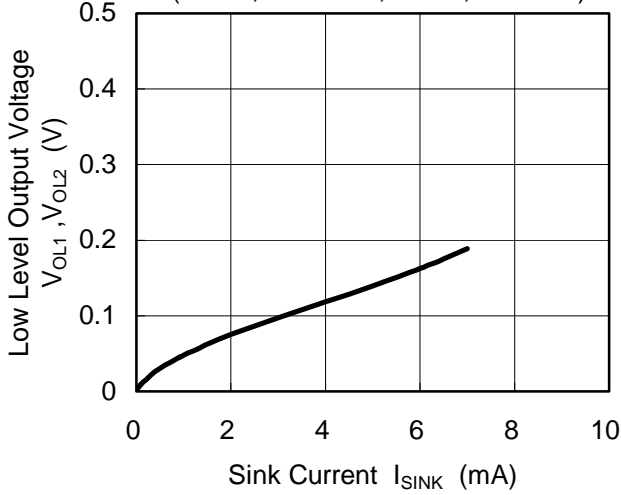


## TIMING CHART

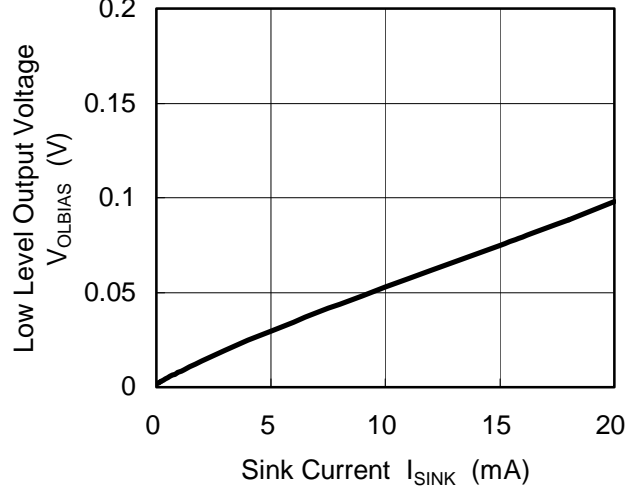


## ■ TYPICAL CHARACTERISTICS

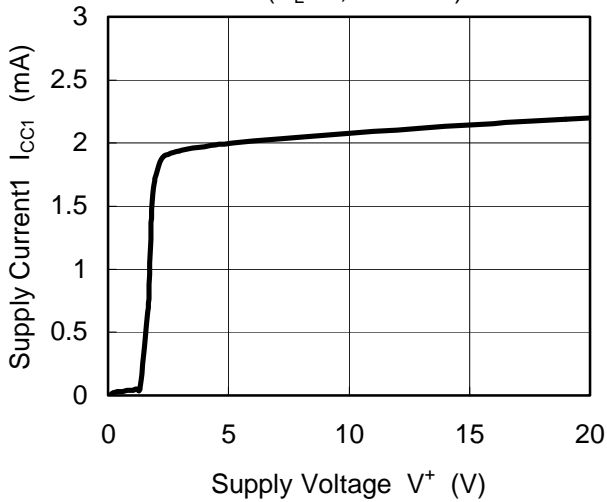
Low Level Output Voltage vs. Sink Current  
(OUT1,OUT2 Pin,  $V^+=5V, T_a=25^\circ C$ )



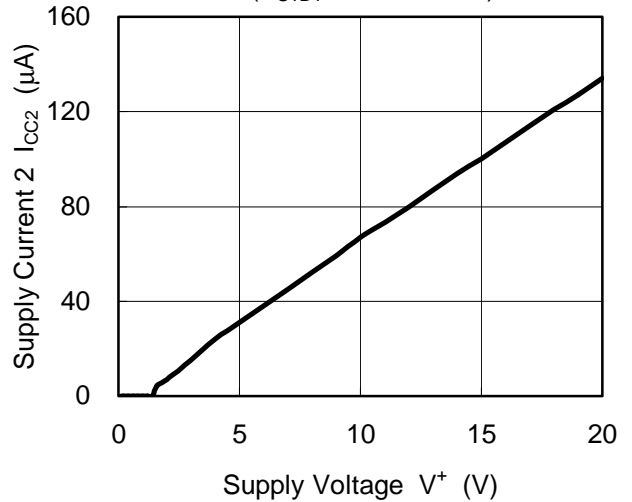
Low Level Output Voltage vs. Sink Current  
(HBIAS Pin,  $V^+=5V, T_a=25^\circ C$ )



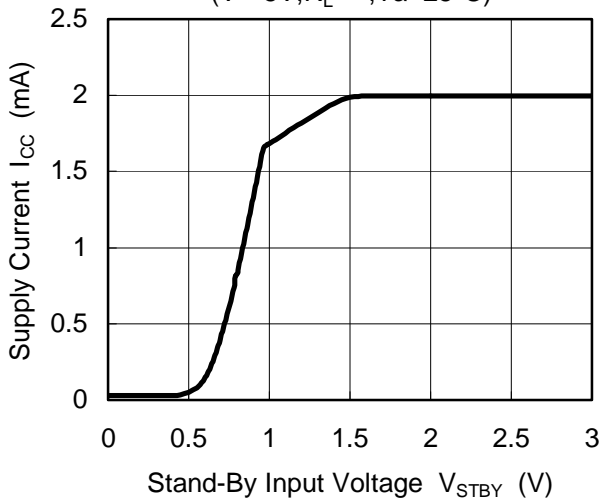
Supply Current 1 vs. Supply Voltage  
( $R_L=\infty, T_a=25^\circ C$ )



Supply Current 2 vs. Supply Voltage  
( $V_{STBY}=0V, T_a=25^\circ C$ )

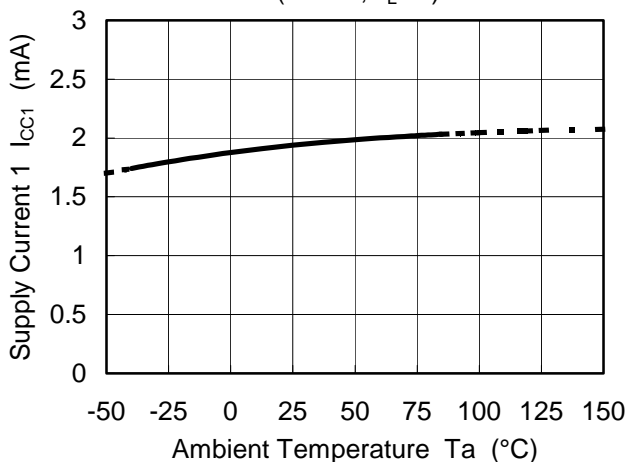


Supply Current vs. Stand-By Input Voltage  
( $V^+=5V, R_L=\infty, T_a=25^\circ C$ )

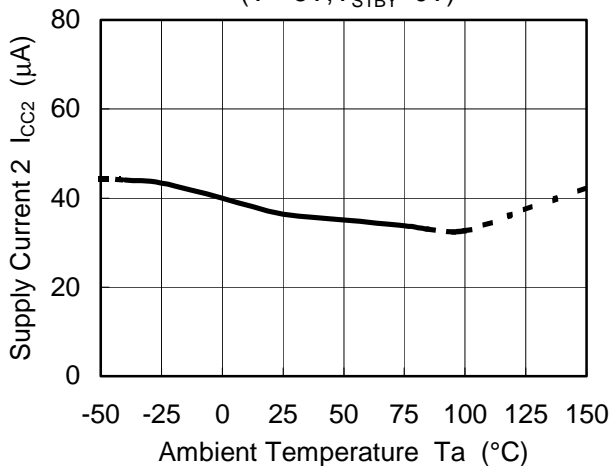


## TYPICAL CHARACTERISTICS

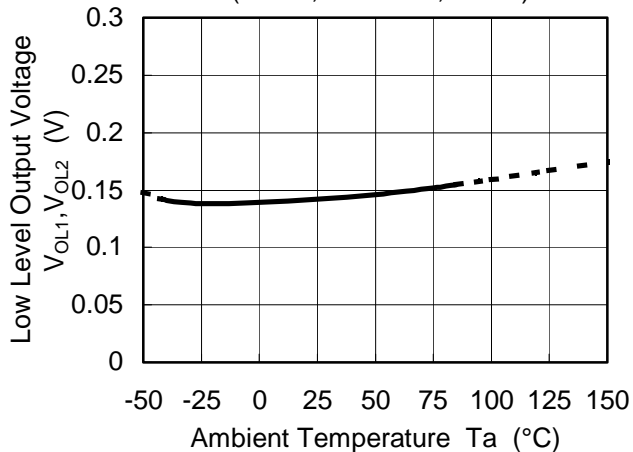
Supply Current 1 vs. Temperature  
( $V^+=5V, R_L=\infty$ )



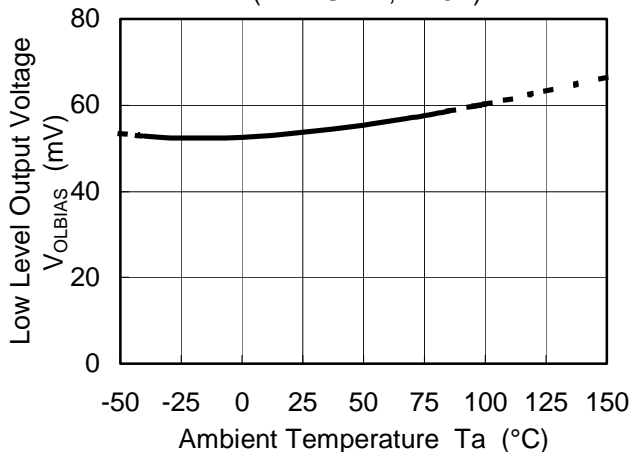
Supply Current 2 vs. Temperature  
( $V^+=5V, V_{STBY}=0V$ )



Low Level Output Voltage vs. Temperature  
(OUT1, OUT2 Pin,  $V^+=5V$ )



Low Level Output Voltage vs. Temperature  
(HBIAS Pin,  $V^+=5V$ )



[CAUTION]  
The specifications on this databook are only given for information, without any guarantee as regards either mistakes or omissions. The application circuits in this databook are described only to show representative usages of the product and not intended for the guarantee or permission of any right including the industrial rights.