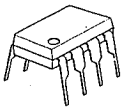
HIGH PERFORMANCE LOW-NOISE DUAL OPERATIONAL AMPLIFIER

■ GENERAL DESCRIPTION

The NJM5532 is a high performance dual low noise operational amplifier. Compared to the standard dual operational amplifiers, such as the NJM1458, it shows better noise performance, improved output drive capability, and considerably higher small-signal and power bandwidths.

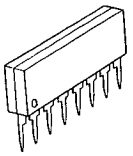
This makes the device especially suitable for application in high quality and professional audio equipment, instrumentation, control circuits, and telephone channel amplifiers. The op amp is internally compensated for gains equil to one If very low noise is of prime importance, version be used which has guaranteed NJM5532DD it is recommended that the noise specifications.

■ PACKAGE OUTLINE



NJM5532D

NJM5532M



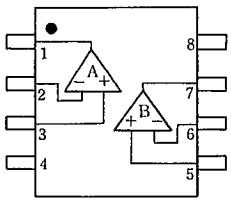
NJM5532L

■ FEATURES

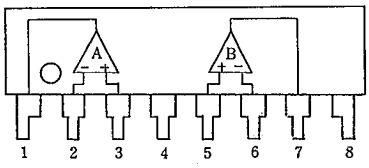
- Operating Voltage (±3V～±20V)
- Small Signal Bandwidth (10MHz typ.)
- Output Drive Capability (600Ω, 10Vrms typ.)
- Input Noise Voltage ($5nV/\sqrt{Hz}$ typ.)
- Power Bandwidth (140kHz typ.)
- Slew Rate (8V/μs typ.)
- Package Outline DIP8, DMP8, SIP8
- Bipolar Technology

4

■ PIN CONFIGURATION



NJM5532D
NJM5532M

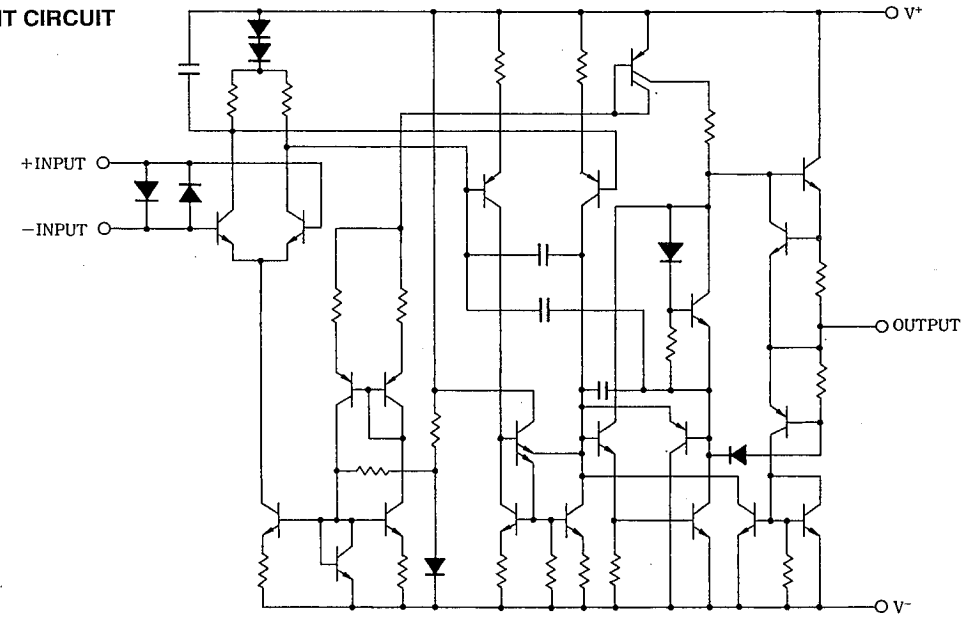


NJM5532L

PIN FUNCTION
1. A OUTPUT
2. A−INPUT
3. A+INPUT
4. V⁻
5. B+INPUT
6. B−INPUT
7. B OUTPUT
8. V⁺

■ EQUIVALENT CIRCUIT

(1/2 Shown)



■ ABSOLUTE MAXIMUM RATINGS

(Ta=25℃)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V^+/V^-	±22	V
Input Voltage	V_{IC}	V^+/V^-	(V)
Differential Input Voltage	V_{ID}	±0.5	V
Power Dissipation	P_D	(DIP8) 500	mW
		(DMP8) 600(note)	mW
		(SIP8) 800	mW
Operating Temperature Range	Topr	−20~+75	℃
Storage Temperature Range	Tstg	−40~+125	℃

(note) At on a ceramic PCB (10×20×0.635mm)

■ ELECTRICAL CHARACTERISTICS
DC ELECTRICAL CHARACTERISTICS

(V^+/V^-=±15V, Ta=25℃)

PARAMETER	SYMBOL	TEST CONDITION	5532			UNIT
			MIN.	TYP.	MAX.	
Input Offset Voltage	V_{IO}		—	0.5	4	mV
Input Offset Current	I_{IO}		—	10	150	nA
Input Bias Current	I_B		—	200	800	nA
Operating Current	I_{CC}		—	9	16	mA
Input Common Mode Voltage Range	V_{ICM}		±12	±13	—	V
Common Mode Rejection Ratio	CMR		70	100	—	dB
Supply Voltage Rejection Ratio	SVR		80	100	—	dB
Large Signal Voltage Gain 1	A_V 1	R_L≧2kΩ, V_O=±10V	88	100	—	dB
Large Signal Voltage Gain 2	A_V 2	R_L≧600Ω, V_O=±10V	83.5	94	—	dB
Maximum Output Voltage Swing 1	V_{OM1}	R_L≧600Ω	±12	±13	—	V
Maximum Output Voltage Swing 2	V_{OM2}	R_L≧600Ω, V^+/V^- =±18V	±15	±16	—	V
Input Resistance	R_{IN}		30	300	—	kΩ
Short Circuit Current	I_{OS}		—	38	—	mA

■ ELECTRICAL CHARACTERISTICS
AC ELECTRICAL CHARACTERISTICS

(V^+/V^-=±15V, Ta=25℃)

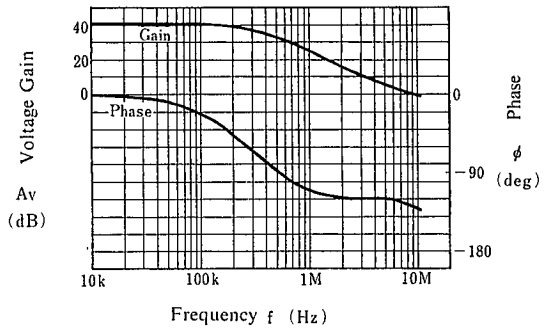
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Resistance	R_O	A_V=30dB, f=10kHz, R_L=600Ω	—	0.3	—	Ω
Overshoot		A_V=1, V_{IN}=100mV_{P-P}, C_L=100pF, R_L=600Ω	—	10	—	%
Gain	A_V	f=10kHz	—	67	—	dB
Slew Rate	SR		—	8	—	V/μS
Gain Bandwidth Product	GB	C_L 100pF, R_L=600Ω	—	10	—	MHz
Power Bandwidth	W_{PG}	V_O=±10V	—	140	—	kHz
Power Bandwidth	W_{PG}	V_O=±14V, R_L=600Ω, V^+/V^-=±18V	—	100	—	kHz
Equivalent Input Noise Voltage 1	e_n 1	f_O=30Hz	—	8	—	nV/√Hz
Equivalent Input Noise Voltage 2	e_n 2	f_O=1kHz	—	5	—	nV/√Hz
Equivalent Input Noise Current 1	i_n 1	f_O=30Hz	—	2.7	—	pA/√Hz
Equivalent Input Noise Current 2	i_n 2	f_o=1kHz	—	0.7	—	pA/√Hz
Channel Separation	CS	f=1kHz, R_S=5kΩ	—	110	—	dB

JRC's general selected products D rank are also prepared for the noise standard (R_S=2.2kΩ, RIAA, V_N=1.4μV Max.)

■ Typical Characteristics

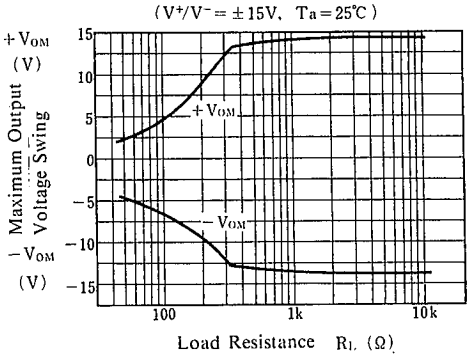
Voltage Gain, Phase vs. Frequency

(Ta=25℃)



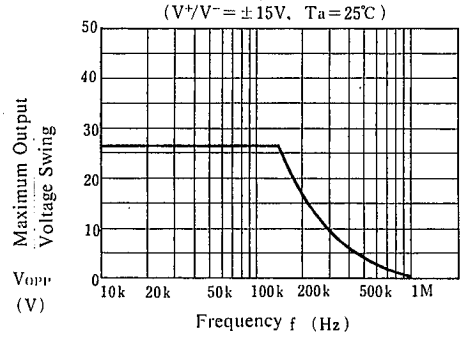
Maximum Output Voltage Swing vs.Load Resistance

(V+/V−=±15V, Ta=25℃)



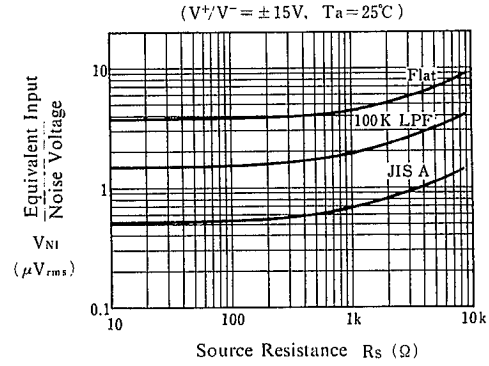
Maximum Output Voltage Swing vs. Frequency

(V+/V−=±15V, Ta=25℃)



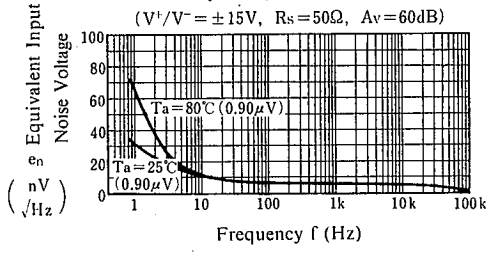
Equivalent Input Noise Voltage vs. Rs

(V+/V−=±15V, Ta=25℃)



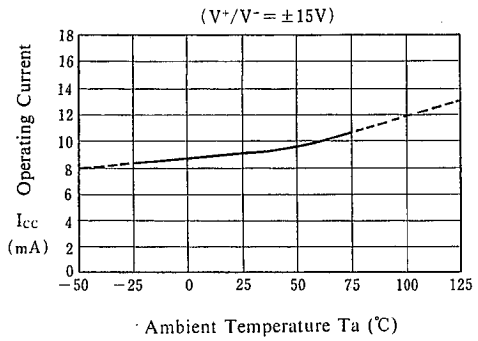
Equivalent Input Noise Voltage vs. Frequency

(V+/V−=±15V, Rs=50Ω, Av=60dB)

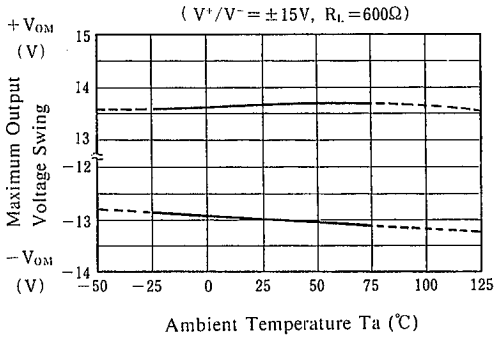


■ TYPICAL CHARACTERISTICS

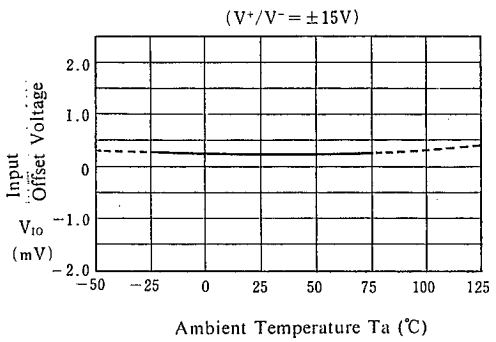
Operating Current vs. Temperature



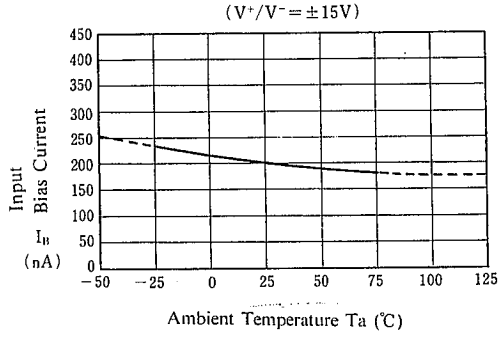
Maximum Output Voltage Swing vs. Temperature



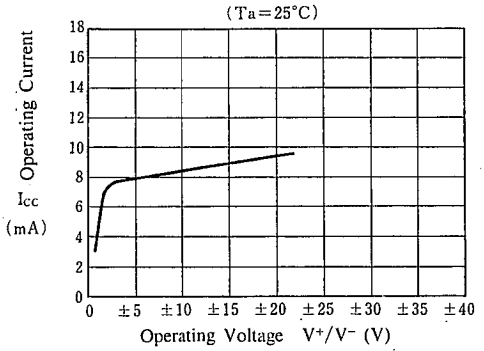
Input Offset Voltage vs. Temperature



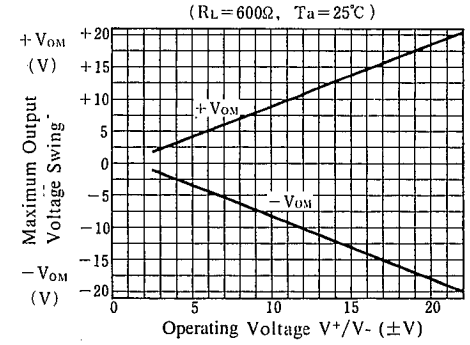
Input Bias Current vs. Temperature



Operating Current vs. Operating Voltage

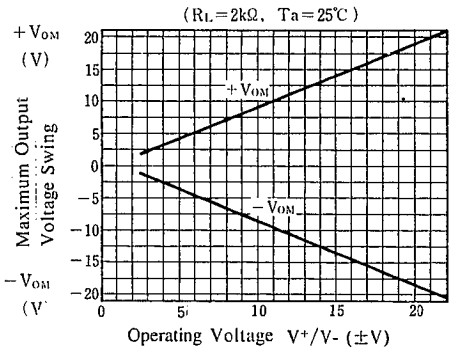


Maximum Output Voltage Swing vs. Operating Voltage

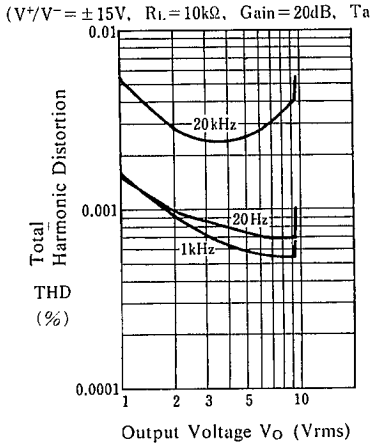


■ TYPICAL CHARACTERISTICS

Maximum Output Voltage Swing vs. Operating Voltage



Total Harmomic Distortion vs. Output Voltage



4

■ NOTICE

When used in voltage follower circuit, put a current limit resistor into non-inverting input terminal in order to avoid inside input diode destruction when the power supply is turned on. (ref. Fig. 1)

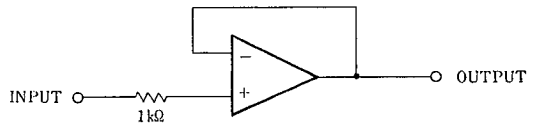


Fig. 1

MEMO