# Complementary NPN-PNP **Silicon Power Bipolar** Transistors

The NJW3281G and NJW1302G are power transistors for high power audio, disk head positioners and other linear applications. Features

- Exceptional Safe Operating Area
- NPN/PNP Gain Matching within 10% from 50 mA to 5 A
- Excellent Gain Linearity
- High BVCEO
- High Frequency
- These are Pb-Free Devices

#### **Benefits**

- Reliable Performance at Higher Powers
- Symmetrical Characteristics in Complementary Configurations
- Accurate Reproduction of Input Signal
- Greater Dynamic Range
- High Amplifier Bandwith

#### Applications

- High-End Consumer Audio Products
  - Home Amplifiers
  - Home Receivers
- Professional Audio Amplifiers
  - Theater and Stadium Sound Systems
  - Public Address Systems (PAs)

#### **MAXIMUM RATINGS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise noted)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V <sub>CEO</sub>	250	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	250	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	5.0	Vdc
Collector-Emitter Voltage - 1.5 V	V <sub>CEX</sub>	250	Vdc
Collector Current - Continuous - Peak (Note 1)	Ι <sub>C</sub>	15 30	Adc
Base Current - Continuous	Ι <sub>Β</sub>	1.6	Adc
Total Power Dissipation @ $T_C = 25^{\circ}C$ Derate Above $25^{\circ}C$	P <sub>D</sub>	200 1.43	W W/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 65 to +150	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.625	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	40	°C/W

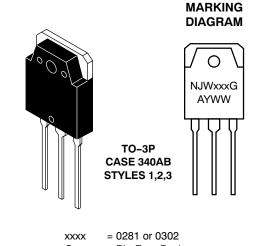
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability. 1. Pulse Test: Pulse Width = 5 ms, Duty Cycle < 10%.



## **ON Semiconductor®**

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## **15 AMPERES** COMPLEMENTARY SILICON POWER TRANSISTORS 250 VOLTS 200 WATTS



G	= Pb-Free Package
A	= Assembly Location
Y	= Year

ww = Work Week

#### **ORDERING INFORMATION**

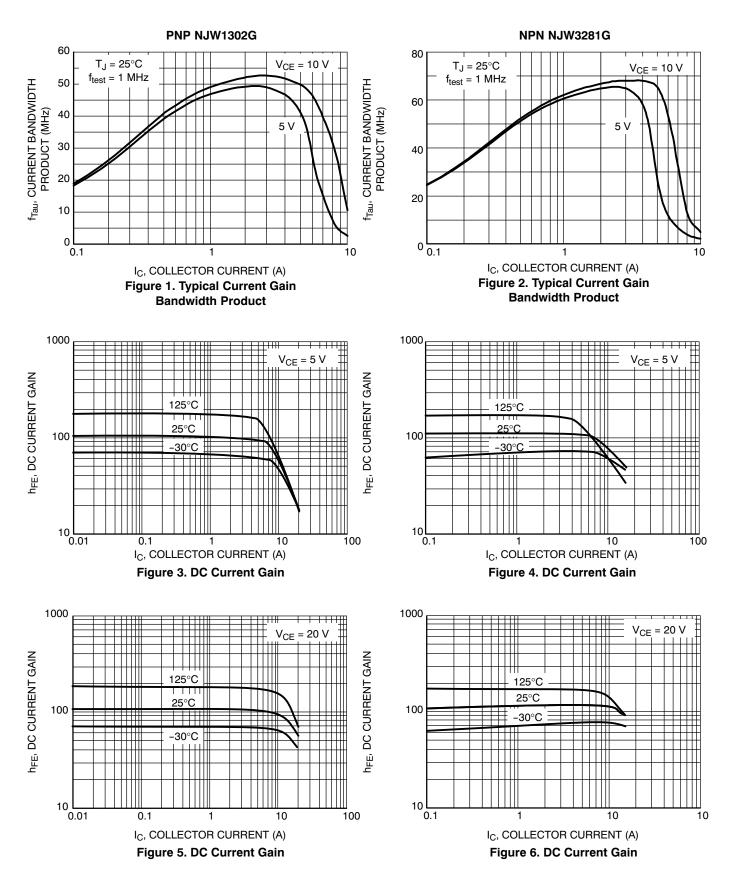
Device	Package	Shipping
NJW3281G	TO-3P (Pb-Free)	30 Units/Rail
NJW1302G	TO-3P (Pb-Free)	30 Units/Rail

Preferred devices are recommended choices for future use and best overall value.

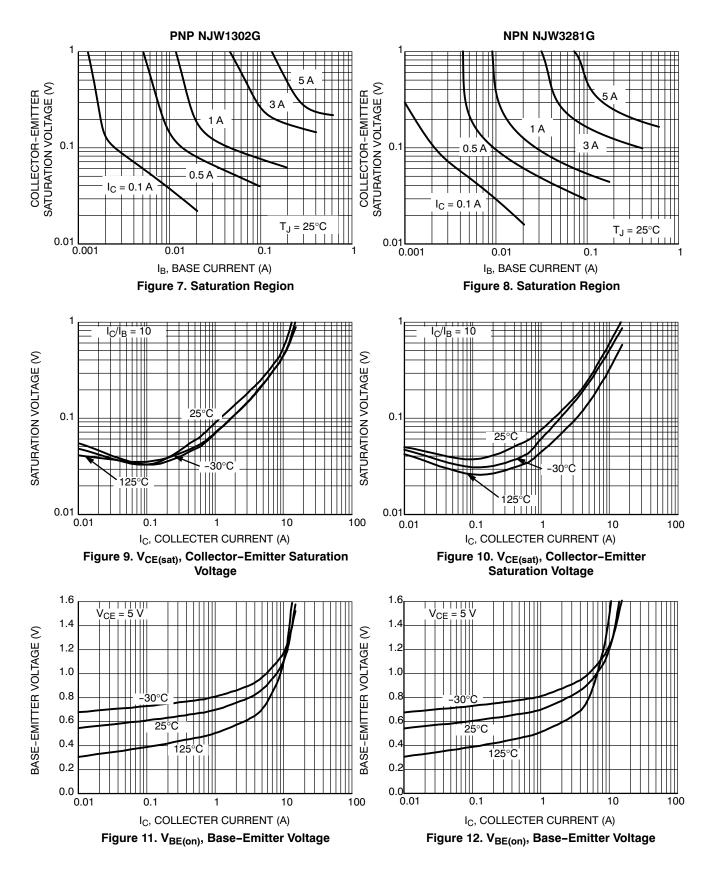
## **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted)

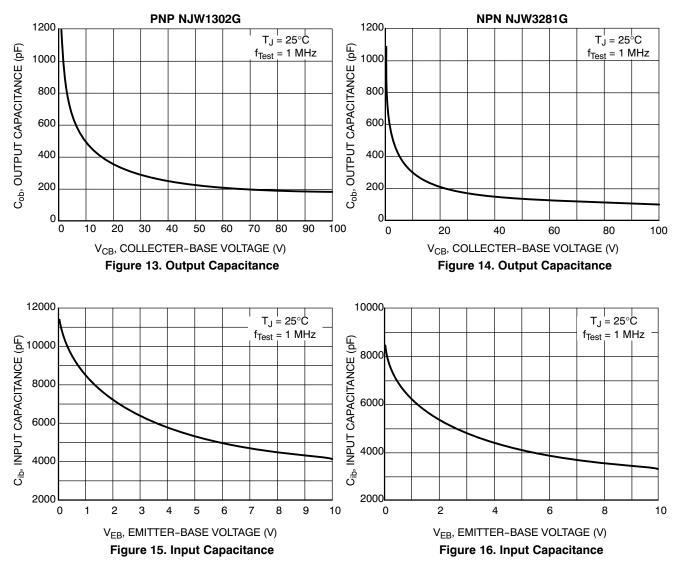
Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS		•			•
Collector-Emitter Sustaining Voltage ( $I_C = 100 \text{ mAdc}, I_B = 0$ )	V <sub>CEO(sus)</sub>	250	-	-	Vdc
Collector Cutoff Current ( $V_{CB} = 250 \text{ Vdc}, I_E = 0$ )	I <sub>CBO</sub>	_	_	50	μAdc
Emitter Cutoff Current ( $V_{EB} = 5 \text{ Vdc}, I_C = 0$ )	I <sub>EBO</sub>	_	-	5	μAdc
SECOND BREAKDOWN					
Second Breakdown Collector with Base Forward Biased $(V_{CE} = 50 \text{ Vdc}, t = 1 \text{ s (non-repetitive)})$	I <sub>S/b</sub>	4	-	-	Adc
ON CHARACTERISTICS		•		•	
$ \begin{array}{l} \text{DC Current Gain} \\ (I_C = 100 \text{ mAdc}, \text{ V}_{CE} = 5 \text{ Vdc}) \\ (I_C = 1 \text{ Adc}, \text{ V}_{CE} = 5 \text{ Vdc}) \\ (I_C = 3 \text{ Adc}, \text{ V}_{CE} = 5 \text{ Vdc}) \\ (I_C = 5 \text{ Adc}, \text{ V}_{CE} = 5 \text{ Vdc}) \\ (I_C = 8 \text{ Adc}, \text{ V}_{CE} = 5 \text{ Vdc}) \\ (I_C = 8 \text{ Adc}, \text{ V}_{CE} = 5 \text{ Vdc}) \end{array} $	h <sub>FE</sub>	75 75 75 60 45	- - - -	150 150 150 - -	-
Collector-Emitter Saturation Voltage $(I_C = 8 \text{ Adc}, I_B = 0.8 \text{ Adc})$	V <sub>CE(sat)</sub>	-	0.4	0.6	Vdc
Base-Emitter On Voltage (I <sub>C</sub> = 8 Adc, V <sub>CE</sub> = 5 Vdc)	V <sub>BE(on)</sub>	_	-	1.5	Vdc
DYNAMIC CHARACTERISTICS	·				
Current-Gain – Bandwidth Product ( $I_C = 1 \text{ Adc}, V_{CE} = 5 \text{ Vdc}, f_{test} = 1 \text{ MHz}$ )	f <sub>T</sub>	_	30	-	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f <sub>test</sub> = 1 MHz)	C <sub>ob</sub>	-	-	600	pF

## **TYPICAL CHARACTERISTICS**



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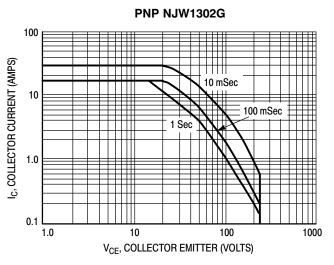


Figure 17. Active Region Safe Operating Area

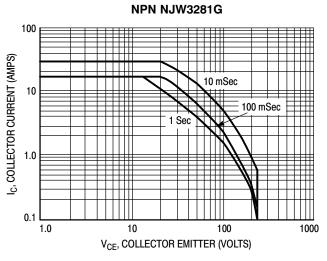


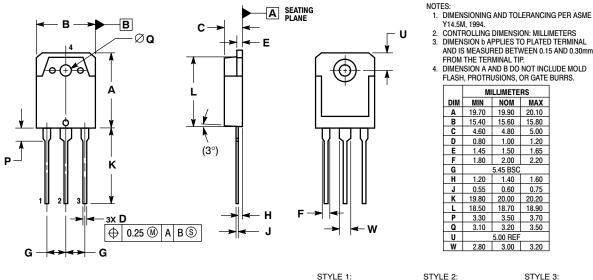
Figure 18. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor; average junction temperature and secondary breakdown. Safe operating area curves indicate  $I_C - V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figures 17 and 18 is based on  $T_{J(pk)} = 150^{\circ}$ C;  $T_{C}$  is variable depending on conditions. At high case temperatures, thermal limitations will reduce the power than can be handled to values less than the limitations imposed by second breakdown.

#### PACKAGE DIMENSIONS

TO-3P-3LD CASE 340AB-01 ISSUE A



	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	19.70	19.90	20.10	
В	15.40	15.60	15.80	
С	4.60	4.80	5.00	
D	0.80	1.00	1.20	
Е	1.45	1.50	1.65	
F	1.80	2.00	2.20	
G	5.45 BSC			
Н	1.20	1.40	1.60	
J	0.55	0.60	0.75	
Κ	19.80	20.00	20.20	
L	18.50	18.70	18.90	
Ρ	3.30	3.50	3.70	
Q	3.10	3.20	3.50	
U	5.00 REF			
W	2.80	3.00	3.20	

Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS

FROM THE TERMINAL TIP.

DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30mm

DIMENSION A AND B DO NOT INCLUDE MOLD

STYLE 1:		STYLE 2		STYLE 3	:
PIN 1. E	BASE	PIN 1.	ANODE	PIN 1.	GATE
2. (	COLLECTOR	2.	CATHODE	2.	DRAIN
3. E	EMITTER	3.	ANODE	3.	SOURCE
4. (	COLLECTOR	4.	CATHODE	4.	DRAIN

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