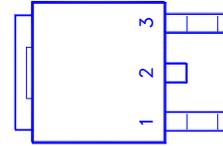
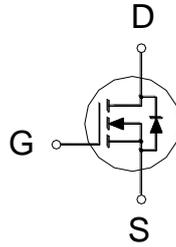


PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
150V	150m Ω	11A



1. GATE
2. DRAIN
3. SOURCE

ABSOLUTE MAXIMUM RATINGS ($T_A = 25\text{ }^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ²	$T_C = 25\text{ }^\circ\text{C}$	I_D	11	A
	$T_C = 100\text{ }^\circ\text{C}$		7	
Pulsed Drain Current ^{1,2}		I_{DM}	30	
Avalanche Current		I_{AS}	1.65	
Avalanche Energy	L = 1mH	E_{AS}	1.3	mJ
Power Dissipation	$T_C = 25\text{ }^\circ\text{C}$	P_D	44	W
	$T_C = 100\text{ }^\circ\text{C}$		17	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		2.8	$^\circ\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

²Limited only by maximum temperature allowed.

ELECTRICAL CHARACTERISTICS ($T_J = 25\text{ }^\circ\text{C}$, Unless Otherwise Noted)

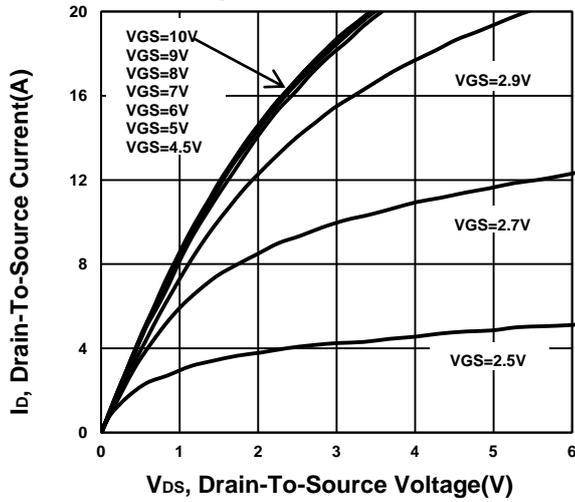
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	150			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.3	1.8	2.3	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 120V, V_{GS} = 0V$			1	μA
		$V_{DS} = 100V, V_{GS} = 0V, T_J = 70\text{ }^\circ\text{C}$			10	

Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 4.5V, I_D = 8A$		107	250	mΩ
		$V_{GS} = 10V, I_D = 8A$		105	150	
Forward Transconductance ¹	g_{fs}	$V_{DS} = 10V, I_D = 8A$		38		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = 25V, f = 1MHz$		1211		pF
Output Capacitance	C_{oss}			73		
Reverse Transfer Capacitance	C_{rss}			47		
Gate Resistance	R_g	$V_{GS} = 0V, V_{DS} = 0V, f = 1MHz$		0.68		Ω
Total Gate Charge ²	Q_g	$V_{DS} = 75V, V_{GS} = 10V, I_D = 8A$		32		nC
Gate-Source Charge ²	Q_{gs}			3.2		
Gate-Drain Charge ²	Q_{gd}			9.3		
Turn-On Delay Time ²	$t_{d(on)}$	$V_{DD} = 75V, I_D \cong 8A, V_{GS} = 10V, R_{GS} = 6\Omega$		13		nS
Rise Time ²	t_r			20		
Turn-Off Delay Time ²	$t_{d(off)}$			43		
Fall Time ²	t_f			52		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T_J = 25 °C)						
Continuous Current	I_S				8	A
Forward Voltage ¹	V_{SD}	$I_F = 8A, V_{GS} = 0V$			1.2	V
Reverse Recovery Time	t_{rr}	$I_F = 8A, di/dt=100A/\mu s$		43		nS
Reverse Recovery Charge	Q_{rr}			69		nC

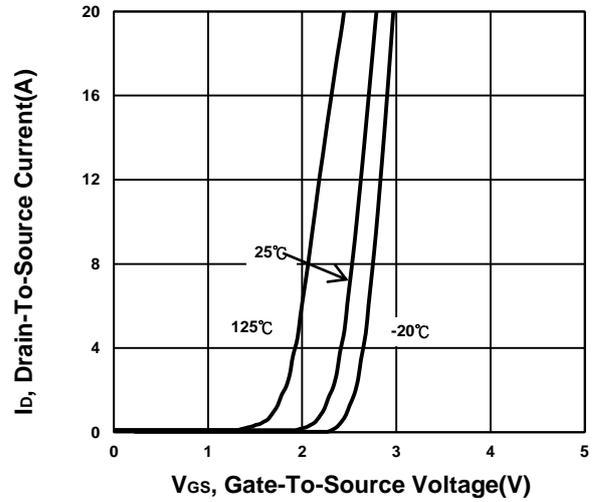
¹Pulse test : Pulse Width ≤ 300 μsec, Duty Cycle ≤ 2%.

²Independent of operating temperature.

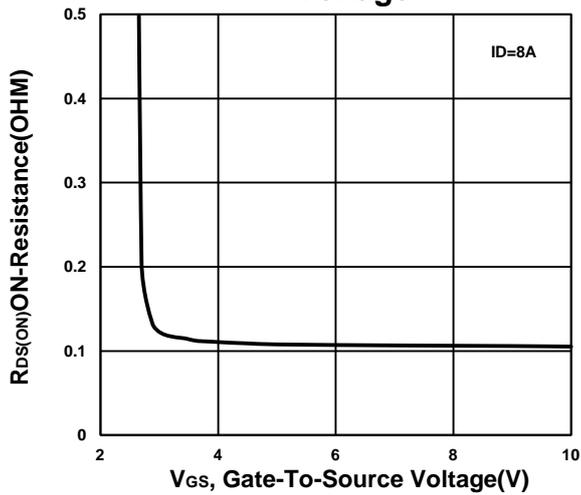
Output Characteristics



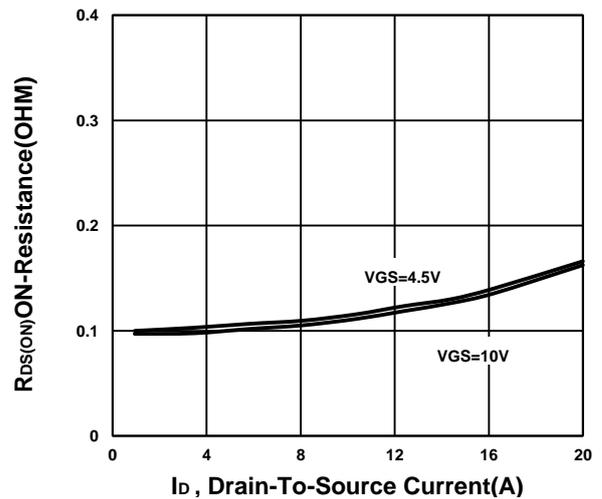
Transfer Characteristics



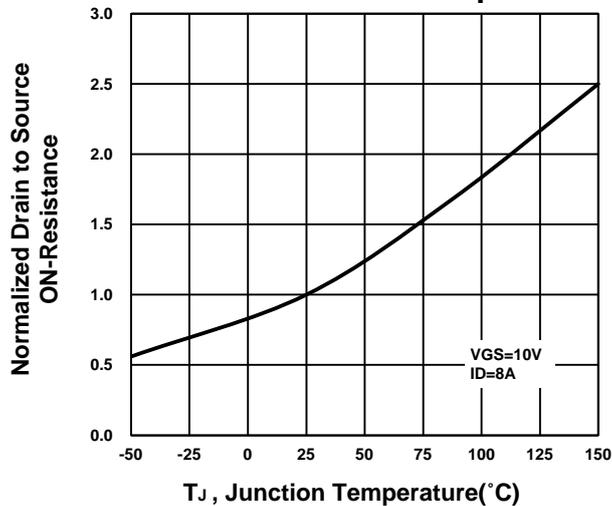
On-Resistance VS Gate-To-Source Voltage



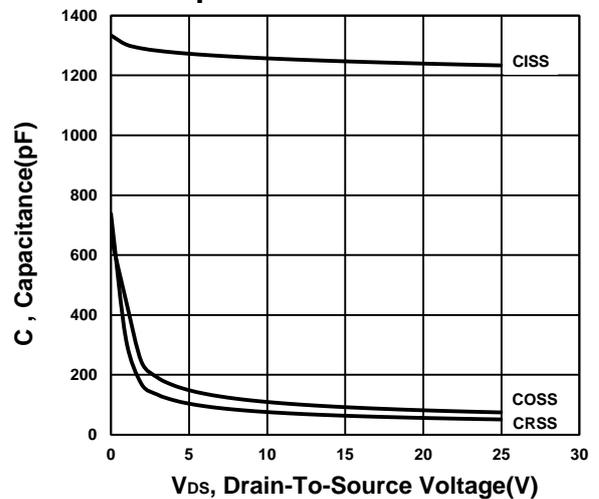
On-Resistance VS Drain Current



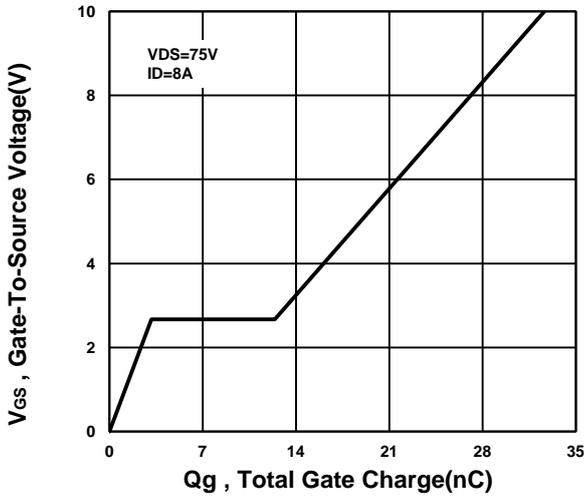
On-Resistance VS Temperature



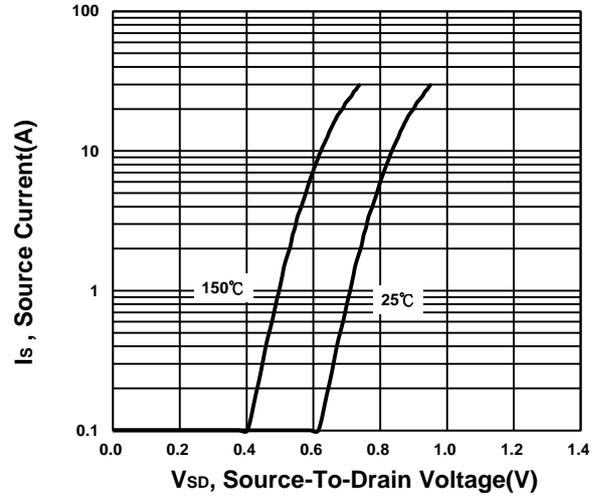
Capacitance Characteristic



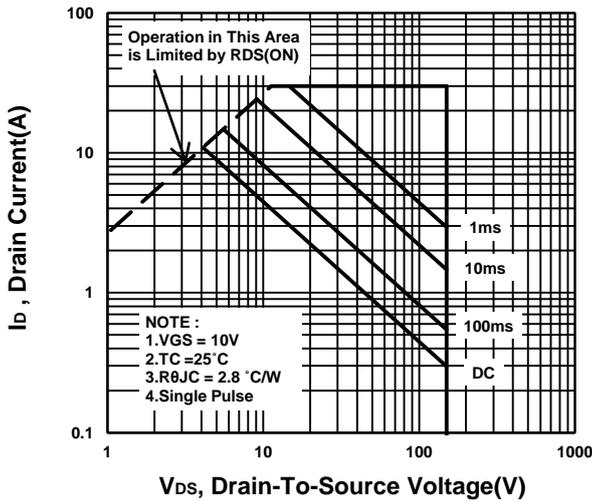
Gate charge Characteristics



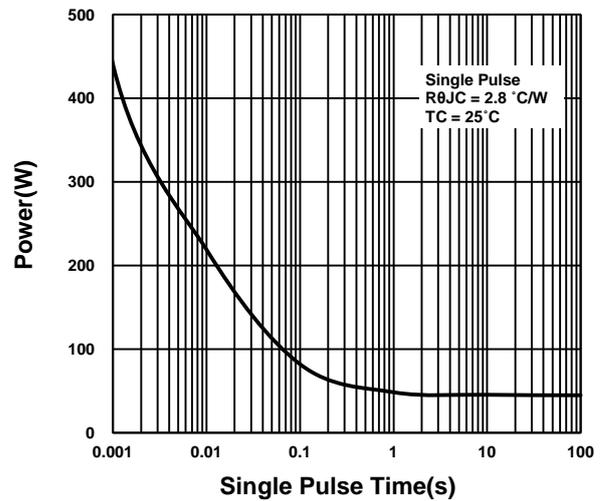
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

