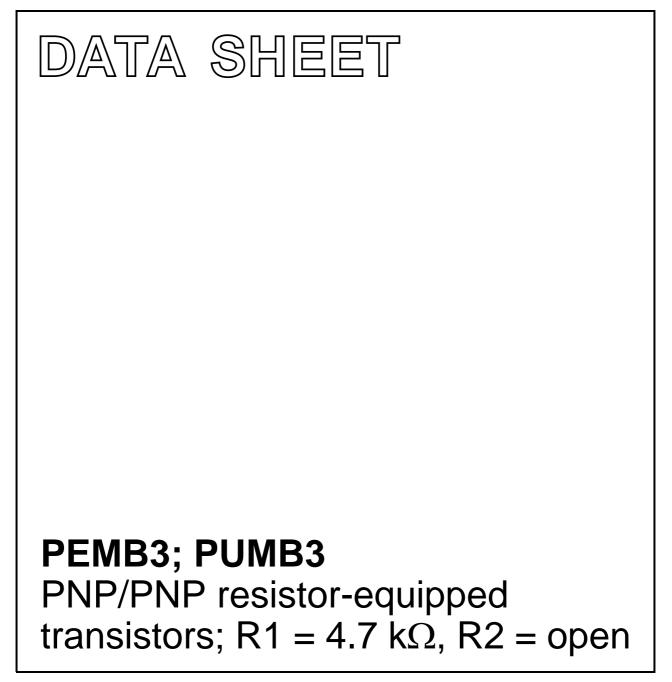
DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 2001 Sep 19 2003 Oct 15



PEMB3; PUMB3

FEATURES

- · Built-in bias resistors
- Simplified circuit design
- Reduction of component count
- Reduced pick and place costs.

APPLICATIONS

- Low current peripheral drivers
- Replacement of general purpose transistors in digital applications
- Control of IC inputs.

DESCRIPTION

PNP/PNP resistor-equipped transistors (see "Simplified outline, symbol and pinning" for package details).

PRODUCT OVERVIEW

TYPE NUMBER	PACKAGE		MARKING CODE	NPN/PNP	NPN/NPN COMPLEMENT	
ITFE NOWBER	PHILIPS			COMPLEMENT		
PEMB3	SOT666	_	Z3	PEMD6	PEMH7	
PUMB3	SOT363	SC-88	B5* ⁽¹⁾	PUMD6	PUMH7	

Note

1. * = p: Made in Hong Kong.

* = t: Made in Malaysia.

* = W: Made in China.

SIMPLIFIED OUTLINE, SYMBOL AND PINNING

TYPE NUMBER	SIMPLIFIED OUTLINE AND SYMBOL	PINNING		
ITFE NOWBER	SIMPLIFIED OUTLINE AND STMBOL	PIN	DESCRIPTION	
PEMB3 PUMB3	$\begin{array}{c c} \hline 6 & 5 & 4 \\ \hline & & & \\ \hline \hline & & & \\ \hline \hline \\ \hline \hline & & & \\ \hline \hline \hline \\ \hline \hline \hline \\ \hline \hline \hline \hline \hline \\ \hline \hline \hline \hline \hline \\ \hline \hline$	1 2 3 4 5 6	emitter TR1 base TR1 collector TR2 emitter TR2 base TR2 collector TR1	
	Top view MAM452			

QUICK REFERENCE DATA

SYMBOL	PARAMETER	TYP.	MAX.	UNIT
V _{CEO}	collector-emitter voltage	-	-50	V
lo	output current (DC)	_	-100	mA
TR1	PNP	-	-	-
TR2	PNP	_	-	-
R1	bias resistor	4.7	-	kΩ
R2	bias resistor	open	-	-

PEMB3; PUMB3

ORDERING INFORMATION

TYPE NUMBER	PACKAGE			
ITFE NOWBER	NAME	DESCRIPTION	VERSION	
PEMB3	_	plastic surface mounted package; 6 leads		
PUMB3	_	 plastic surface mounted package; 6 leads SO 		

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per transi	stor	·			
V _{CBO}	collector-base voltage	open emitter	_	-50	V
V _{CEO}	collector-emitter voltage	open base	-	-50	V
V _{EBO}	emitter-base voltage	open collector	-	-5	V
I _O	output current (DC)		-	-100	mA
I _{CM}	peak collector current		-	-100	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$			
	SOT363	note 1	_	200	mW
	SOT666	notes 1 and 2	-	200	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C
Per device	9			•	
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$			
	SOT363	note 1	-	300	mW
	SOT666	notes 1 and 2	_	300	mW

Notes

1. Device mounted on an FR4 printed-circuit board, single-sided copper, standard footprint.

2. Reflow soldering is the only recommended soldering method.

PEMB3; PUMB3

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
Per transist	or	·	·	
R _{th j-a}	thermal resistance from junction to ambient	$T_{amb} \le 25 \ ^{\circ}C$		
	SOT363	note 1	625	K/W
	SOT666	notes 1 and 2	625	K/W
Per device	•			
R _{th j-a}	thermal resistance from junction to ambient	$T_{amb} \le 25 \ ^{\circ}C$		
	SOT363	note 1	416	K/W
	SOT666	note 1	416	K/W

Notes

1. Device mounted on an FR4 printed-circuit board, single-sided copper, standard footprint.

2. Reflow soldering is the only recommended soldering method.

CHARACTERISTICS

 $T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified.

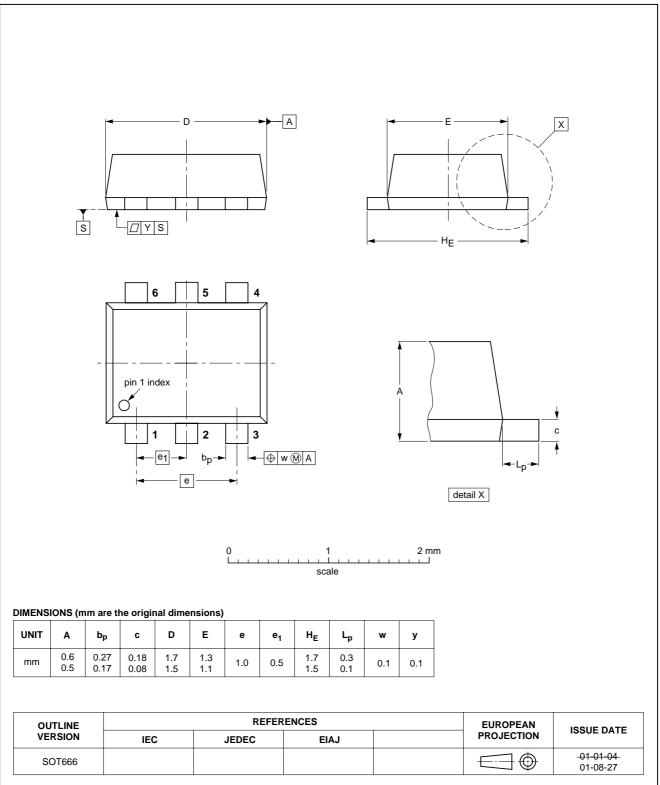
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	$V_{CB} = -50 \text{ V}; \text{ I}_{E} = 0$	-	-	-100	nA
I _{CEO}	collector-emitter cut-off current	$V_{CE} = -30 \text{ V}; I_B = 0$	-	-	-1	μA
		$V_{CE} = -30$ V; $I_B = 0$; $T_j = 150$ °C	-	-	-50	μA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -5 \text{ V}; \text{ I}_{C} = 0$	-	-	-100	nA
h _{FE}	DC current gain	$V_{CE} = -5 \text{ V}; I_C = -1 \text{ mA}$	200	_	_	
V _{CEsat}	saturation voltage	$I_{\rm C} = -5$ mA; $I_{\rm B} = -0.25$ mA	-	_	-100	mV
R1	input resistor		3.3	4.7	6.1	kΩ
C _c	collector capacitance	$I_E = i_e = 0; V_{CB} = -10 V;$ f = 1 MHz	-	_	3	pF

PEMB3; PUMB3

PNP/PNP resistor-equipped transistors; R1 = 4.7 k Ω , R2 = open

PACKAGE OUTLINES

Plastic surface mounted package; 6 leads



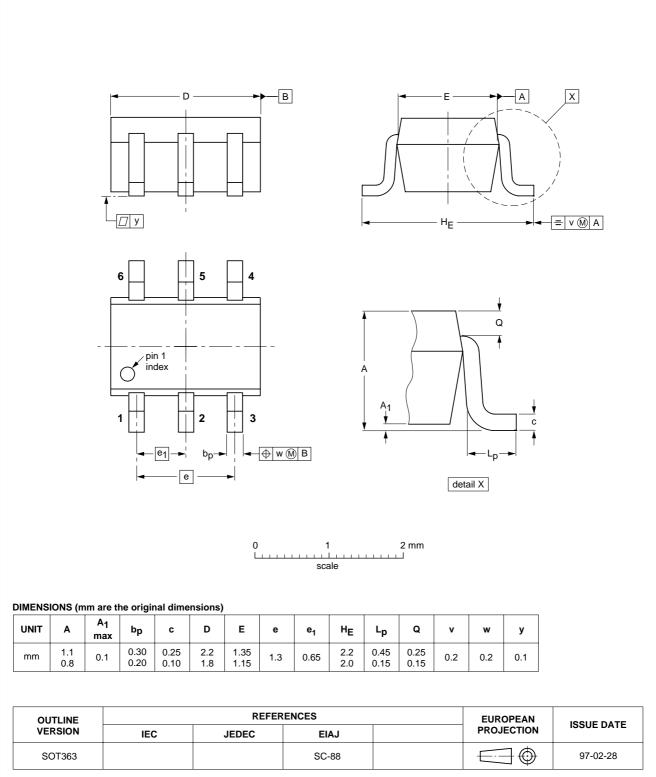
SOT666

SOT363

PNP/PNP resistor-equipped transistors; R1 = 4.7 k Ω , R2 = open

PEMB3; PUMB3

Plastic surface mounted package; 6 leads



PEMB3; PUMB3

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nxp.com.

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NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors. No changes were made to the content, except for the legal definitions and disclaimers.

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Printed in The Netherlands

R75/02/pp8

Date of release: 2003 Oct 15

Document order number: 9397 750 11792

