



PJESDA5V6-5G SERIES

QUAD ARRAY FOR ESD PROTECTION

FEATURES

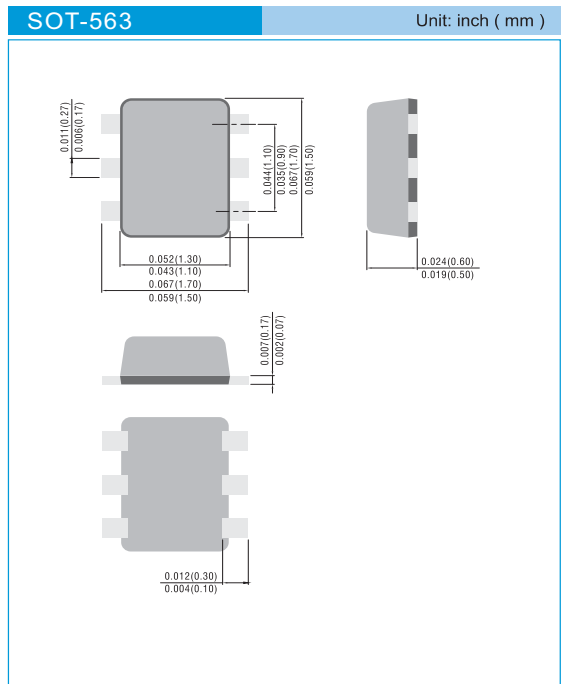
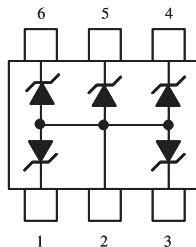
- Low Leakage <math> < 1 \mu A @ V_{RWM}</math>
- Breakdown Voltage : 5.6Volt-6.7Volt@1mA
- ESD Protection Meeting IEC61000-4-2-Level 4
- In compliance with EU RoHS 2002/95/EC directives

MECHANICAL DATA

Case : SOT-563, Plastic

Terminals : Solderable per MIL-STD-750, Method 2026

Approx. Weight : 0.003 gram



MAXIMUM RATINGS (T_A=25°C unless otherwise noted)

Parameter	Symbol	Limits	Unit
Peak Power Dissipation (8x20μs@T _A =25°C)(Note 1)	P _{PK}	40	W
Steady State Power-1Diode (Note 2)	P _D	300	mW
Thermal Resistance Junction to Ambient Above 25°C,Derate	R _{θJA}	370 2.7	°C/W mW/°C
Lead Solder Temperature (10 seconds duration)	T _L	260	°C
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

ELECTRICAL CHARACTERISTICS (T_A=25°C)

Part Number	Breakdown Voltage V _{BR} @1mA (Volts)			Leakage Current I _{RM} @V _{RM}		V _C Max@I _{PP}		Max Capacitance @0V Bias (1MHz)	Marking
	Min	Nom	Max	V _{RWM}	I _{RWM}	V _C	I _{PP}		
	V	V	V	V	μA	V	A	pF	
PJESDA5V6-5G	5.32	5.6	5.88	3.0	1	10	5.0	45	SB
PJESDA6V2-5G	5.89	6.2	6.51	4.3	1	11	4.5	40	SC
PJESDA6V8-5G	6.37	6.7	7.04	5.0	1	12	4.0	35	SD

1.Non-repetitive current per Figure 1.

2.Only 1 diode under power. For all 4 diodes under power, P_D will be 25%. Mounted on FR-4 board with min pad



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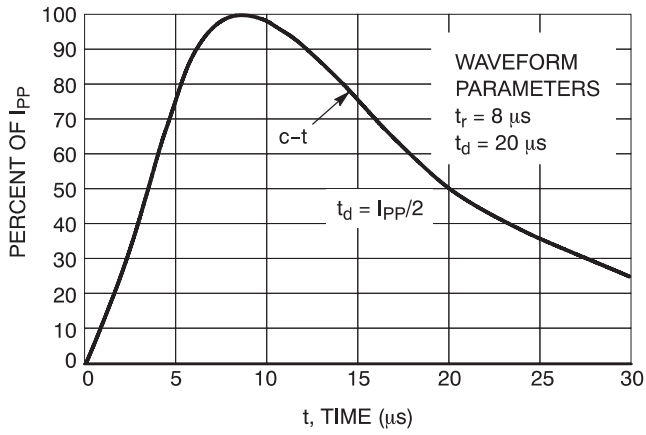


Figure 1. Pulse Waveform

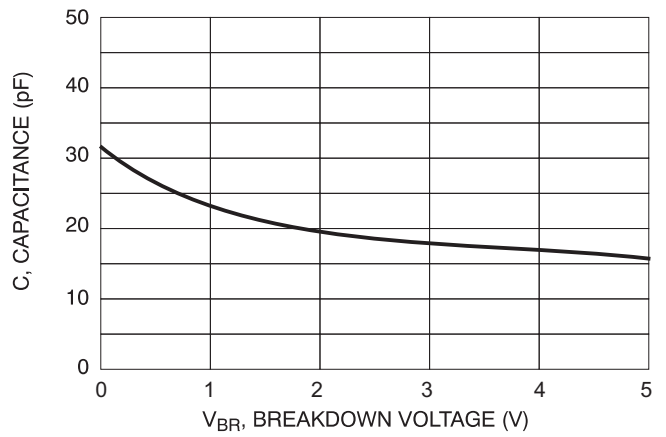


Figure 2. Typical Capacitance

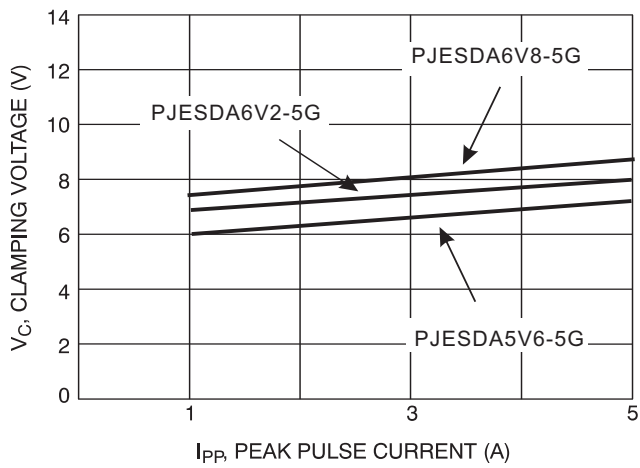
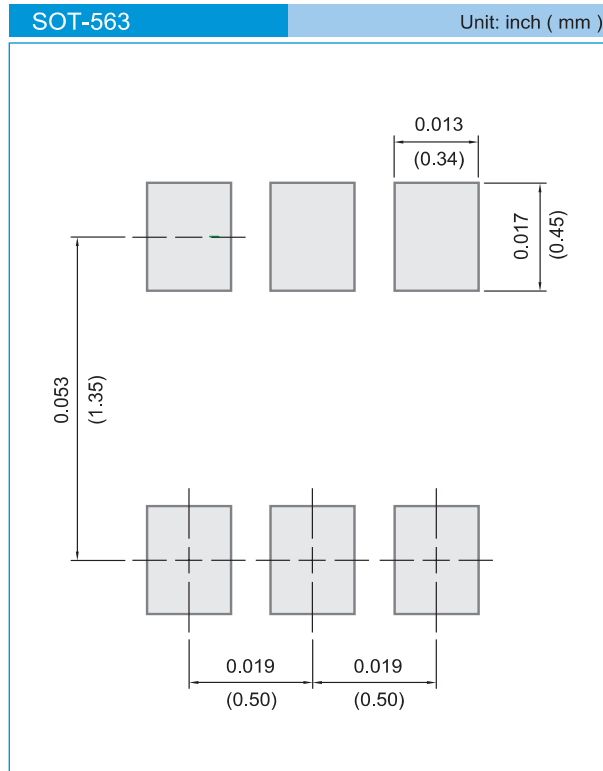


Figure 3. Clamping Voltage versus Peak Pulse Current



MOUNTING PAD LAYOUT



ORDER INFORMATION

- Packing information

T/R - 4K per 7" plastic Reel

T/R - 10K per 13" plastic Reel

LEGAL STATEMENT

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