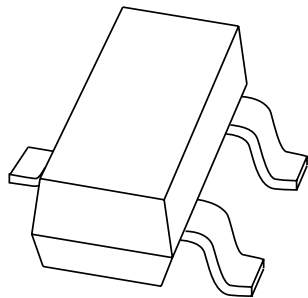


# DATA SHEET



**PMBD354**

Schottky barrier double diode

Product specification  
Supersedes data of 2002 Aug 06

2003 Mar 25

# Schottky barrier double diode

# PMBD354

### FEATURES

- Low forward voltage
- Small SMD package
- Low capacitance
- Matched capacitance.

### APPLICATIONS

- UHF mixer
- Sampling circuits
- Modulators
- Phase detection.

### DESCRIPTION

Planar Schottky barrier double diode in a SOT23 small plastic SMD package.

### MARKING

TYPE NUMBER	MARKING CODE <sup>(1)</sup>
PMBD354	*V8

### Note

- \* = p : Made in Hong Kong.  
 \* = t : Made in Malaysia.  
 \* = W : Made in China.

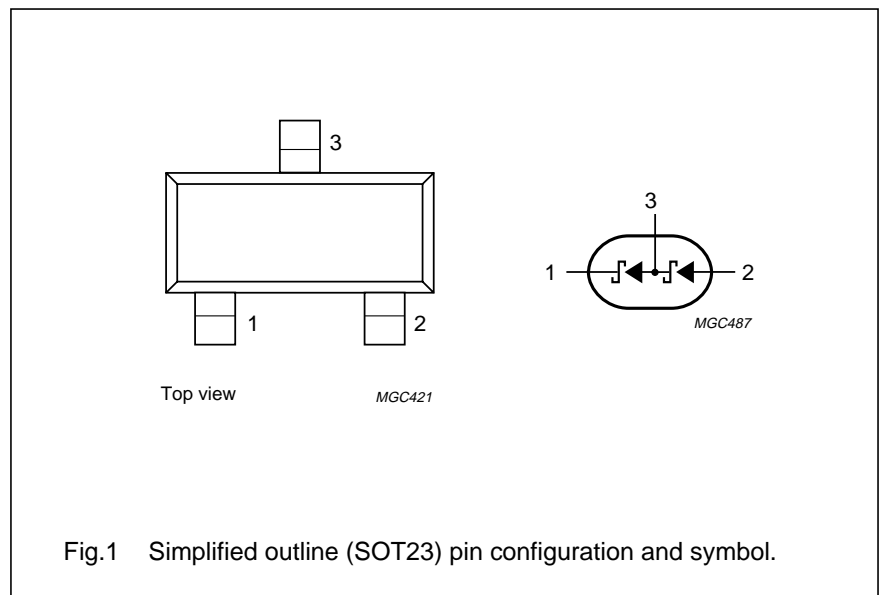
### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
<b>Per diode</b>				
$V_R$	continuous reverse voltage	–	4	V
$I_F$	continuous forward current	–	30	mA
$T_{stg}$	storage temperature	–65	+150	°C
$T_j$	junction temperature	–	100	°C

### PINNING

PIN	DESCRIPTION
1	cathode $k_1$
2	anode $a_2$
3	common connection $a_1, k_2$



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**ELECTRICAL CHARACTERISTICS** $T_{amb} = 25\text{ }^{\circ}\text{C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
<b>Per diode</b>				
$V_F$	forward voltage	see Fig.2 $I_F = 0.1\text{ mA}$ $I_F = 1\text{ mA}$ $I_F = 10\text{ mA}$	350 450 600	mV mV mV
$I_R$	reverse current	$V_R = 3\text{ V}$ ; note 1; see Fig.3	0.25	$\mu\text{A}$
$C_d$	diode capacitance	$f = 1\text{ MHz}$ ; $V_R = 0$ ; see Fig.4	1	pF
$\Delta C_d$	capacitance matching	$f = 1\text{ MHz}$ ; $V_R = 0$	0.1	pF

**Note**

1. Pulse test:  $t_p = 300\text{ }\mu\text{s}$ ;  $\delta = 0.02$ .

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	note 1	500	K/W

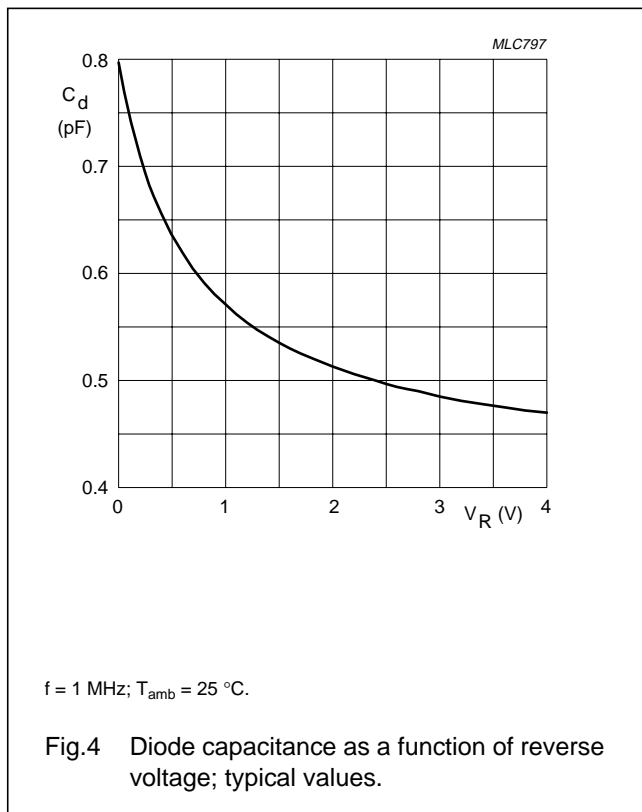
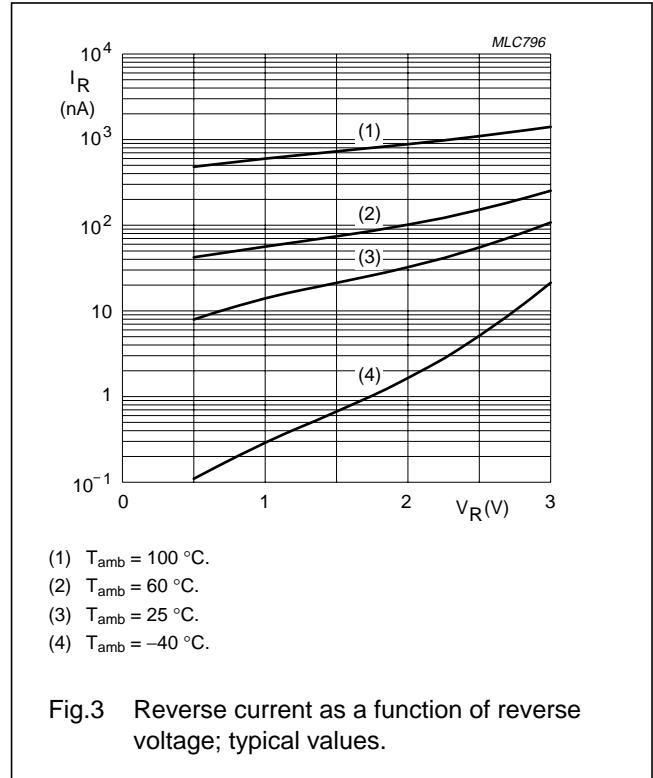
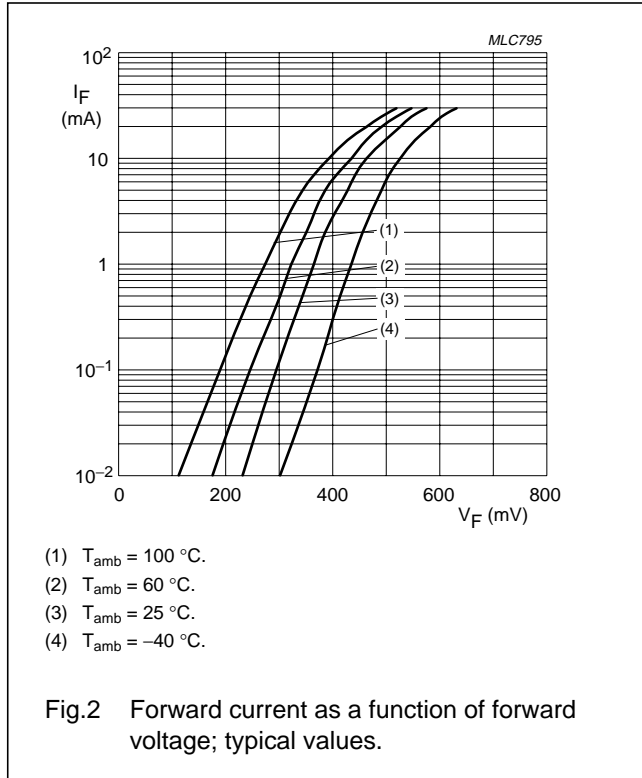
**Note**

1. Refer to SOT23 standard mounting conditions.

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GRAPHICAL DATA



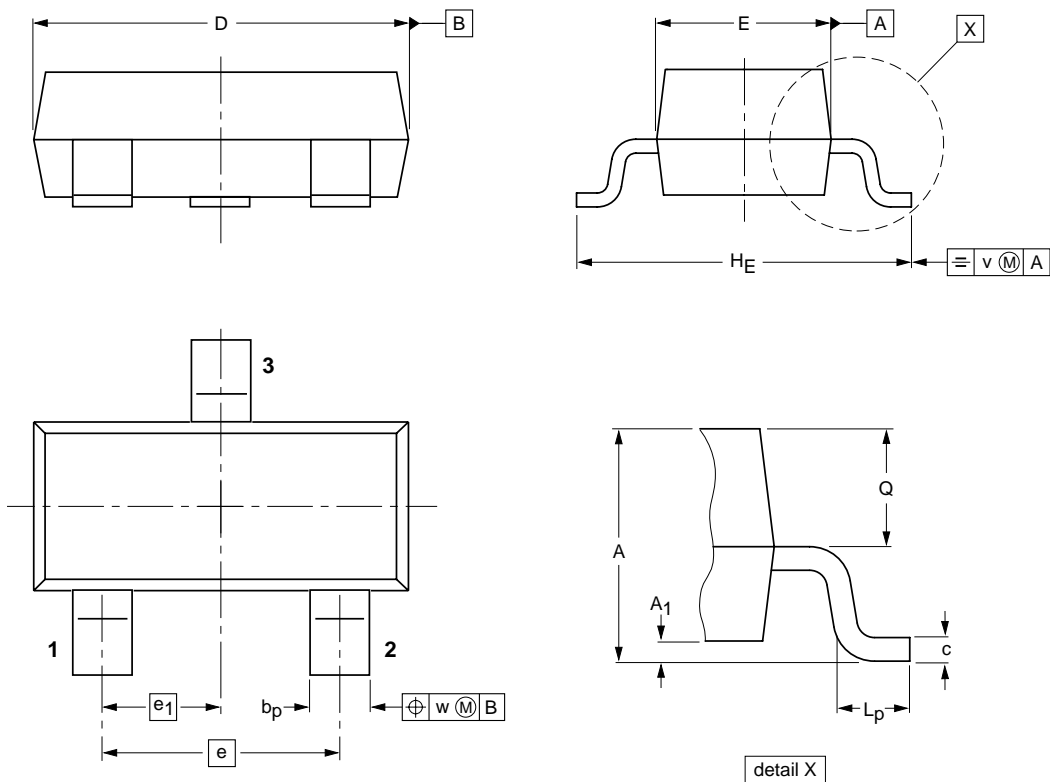
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PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



DIMENSIONS (mm are the original dimensions)

UNIT	A	A <sub>1</sub> max.	b <sub>p</sub>	c	D	E	e	e <sub>1</sub>	H <sub>E</sub>	L <sub>p</sub>	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES			EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ		
SOT23		TO-236AB			97-02-28 99-09-13

## Schottky barrier double diode

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## DATA SHEET STATUS

LEVEL	DATA SHEET STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)(3)</sup>	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
III	Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Relevant changes will be communicated via a Customer Product/Process Change Notification (CPCN).

## Notes

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3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

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**Limiting values definition** — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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**NOTES**

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