

ELECTRICAL CHARACTERISTICS (Ta = 0 to 70 °C)

CHARACTERISTIC		SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Diode	Forward Voltage	V _F		1.42	1.7	V	I _F =10 mA, Ta=25 °C
	Reverse Current	I _R		0.01	10	μA	V _R =5 V, Ta=25 °C
	Capacitance	C _t		60		pF	V=0, f=1.0 MHz
Detector	High Level Enable Current	I _{EH}		-0.8		mA	V _{CC} =5.5 V, V _{EH} =2.0 V
	Low Level Enable Current	I _{EL}		-1.2	-2.0	mA	V _{CC} =5.5 V, V _{EL} =0.5 V
Coupled	High Level Output Current	I _{OH}		30	250	μA	V _{CC} =V _O =5.5 V, I _F =250 μA, V _E =2.0 V
	Low Level Output Voltage	V _{OL}		0.4	0.6	V	V _{CC} =5.5 V, V _E =2.0 V, I _F =5 mA, I _O =13 mA
	Low Level Supply Current	I _{CCL}		10	18	mA	V _{CC} =5.5 V, V _E =2 V, I _F =10 mA
	High Level Supply Current	I _{CCH}		7	15	mA	V _{CC} =5.5 V, V _E =0.5 V, I _F =0 mA

ELECTRICAL CHARACTERISTICS (Ta = 25 °C)

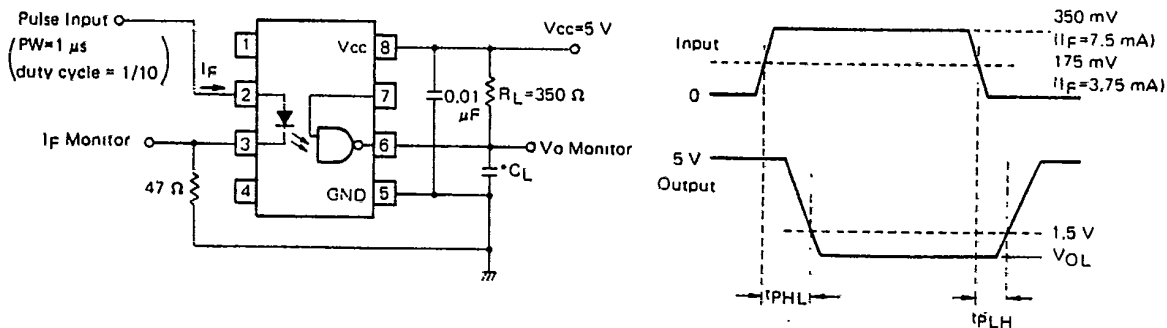
CHARACTERISTIC		SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Coupled	Current Transfer Ratio	CTR		600		%	I _F =5 mA, V _{CC} =5 V, R _L =100 Ω
	Isolation Resistance	R ₁₋₂		10 ¹²		Ω	V _{in-out} =1 kV
	Isolation Capacitance	C ₁₋₂		0.7		pF	V=0, f=1 MHz
	Propagation Delay Time to Low Output Level	t _{PHL} *2		50	75	ns	I _F =7.5 mA, V _{CC} =5 V, R _L =350 Ω, C _L =15 pF
	Propagation Delay Time to High Output Level	t _{PLH} *2		50	75	ns	
	Propagation Delay Time of Enable to Low Output Level	t _{EHL}		15		ns	I _F =7.5 mA, V _{CC} =5 V, R _L =350 Ω, V _{EH} =3 V, C _L =15 pF
	Propagation Delay Time of Enable to High Output Level	t _{ELH}		30		ns	

*1 Measuring Condition

DC voltage for 1 minute at Ta = 25 °C, RH = 60 %

Between input (pin No. 1, 2, 3, 4 Common) and Output (Pin No. 5, 6, 7, 8 Common)

*2 Measuring Circuit

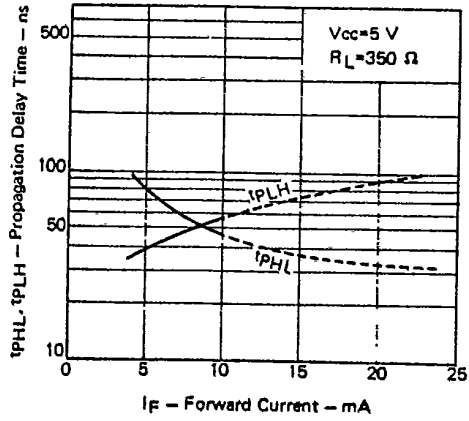


*C_L is approximately 15 pF, which includes probe and stray wiring capacitance.

PS2007B

T-41-89

PROPAGATION DELAY TIME vs. FORWARD CURRENT



PROPAGATION DELAY TIME vs. AMBIENT TEMPERATURE

