



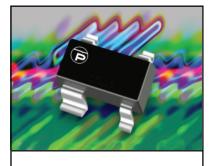
STEERING DIODE / TVS ARRAY COMBO

APPLICATIONS

- ✔ Ethernet 10/100 Base T
- ✓ FireWire
- ✓ Wireless Communications
- ✓ USB Interface

IEC COMPATIBILITY (EN61000-4)

- ✔ 61000-4-2 (ESD): Air 15kV, Contact 8kV
- ✔ 61000-4-4 (EFT): 40A 5/50ns
- ✓ 61000-4-5 (Surge): 24A, 8/20µs Level 2(Line-Gnd) & Level 3(Line-Line)



SOT-143

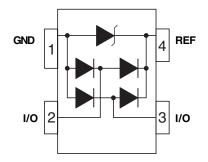
FEATURES

- ✓ 500 Watts Peak Power per Line (tp = 8/20µs)
- ✓ ESD Protection > 25 kilovolts
- ✓ Low Clamping Voltage
- ✓ Unidirectional Configuration
- ✓ PROTECTS 2 I/O PORTS & POWER SUPPLY
- ✓ LOW CAPACITANCE: 10pF

MECHANICAL CHARACTERISTICS

- ✔ Molded JEDEC SO-143
- ✔ Weight 35 milligrams (Approximate)
- ✔ Flammability rating UL 94V-0
- ✔ 8mm Tape and Reel Per EIA Standard 481
- ✓ Marking: Marking Code

PINCONFIGURATION



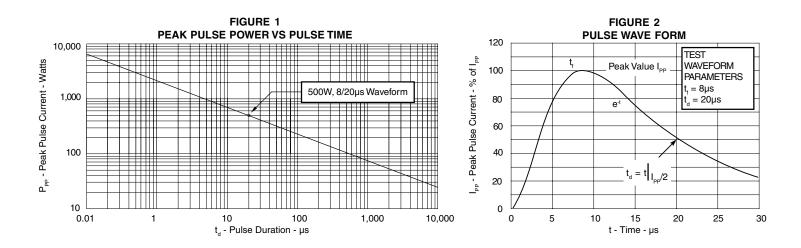


DEVICE CHARACTERISTICS

MAXIMUN RATINGS @ 25°C Unless Otherwise Specified						
PARAMETER	SYMBOL	VALUE	UNITS			
Peak Pulse Power ($t_p = 8/20\mu s$) - See Figure 1	P _{PP}	500	Watts			
Operating Temperature	TJ	-55°C to 150°C	So			
StorageTemperature	T _{STG}	-55°C to 150°C	°C			
Peak Forward Voltage - $I_F = 1A$, 8/20µs	V _F	1.5	Volts			

ELECTRICAL CHARACTERISTICS PER LINE @ 25°C Unless Otherwise Specified									
PART NUMBER			MINIMUM BREAKDOWN VOLTAGE	MAXIMUM CLAMPING VOLTAGE (See Fig. 2)	MAXIMUM CLAMPING VOLTAGE (See Fig. 2)	LAMPING LEAKAGE /OLTAGE CURRENT			
		V _{WM} VOLTS	@ 1A V _(BR) VOLTS	@ I _p = 1A V _c VOLTS	8/20µs V _c @ I _{pp} VOLTS	@V _{₩Μ} Ι _D μΑ	@0V, 1 MHz C _{J(SD)} pF		
PSR05	5A	5.0	6.0	9.8	20.0V @ 28.0A	5.0	10		

Note 1: As shown in Figure 5, REF 1 is connected to ground, REF 2 is connected to + V_{cc} and input applies to V_{cc} = 5V, V_{sign} = 30mV, F = 1MHz.





GRAPHS

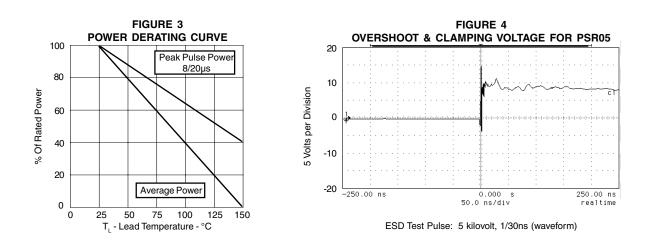
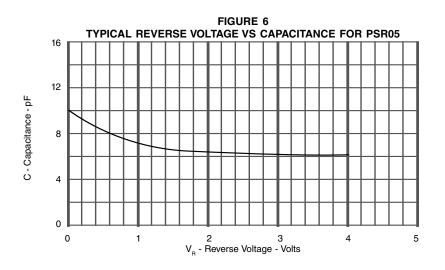


FIGURE 5 INPUT CAPACITANCE CIRCUIT REF2 V_{SIGN}





APPLICATION NOTE

The PSR05 is a low capacitance, bidirectional TVS array that is designed to protect I/O or high speed data lines from the damaging effects of ESD or EFT. This product series has a surge capability of 500 Watts P_{pp} per line for an 8/20µs waveform and offers ESD protection > 25kV.

COMMON-MODE CONFIGURATION (Figure 1)

Ideal for use in USB applications, two PSR05 devices provides up to two(2) lines of protection(per device) in a common-mode configuration as depicted in Figure 1.

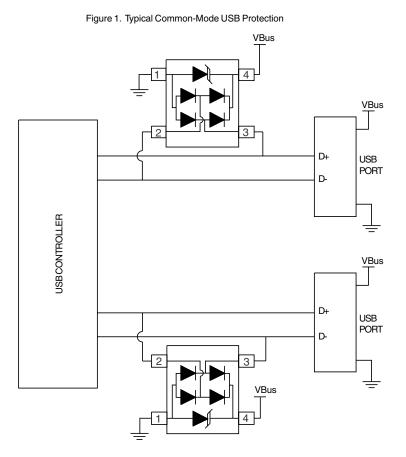
Circuit connectivity is as follows:

- ✓ Pins 2 and 3 are connected to the datalines.
- ✓ Pin 1 is connected to ground.
- ✓ Pin 4 is connected to the databus.

CIRCUIT BOARD LAYOUT RECOMMENDATIONS

Circuit board layout is critical for Electromagnetic Compatibility (EMC) protection. The following guidelines are recommended:

- ✓ The protection device should be placed near the input terminals or connectors, the device will divert the transient current immediately before it can be coupled into the nearby traces.
- ✓ The path length between the TVS device and the protected line should be minimized.
- All conductive loops including power and ground loops should be minimized.
- ✓ The transient current return path to ground should be kept as short as possible to reduce parasitic inductance.
- ✔ Ground planes should be used whenever possible. For multilayer PCBs, use ground vias.





PACKAGE OUTLINE & DIMENSIONS

PACKAGEOUTLINE				SOT-143					
	в с			P		EDIME			
					METERS INCHES				
				DIM	MIN	MAX	MIN	MAX	
				А	2.80	3.04	0.110	0.0120	
	— F		D	В	1.20	1.39	0.047	0.055	
				С	0.84	1.14	0.033	0.045	
				D	0.39	0.50	0.015	0.020	
	\int			F	0.79	0.93	0.031	0.037	
н−_		1	C	G	1.78	2.03	0.070	0.080	
<u> </u>				H	0.013	0.10	0.0005	0.004	
≜				J	0.08	0.15	0.003	0.006	
		н R		K	0.46	0.60	0.018	0.024	
	1 1			L	0.445	0.60	0.0175	0.024	
				R	0.72	0.83	0.028	0.033	
				S	2.11	2.48	0.083	0.098	
MOUNTINGPAD				NOTES					
	TYPICAL		◄ 1 ─ ►	 Dimensioning and tolerances per ANSIY14.5M, 1985. Controlling Dimension: Inches 					
DIM	Millimeters	Inches			lling Dimension sions are exclus		ash and meta	l burrs.	
1	2.85	0.112						. burrer	
2	2.00	0.079	<u> </u>	TAPE&R	EEL ORDERING	GNOMENCLA	TURE		
3	1.80 1.90	0.071 0.075		1. Surfac with El	e mount produc	ct is taped and	reeled in acc	cordance	
5	1.05	0.041	4 - 3 - 5 6	al - 3 000 niac	pieces per 8mm tape,				
6 7	2.75 1.20	0.108 0.047		i.e., PS	SR05-T7.		•	• •	
8	0.80	0.047			T13 = 13 Inch F	Reel - 10,00 pi	eces per 8mr	n tape,	
9	0.85	0.033		i.e., <i>PS</i>	SR05-T13.				
10 11	0.85 0.85	0.033 0.033		0		olono, Davi	1 11/01	00011	
				Outli	ne & Dimen	SIONS: HEV	1 - 11/01,	110011	

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