UTCPUMT1 PNP EPITAXIAL SILICON TRANSISTOR

PNP GENERAL PURPOSE **DUAL TRANSISTOR**

DESCRIPTION

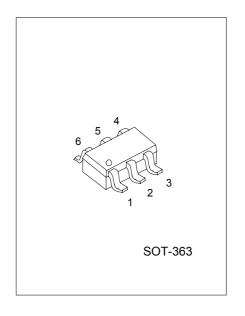
Two independently operating PNP transistors.

FEATURES

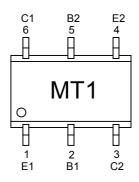
- *Low current (max.100mA)
- *Low voltage (max.40V)
- ${}^{\star}\text{Reduces}$ number of components and board space.
- * Complement to PUMX1.

APPLICATIONS

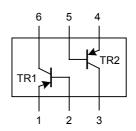
*General purpose switching and amplification.



PIN CONFIGURE/MARKING CODE



EQUIVALENT CIRCUITS



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The following characteristics apply to both Tr1 and Tr2

ABSOLUTE MAXIMUM RATINGS (Ta=25℃)

PARAMETER	SYMBOL	RATING	UNIT
Collector-base voltage	Vсво	-50	V
Collector-emitter voltage	VCEO	-40	V
Emitter-base voltage	VEBO	-5	V
Collector current (DC)	lc	-100	mA
Peak collector current	Ісм	-200	mA
Peak base current	Івм	-200	mA
Collector Power Dissipation (total)	Pc	300	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-65~+150	$^{\circ}$

ELECTRICAL CHARACTERISTICS (Ta=25°C)

PARAMETER	SYMBOL	CONDITION	MIN	MAX	UNIT
Collector cut-off current	ICRO	IE=0, VcB=-30V		-100	nA
		IE=0, VcB=-30V, Tj=150℃		-10	μА
Emitter cut-off current	ІЕВО	V _{EB} =-4V, I _C =0		-100	nA
DC current gain	hfe	Ic=-1mA, VcE=-6V	120		
Collector-emitter saturation voltage	VCE(sat)	Ic=-50mA, IB=-5mA (note 1)		-200	mV
Collector capacitance	Сс	IE=ie=0, VcB=-12V, f=1MHz		2.2	pF
Transition frequency	fr	Ic=-2mA, Vc==-12V, f=100MHz	100		MHz

Note:1. Pulse test: $tp \le 300 \mu s$, $\delta \le 0.02$

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