

RF2367

Absolute Maximum Ratings

Parameter	Rating	Unit
Supply Voltage	-0.5 to +8.0	V _{DC}
Input RF Level	+10	dBm
Storage Temperature	-40 to +150	°C



Caution! ESD sensitive device.

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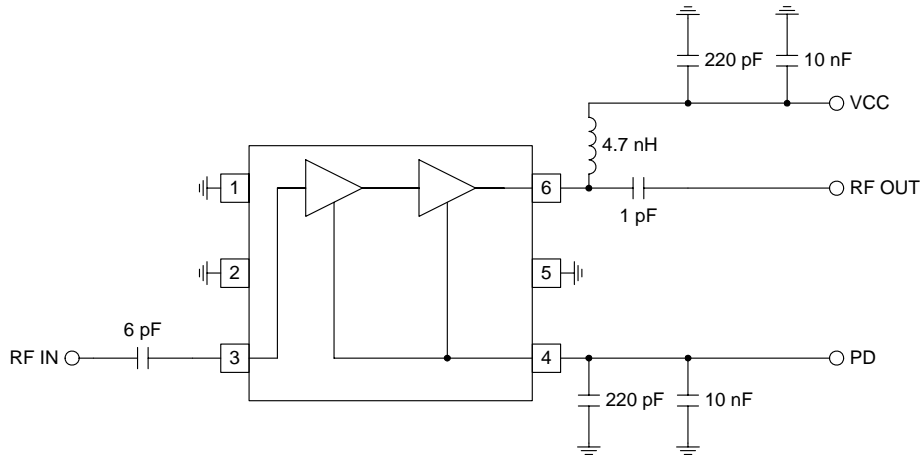
Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Operating Range					
Overall Frequency Range		150 to 2500		MHz	
Supply Voltage (V _{CC})	2.5		6.0	V	
Power Down Voltage (V _{PD})	2.7		2.9	V	For normal operation
Total Current Consumption	24	37	45	mA	For power down operation
Operating Ambient Temperature	-40		+85	°C	V _{CC} =3.0V, V _{PD} =2.8V
Input Impedance		50		Ω	V _{CC} =3.0V, V _{PD} <0.9V
Output Impedance		50		Ω	
1880MHz Performance					All parameters measured from evaluation board with T = 25°C, RF = 1880MHz, V _{CC} =3.0V, V _{PD} =2.8V
Gain	20	21.5	23	dB	
Output IP3	+20	+24		dBm	
Noise Figure		2.2	2.5	dB	
Reverse Isolation	32	34		dB	
Input VSWR		1.9:1	2:1		
Output VSWR		1.5:1	2:1		Using External LC network used on Evaluation Board
Output P _{1dB}	+13	+14	+15	dBm	

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Pin	Function	Description	Interface Schematic
1	GND	Ground connection. Keep traces physically short and connect immediately to ground plane for best performance.	
2	GND	Same as pin 1.	
3	RF IN	RF input pin. This pin is DC coupled and internally matched to a <2:1 VSWR at 1880MHz.	
4	PD	Power Down for the IC. $V_{PD} = 2.8V \pm 0.1V$ turns on the Part. $V_{PD} < 0.9V$ turns off the Part. Lower threshold for device operation is approximately 1.2V. External RF bypassing is required. The trace length between the pin and the bypass capacitors should be minimized. The ground side of the bypass capacitors should connect immediately to ground plane. Nominal current for this pin for $V_{PD} = 2.8V$ is 8mA typical.	See pin 3.
5	GND	Same as pin 1.	
6	RF OUT	Amplifier Output pin. This pin is an open-collector output. It must be biased to either V_{CC} or pin 4 through a choke or matching inductor. This pin is typically externally matched to 50Ω with a shunt bias/matching inductor and series blocking/matching capacitor. Refer to application/evaluation board schematics.	

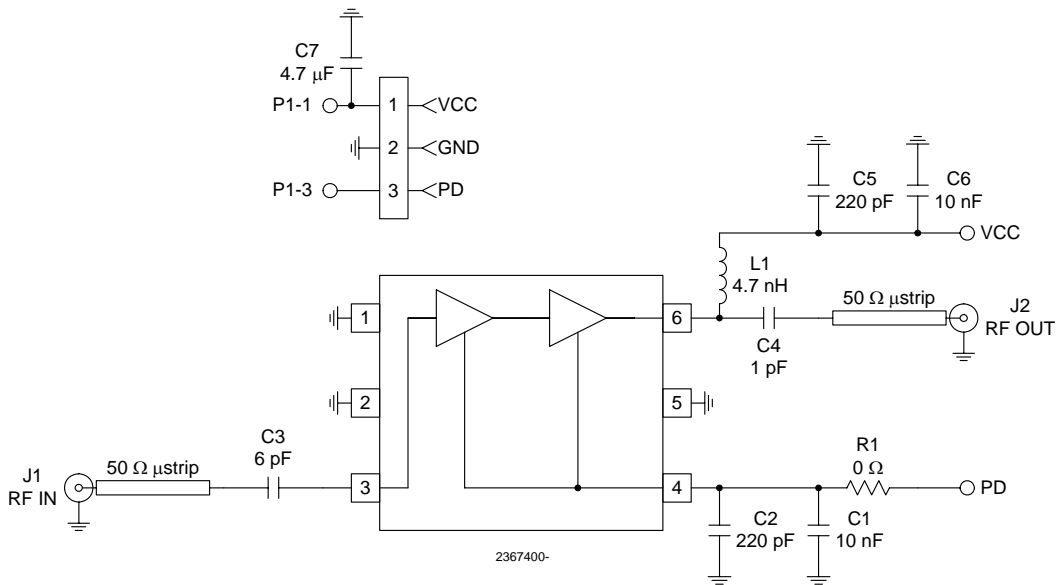
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Application Schematic: ~1880 MHz Operation



Evaluation Board Schematic

(Download [Bill of Materials](http://www.rfmd.com) from www.rfmd.com.)



Evaluation Board Layout

Board Size 0.948" x 1.063"
Board Thickness 0.031", Board Material FR-4

