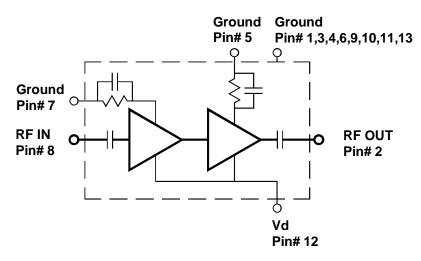
Description	The Raytheon RMLA3565-53 is a single bias wideband low noise MMIC amplifier that meets the following specifications over the $3.5 - 6.5$ GHz frequency range. The MMIC requires no external matching circuits and no external gate bias supply. To provide low noise, high linearity, and low current this device uses Raytheon's advanced 0.25 $\mu$ m PHEMT process.					
Features	<ul> <li>18.0 dB Gain</li> <li>1.35 dB Noise Figure</li> <li>Single Positive Bias</li> <li>Small Outline Quad Package</li> </ul>					
Performance Characteristics	Parameter Frequency Range Gain (Small Signal) Gain Variation vs Temp Noise Figure Input/Output Return Loss Power Out, P-1dB IP3 @ 5.5GHz,-8dBm Out Idd Vdd Case Operating Temp Thermal Resistance Notes: Operated at 25 °C and Vdd=4.0V. 25 °C does not apply to "Case Operating"	Min 3.5 17.0 8.0 -35 Temperature"	<b>Typ</b> 18.0 -0.013 1.35 -10.0 9.0 21.0 70.0 4.0 77.5	Max 6.5 1.9 -5.0 90.0 6.0 +95	Unit GHz dB dB/°C dB dB dBm dBm mA V °C °C/W	
Absolute Maximum Ratings	ParameterPositive Drain DC VoltageRF Input Power (from 50Ω source)Drain CurrentChannel TemperatureOperating Case TemperatureStorage Temperature RangeSoldering Temperature	Symbol V <sub>dd</sub> P <sub>IN</sub> (CW I <sub>dd</sub> T <sub>ch</sub> T <sub>Case</sub> T <sub>Storage</sub> T <sub>solder</sub>	V) 1 -40 t -40 t	alue 5.5 0 10 75 to 100 to 110 220	Unit V dBm mA °C °C °C °C °C	

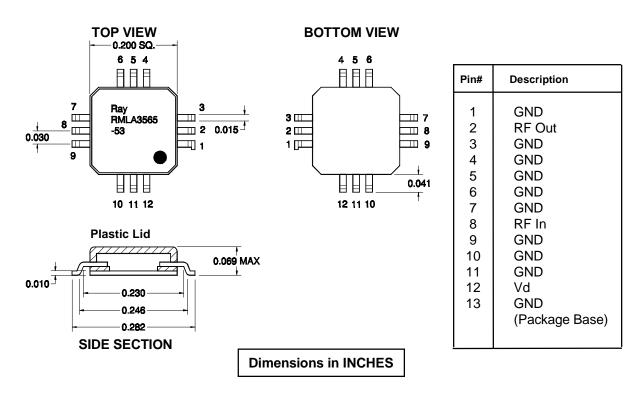
Characteristic performance data and specifications are subject to change without notice.

Page 1

Figure 1. Functional Block Diagram







#### Characteristic performance data and specifications are subject to change without notice.

Revised July 30, 1999

### Application Information

### CAUTION: THIS IS AN ESD SENSITIVE DEVICE

The following briefly describes a procedure for evaluating the high efficiency PHEMT amplifier packaged in a surface mount package. It may be noted that the chip is a fully monolithic single ended two stage amplifier for 3.5 to 6.5 GHz applications. Figure 1 shows the functional block diagram of the packaged product.

#### Test Fixture

Figure 2 shows the outline and pin-out descriptions for the packaged device. A typical test fixture schematic showing external bias components is shown in figure 3. Figure 4 shows typical layout of an evaluation board corresponding to the schematic diagram. A typical performance obtained from the test fixture is shown in figure 5. The following should be noted:

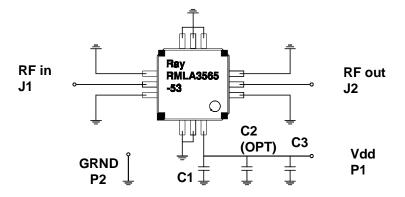
- (1) Package pin designations are as shown in figure 2.
- (2) Vd is the Drain Voltage (positive) applied at the pins of the package
- (3) Vdd is the positive supply voltage at the evaluation board terminal

#### Test Procedure for the evaluation board (RMLA3565-53-TB)

The following sequence of procedure must be followed to properly test the power amplifier:

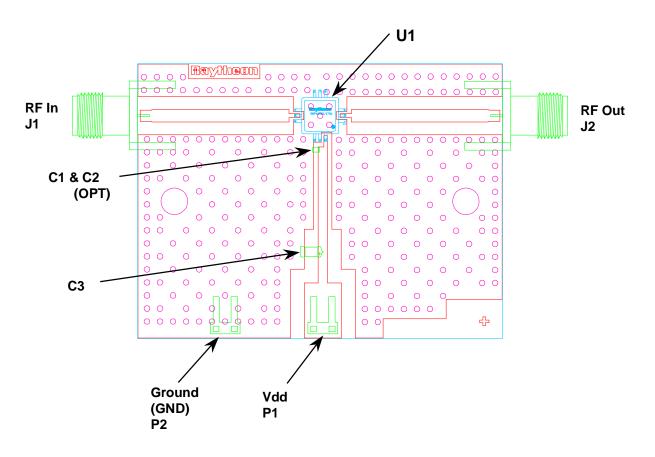
- Step 1: Turn off RF input power.
- Step 2: Use GND terminal of the evaluation board for DC supplies.
- Step 3: Apply drain supply voltages of +4.0 V to evaluation board terminals Vdd.
- Step 4: After the bias condition is established, RF input signal may now be applied.
- Step 5: Follow turn-off sequence of:
  - (i) Turn off RF Input Power (ii) Turn down and off Vdd

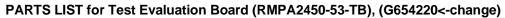
#### Figure 3. Schematic for a Typical Test Evaluation Board (RMLA3565-53-TB)



Characteristic performance data and specifications are subject to change without notice.

Figure 4. Layout and Assembly of Test Evaluation Board (RMLA3565-53-TB)





PART	VALUE	SIZE(L"xW")	Vendors
C1	330 pF	.04" x .02"	AVX, Murata, Novacap,
C2	1000 pF	.04" x .02"	AVX, Murata, Novacap
C3	4.75 uF	.14"x .11"	Sprague, ATC, AVX, Murata,
U1	RMLA3565-53	.28" x .28" x .07	Raytheon
P1, P2	Terminal		Samtec
J1, J2	SMA Connectors		E.F. Johnson
Board	RO4003(Rogers)	1.99x1.50x.032	Raytheon Dwg# ??????

Characteristic performance data and specifications are subject to change without notice.

Revised July 30, 1999

4

6 6.5 7 7.5 8 -30

-28

26

24

-22 (22 -20 -28 -28 -28

-16

14

-12

-10

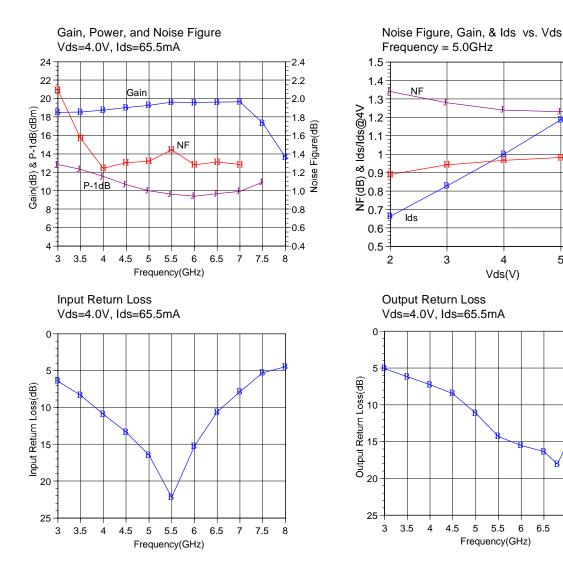
6

Gain

5

# **RMLA3565-53** Wideband Low Noise MMIC Amplifier

### Figure 5. Typical Performance



Characteristic performance data and specifications are subject to change without notice.

Revised July 30, 1999