

# Miscellaneous Communications Products



Device Number	Description	Available Packages
<b>Diallers</b>		
S25089	DTMF Generator, Single/Dual Tone	DIP 16, SOIC 16
S2559E	DTMF Generator, Mode Select	DIP 16, SOIC 16
S2559F	DTMF Generator, Chip Disable	DIP 16, SOIC 16
S2560G	Pulse Dialler with LNR	DIP 18
S2560H	Pulse Dialler with LNR	DIP 18
S25610	Pulse Dialler with 10 Memories	DIP 18
S25610E	Pulse Dialler with 10 Memories and AP	DIP 18
S2569B	DTMF Dialler with LNR, $t_{TD} = 70$ ms	DIP 18
S2569G	DTMF Dialler with LNR, $t_{TD} = 80$ ms	DIP 18
S2569H	DTMF Dialler with LNR, Auto PD	DIP 18
S2570-1	LD/MF Dialler with LNR, Pause Key	DIP 22, SOIC 28
S2570-2	LD/MF Dialler with LNR, Atlanta Procedure	DIP 22, SOIC 28
S2570-3	LD/MF Dialler with LNR, Sliding Cursor	DIP 22, SOIC 28
S2571	Pulse Dialler with LNR and Auto Pause	DIP 18
S2573A	Pulse Dialler with LNR, Tri-state Outputs	DIP 18
S2573C	Pulse Dialler with LNR	DIP 18
S2579B	DTMF Generator, Binary Inputs	DIP 16
<b>Codecs</b>		
S3506	A-Law Codec, Programmable Gain	DIP 22, CerDIP 22 PLCC 28
S3507	$\mu$ -Law Codec, Programmable Gain	CerDIP 22
S3507A	$\mu$ -Law Codec, A/B Signalling	DIP 28, PLCC 28
S44231	A-Law Codec, Synchronous	DIP 16, CerDIP 16
S44232	$\mu$ -Law Codec, Synchronous	DIP 16, CerDIP 16
S44233	A-Law Codec, Asynchronous	DIP 16, CerDIP 16
S44234	$\mu$ -Law Codec, Asynchronous	DIP 16, CerDIP 16
S44235	A-Law Codec, Synchronous with PLL	DIP 16, CerDIP 16
S44236	$\mu$ -Law Codec, Synchronous with PLL	DIP 16, CerDIP 16
S44237	A-Law Codec, Asynchronous with PLL	DIP 16, CerDIP 16
S44238	$\mu$ -Law Codec, Asynchronous with PLL	DIP 16, CerDIP 16
S44231 I	A-Law Codec, Synchronous	DIP 16, CerDIP 16
S44232 I	$\mu$ -Law Codec, Synchronous	DIP 16, CerDIP 16
S44233 I	A-Law Codec, Asynchronous	DIP 16, CerDIP 16
S44234 I	$\mu$ -Law Codec, Asynchronous	DIP 16, CerDIP 16
S44235 I	A-Law Codec, Synchronous with PLL	DIP 16, CerDIP 16
S44236 I	$\mu$ -Law Codec, Synchronous with PLL	DIP 16, CerDIP 16
S44237 I	A-Law Codec, Asynchronous with PLL	DIP 16, CerDIP 16
S44238 I	$\mu$ -Law Codec, Asynchronous with PLL	DIP 16, CerDIP 16
S44278	$\mu$ -Law Codec, Asynchronous with PLL	DIP 16

<b>Device Number</b>	<b>Description</b>	<b>Available Packages</b>
	<b>Filters</b>	
S3524A	Digital Frequency Detection, Clock Tunable	DIP 8
S3526B	BP Notch Filter, Generator, Clock Tunable	DIP 14, SOIC 16
S3528	Programmable SC Low Pass Filter	DIP 18, PLCC 28
S3528 I	Programmable SC Low Pass Filter	DIP 18, PLCC 28
S3529	Programmable SC High Pass Filter	DIP 18, PLCC 28
S3529 I	Programmable SC High Pass Filter	DIP 18, PLCC 28
	<b>Modem</b>	
S35212A	V22/212A Modem Filter/ Equalizer	DIP 28, PLCC 28