

January 29, 1998

TEL:805-498-2111 FAX:805-498-3804 WEB:http://www.semtech.com

DESCRIPTION

The SC1578 is a high performance step-down DC-DC converter, designed to drive an external P-channel MOSFET to generate programmable output voltages. Two main schemes of pulse-skipping and pulse-frequency modulation are employed to maintain low quiescent current and high conversion efficiency over wide ranges of input voltage and loads. The SC1578 delivers 10mA to 2A of output current with 87%-93% efficiency at $V_{IN}=9V$, $V_{OUT}=5V$. A current sense comparator with both inverting and non-inverting inputs uncommitted is included to provide the crucial function of either current limit protection or constant output current control. When the SC1578 is used in a high-side current sensing step-down constant current source, the efficiency is typically greater than 90%. Duty cycle can be adjusted to greater than 90% by connecting a resistor from the DUTY pin to V_{IN} . Quiescent current is about 90 μ A and can be reduced to 8 μ A in shutdown mode. With a switching frequency range of 90kHz to 280kHz, small size switching components may be used, which is ideal for battery powered portable equipment.

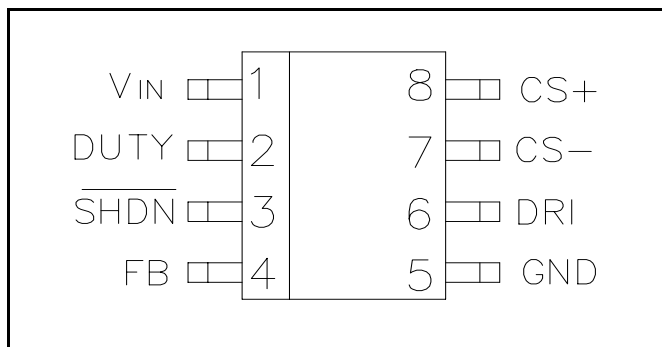
ORDERING INFORMATION

DEVICE ⁽¹⁾	PACKAGE
SC1578CS	SO-8

Note:

(1) Add suffix 'TR' for tape and reel.

PIN CONFIGURATION



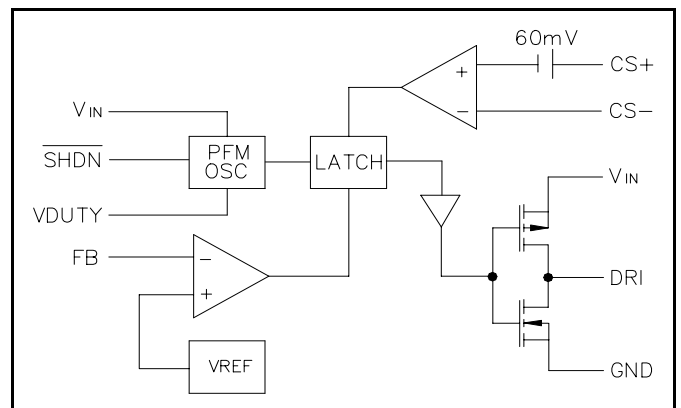
FEATURES

- 4V to 24V input voltage operation.
- High efficiency (up to 95%).
- Low quiescent current at 90 μ A.
- Pulse-skipping and pulse-frequency modulation.
- Inputs-uncommitted current sense comparator.
- Duty cycle adjustable
- 90kHz to 280kHz oscillator frequency
- Power-saving shutdown mode (8 μ A Typical).
- Push-pull driver output

APPLICATIONS

- Notebook 5V/3.3V main power
- Step-down DC-DC converter module.
- Constant current source for battery chargers.

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

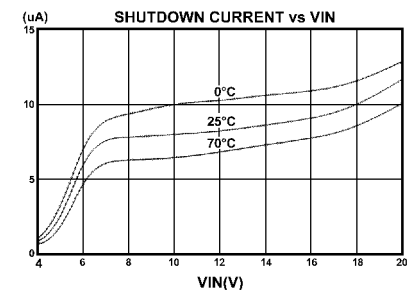
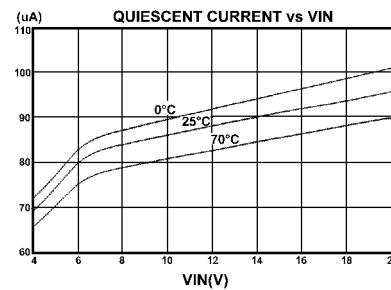
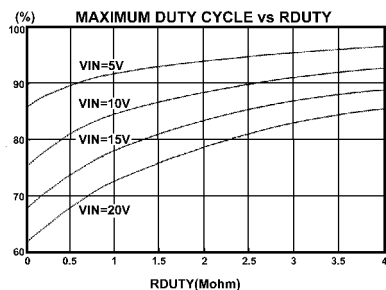
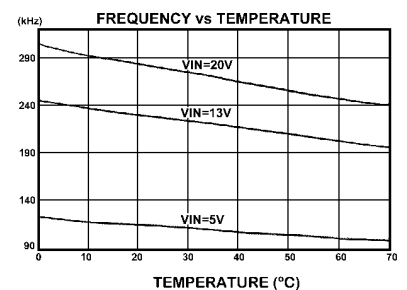
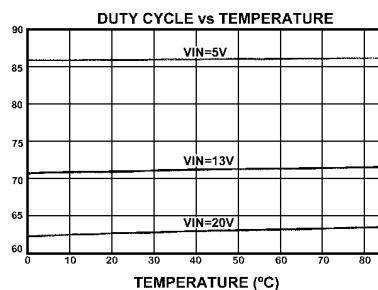
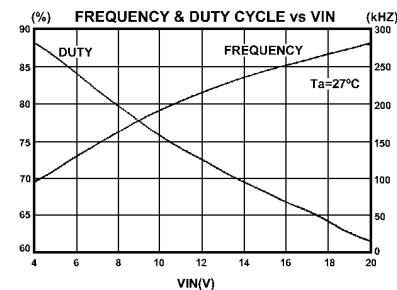
Parameter	Symbol	Maximum	Units
Supply Voltage	V _{IN}	24	V
Duty Voltage	V _{DUTY}	20	V
SHDN Voltage	V _{SHDN}	15	V
Operating Temperature Range	T _A	0 to 70	°C
Storage Temperature Range	T _{STG}	-65 to 150	°C

January 29, 1998

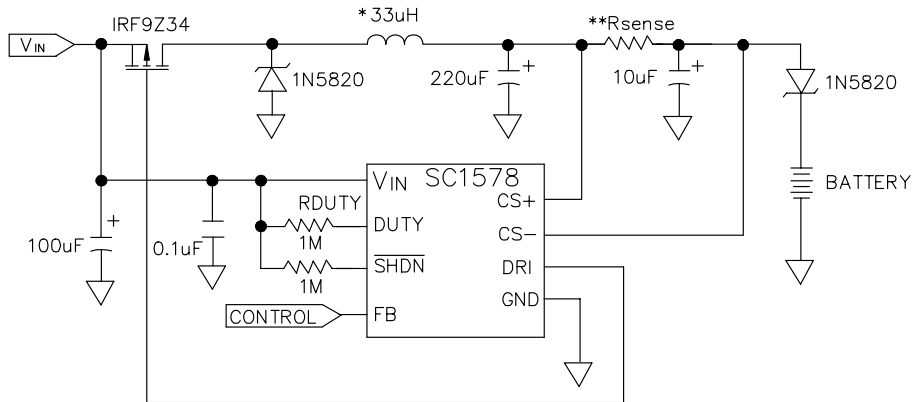
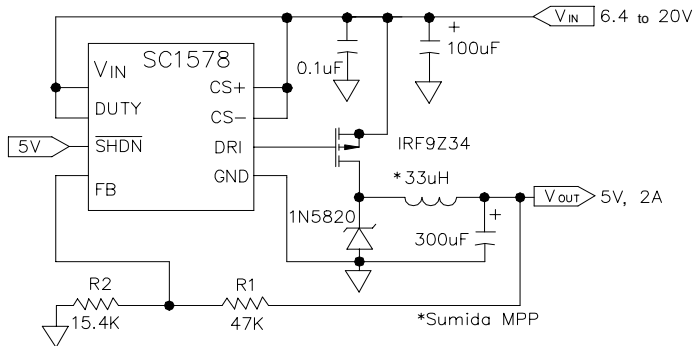
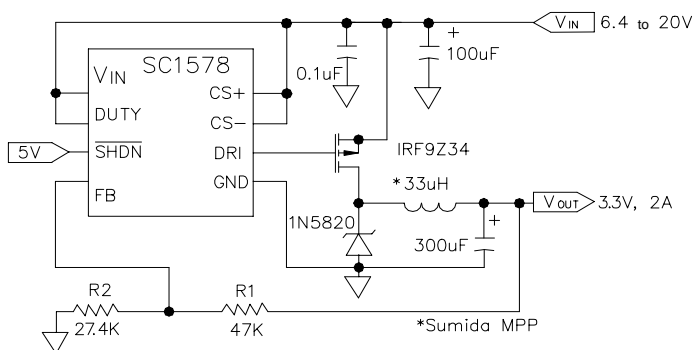
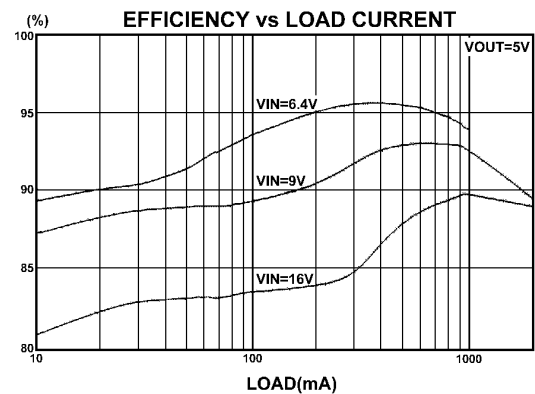
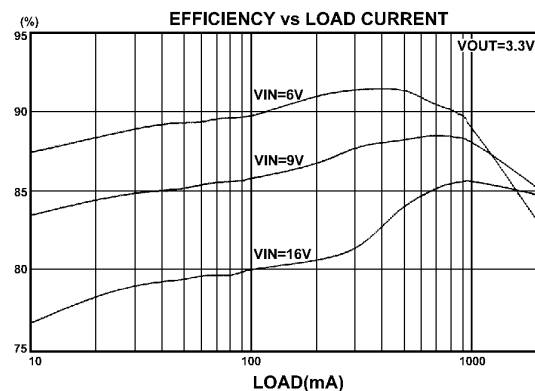
ELECTRICAL CHARACTERISTICS

 Unless otherwise specified, $T_A = 25^\circ\text{C}$, $V_{IN} = 13\text{V}$

Parameter		Conditions	Min	Typ	Max	Units
Operation Voltage			4		24	V
Quiescent Current		$V_{FB} = 1.5\text{V}$		90	160	μA
Shutdown Mode Current		$V_{\overline{\text{SHDN}}} = 0\text{V}$		8	20	μA
Internal Reference Voltage			1.16	1.22	1.28	V
Driver Sinking "On resistance"				16		Ω
Driver Sourcing "On resistance"				11		Ω
Current Sense Comparator Threshold	1578 1578A	$V_{\text{CS}+} = 13\text{V}$	42 52	57 57	72 62	mV
Shutdown Threshold			0.8	1.5	2.4	V
SHDN Pin Leakage Current		$V_{\text{SHDN}} < 15\text{V}$			1	μA
Duty Cycle		$V_{\text{DUTY}} = V_{\text{IN}}$		71		%
Oscillator Frequency		$V_{\text{DUTY}} = V_{\text{IN}}$		225		kHz

TYPICAL PERFORMANCE CHARACTERISTICS


January 29, 1998

TYPICAL APPLICATION CIRCUIT

 *: Sumida MPP **: Charge current = $57\text{mV}/R_{\text{sense}}$
APPLICATION EXAMPLES

Figure 1. 5V Step-Down Converter

Figure 2. 3.3 Step-Down Converter


January 29, 1998

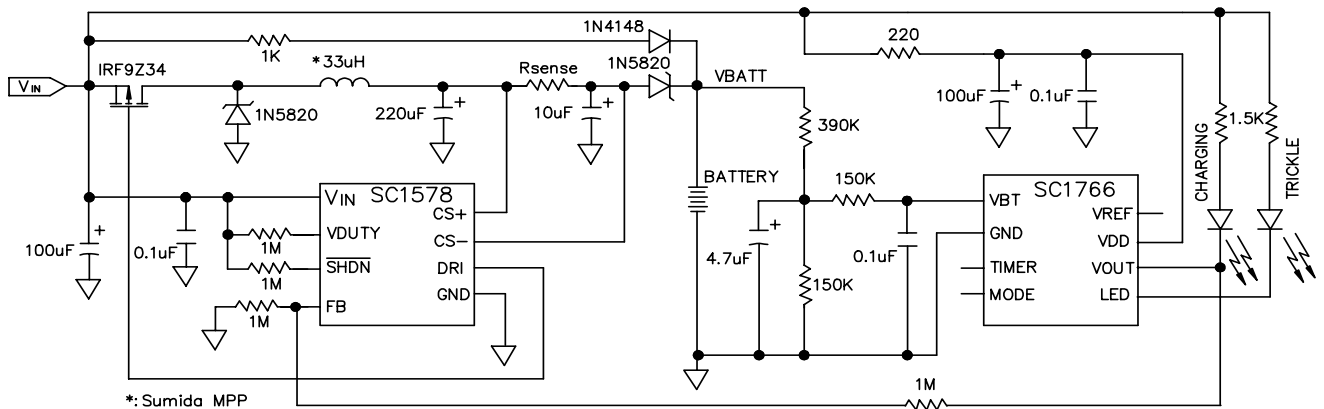
TYPICAL APPLICATION CIRCUIT


Figure 3. Battery Charge Circuit with High-Side Current Sensing Constant Current Source

NOTE: $R_{SENSE}=0.1$ ohm, charge current= $0.5A \pm 10\%$, $V_{IN}>V_{BATT} + 3.5V$
 $R_{SENSE}=0.05$ ohm, charge current= $1A \pm 10\%$, $V_{IN}>V_{BATT} + 4V$
 $R_{SENSE}=0.033$ ohm, charge current= $1.5A \pm 10\%$, $V_{IN}>V_{BATT} + 4.5V$
 Efficiency $>90\%$, measured at CS- node

PIN DESCRIPTIONS

PIN 1: V_{IN} 4V to 24V input supply voltage.

PIN 2: **DUTY** Duty cycle adjustment pin. To be tied to the V_{IN} pin directly or through a resistor to adjust oscillator duty cycle. See Electrical Characteristics: R_{DUTY} must be above $1M\Omega$ if $V_{IN}>20V$.

PIN 3: **SHDN** Logical input to shutdown the chip: V_{SHDN} = high for normal operation, V_{SHDN} = low for shutdown. This pin should not be left floating or forced greater than 15V. In shutdown mode the DRI pin is high.

PIN 4: **FB** Feedback comparator input, to compare the feedback voltage with the internal reference voltage. Connecting a resistor R1 to the converter output and a resistor R2 to ground yields the output voltage:
 $V_{OUT} = 1.22 \times (R1+R2)/R2$

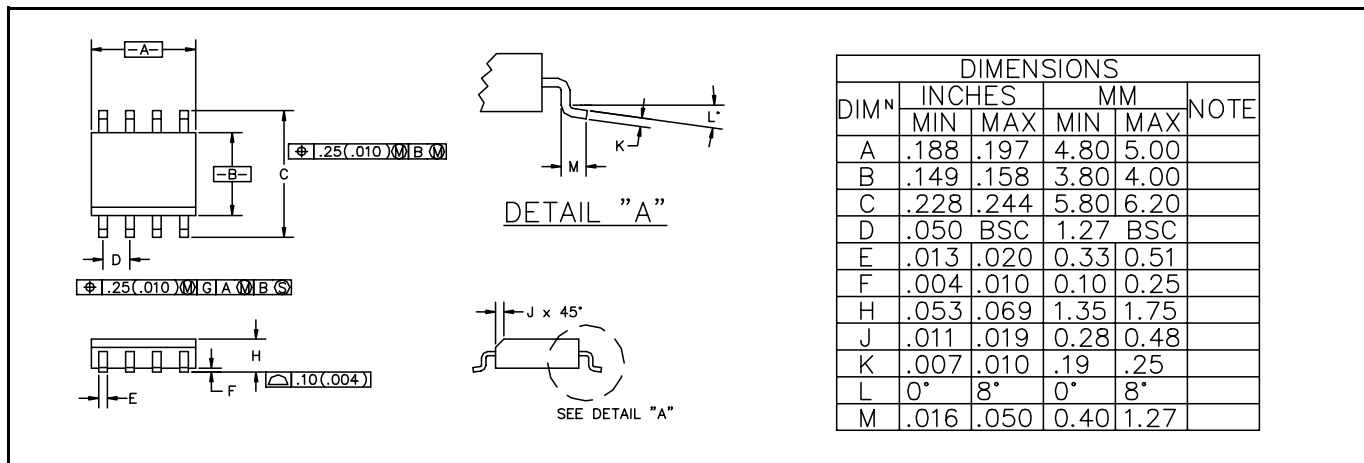
PIN 5: **GND** Power ground.

PIN 6: **DRI** Push-Pull driver output to drive an external P-channel MOSFET or PNP transistor. When driving a PNP bipolar transistor, a base resistor and a capacitor to the base of PNP are recommended.

PIN 7: **CS-** Current sense comparator inverting input, not to exceed V_{IN} .

PIN 8: **CS+** Current sense comparator non-inverting input, not to exceed V_{IN} .

January 29, 1998

DEVICE OUTLINE - SO-8

LAND PATTERN - SO-8
