

Vishay Siliconix

N-Channel 20-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	Q _g (Typ.)			
20	0.020 at V _{GS} = 4.5 V	6	10 nC			
	0.025 at $V_{GS} = 2.5 \text{ V}$	6	10110			

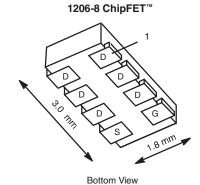
FEATURES

- Halogen-free
- TrenchFET[®] Power MOSFET

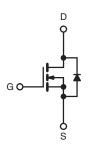


APPLICATIONS

· Load Switches for Portable Devices



Ordering Information: Si5432DC-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V_{DS}	20	V	
Gate-Source Voltage		V_{GS}	± 12		
	T _C = 25 °C		6 ^a		
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C		6 ^a		
Continuous Diam Current (1) = 130 °C)	T _A = 25 °C	l I _D	6 ^{a, b, c}		
	T _A = 70 °C		6 ^{a, b, c}	A	
Pulsed Drain Current		I _{DM}	30		
Continuous Courses Dunin Diede Coursest	T _C = 25 °C	1-	5.2		
Continuous Source-Drain Diode Current	T _A = 25 °C	- I _S -	2.1 ^{b, c}		
	T _C = 25 °C		6.3		
Maximum Dawar Dissination	T _C = 70 °C	P _D	4	w	
Maximum Power Dissipation	T _A = 25 °C] 'D [2.5 ^{b, c}	vv	
	T _A = 70 °C		1.6 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	
Soldering Recommendations (Peak Tempera	ature) ^{e, f}		260		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{a, c, d}	t ≤ 5 s	R _{thJA}	40	50	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	15	20] 5/**		

- a. Package limited, T_C = 25 °C.
 b. Surface Mounted on 1" x 1" FR4 board.
- d. Maximum under Steady State conditions is 95 °C/W.
- e. See Reliability Manual for profile. The ChipFET is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- f. Rework Conditions: manual soldering with a soldering iron is not recommended for leadless components.

Si5432DC

Vishay Siliconix



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static					L		
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 050 A		25		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 4.0			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	0.6		1.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V			1		
		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	30			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 8.3 \text{ A}$		0.016	0.020	Ω	
		V _{GS} = 2.5 V, I _D = 4.5 A		0.020	0.025		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 8.3 A		45		S	
Dynamic ^b	10	20 1				l	
Input Capacitance	C _{iss}			1200		pF	
Output Capacitance	C _{oss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		220			
Reverse Transfer Capacitance	C _{rss}			100			
Total Gate Charge	Q _g	$V_{DS} = 10 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 8.3 \text{ A}$		22	33	nC	
		20 1 00 1 0		10	15		
Gate-Source Charge	Q_{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 8.3 \text{ A}$		2.5			
Gate-Drain Charge	Q_{gd}			1.7			
Gate Resistance	R _g	f = 1 MHz		2.4		Ω	
Turn-on Delay Time	t _{d(on)}			15	25	ns	
Rise Time	t _r	V_{DD} = 10 V, R_L = 1.5 Ω		10	15		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 6.7~A,~V_{GEN}=4.5~V,~R_g=1~\Omega$		35	55		
Fall Time	t _f			12	20		
Turn-on Delay Time	t _{d(on)}			10	15		
Rise Time	t _r	V_{DD} = 10 V, R_L = 1.5 Ω		12	20		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 6.7~A,~V_{GEN}$ = 10 V, R_g = 1 Ω		25	40		
Fall Time	t _f			10	15		
Drain-Source Body Diode Characteristic	cs					l	
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			5.2	^	
Pulse Diode Forward Current	I _{SM}				30	A	
Body Diode Voltage	V_{SD}	$I_S = 6.7 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			20	40	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	L = 6.7 A dl/dt = 100 A/··· T = 05.00		10	20	nC	
Reverse Recovery Fall Time	t _a	$I_F = 6.7 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		10			
Reverse Recovery Rise Time	t _b			10		ns	

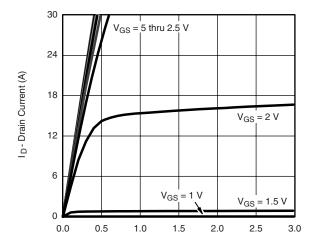
- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



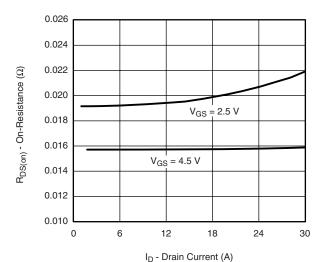
Vishay Siliconix

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

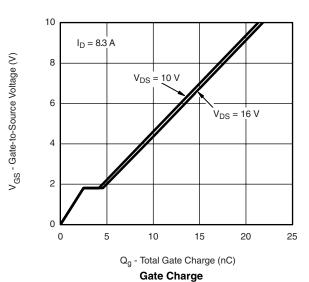


 V_{DS} - Drain-to-Source Voltage (V)

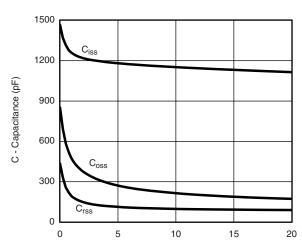




On-Resistance vs. Drain Current

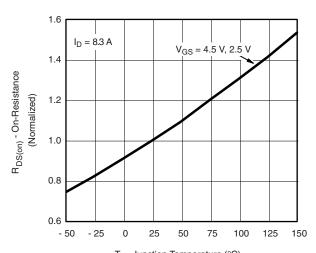


V_{GS} - Gate-to-Source Voltage (V) **Transfer Characteristics**



V_{DS} - Drain-to-Source Voltage (V)

Capacitance



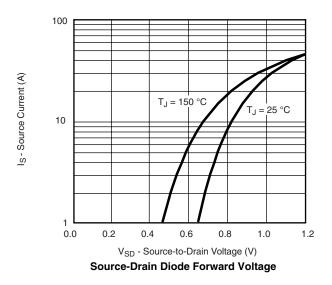
T_J - Junction Temperature (°C)

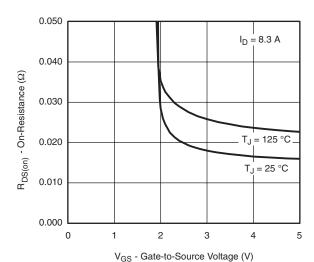
Si5432DC

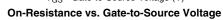
Vishay Siliconix

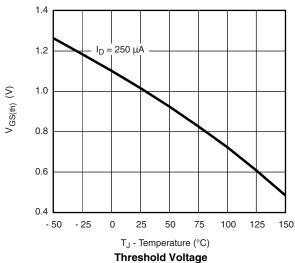
VISHAY.

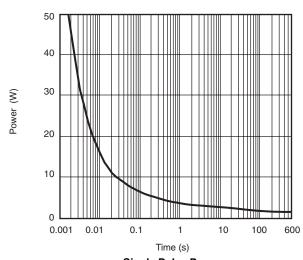
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



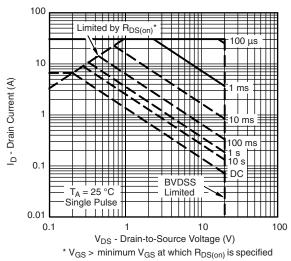








d Voltage Single Pulse Power

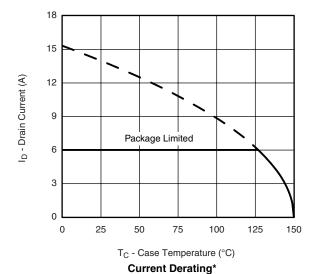


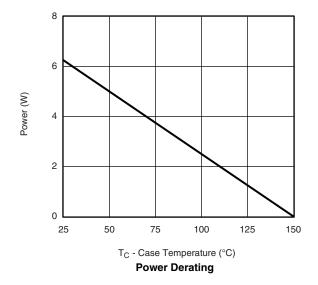
Safe Operating Area, Junction-to-Ambient



Vishay Siliconix

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





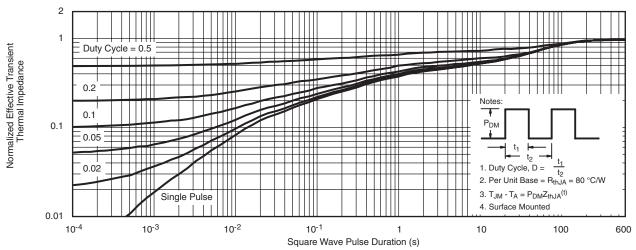
^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

Si5432DC

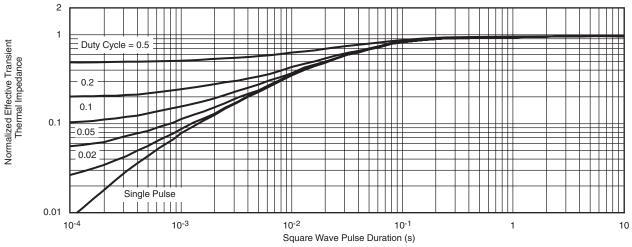
Vishay Siliconix



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see https://www.vishay.com/ppg?68925.



Vishay

Disclaimer

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.

Revision: 18-Jul-08

Document Number: 91000 www.vishay.com