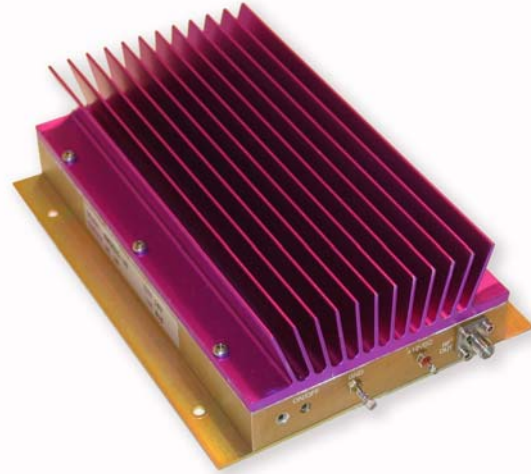


The **SM1720-41L** is a 1.7 to 2.0 GHz solid state GaAs FET amplifier designed for the Personal Communications (PCS) Market. Its small size and ultra high linearity make it ideally suited for systems using single or multi-carrier CDMA, TDMA, or other high dynamic range wireless application. Our proprietary pre-distortion technique dramatically improves the linearity of this amplifier, which provides an OIP3 of +60 dBm typical. The unit provides 51 dB of gain and operates off a single DC supply voltage of +12V.



Features

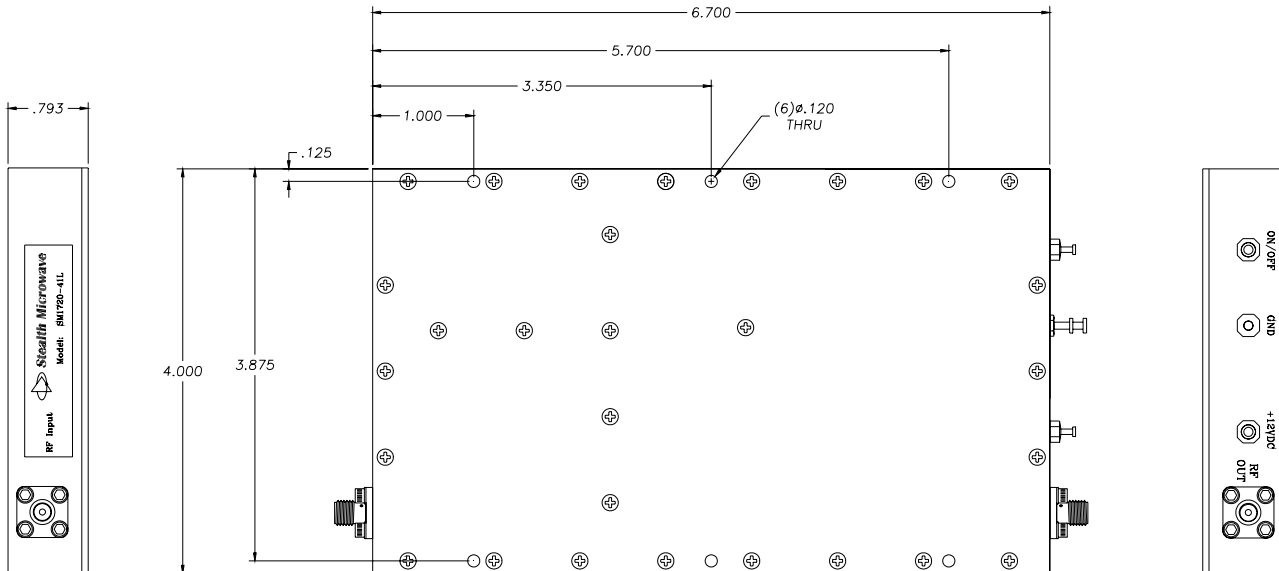
- Built-in linearizer increases the OIP3 by 7 dB
- Temperature compensation
- Single Power Supply
- Over/Reverse Voltage Protection
- Thermal Protection with Auto Reset

Options

- Forward Power Detection
- Logic On/Off Control
- Heatsink

Parameter	Specification
Frequency Range	1.7 – 2.0 GHz (100 MHz BW)
Pout (P1dB)	+ 41 dBm (min.)
Third Order Intercept Point	+ 60 dBm (min.)
Linear Gain	51 dB \pm 1 dB
Gain Flatness over Full Band	\pm .5 dB
Input/Output Return Loss	-14 dB / -14dB
DC Input Voltage	+ 12 Volts
DC Supply Current	5.5 Amps (Varies per application)
Mechanical Dimensions (Without Heatsink)	6.7 x 4.0 x 0.8 Inches
RF Connectors	SMA Female
Operating Temperature	0°C to +85°C
Operating Humidity	95% Non-condensing
Operating Altitude	Up to 10,000 feet above Sea Level

DIMENSIONS IN INCHES



Pin	Description	Values
RF Input	Input Connector (SMA Female)	-8 dBm typical
RF OUT	Output Connector (SMA Female)	+41dBm @P1dB
GND	Ground Turret	---
+12VDC	DC Input Voltage	+ 12 Volts @ 5.5 Amps (typ.)
ON/OFF	TTL Logic On/Off	0 Volts = Off, + 5 Volts = On

Specifications subject to change without notice.