

2-Phase Stepper Motor Unipolar Driver IC

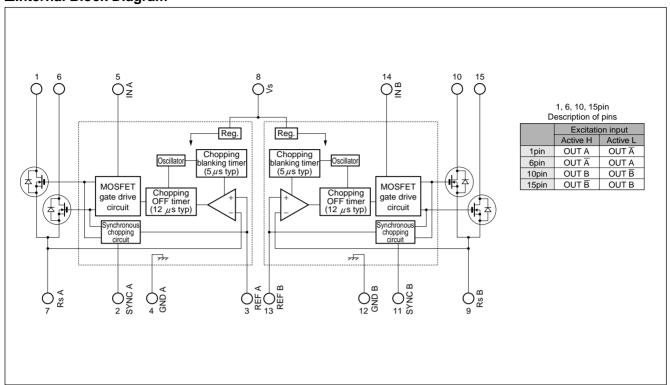
■Absolute Maximum Ratings

Parameter	Symbol	Ratings	Units
Motor supply voltage	Vcc	46	V
Control supply voltage	Vs	46	V
FET Drain-Source voltage	VDSS	100	V
TTL input voltage	Vin	−0.3 to +7	V
SYNC terminal voltage	Vsync	-0.3 to +7	V
Reference voltage	VREF	-0.3 to +7	V
Sense voltage	V _{RS}	−5 to +7	V
Output current	lo	1.5	Α
Dawar dissination	P _{D1}	4.0 (T _a =25°C)	W
Power dissipation	P _{D2}	28 (T₀=25°C)	W
Channel temperature	T _{ch}	150	°C
Storage temperature	T _{stg}	-40 to +150	°C
Ambient operating temperature	Ta	−20 to +85	°C

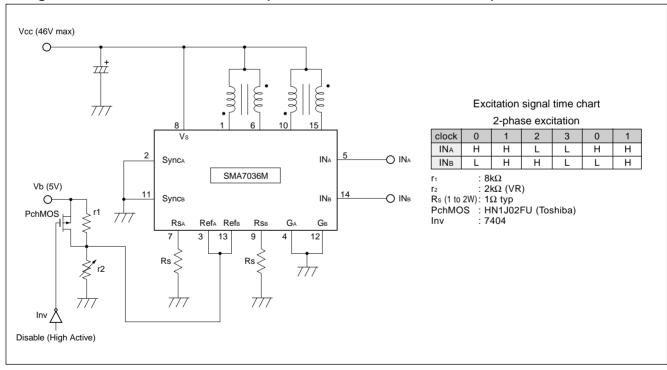
■Electrical Characteristics

Parameter		0 1 1	Ratings						
		Symbol	min typ max			Units			
Control supply current		Is		10	15	m/			
		Condition	V _S =44V						
Control supply voltage		Vs	10	24	44	V			
FET Drain-Source		V _{DSS}	100						
voltage		Condition	100	Vs=44V, lbss=250 μA		v			
	voitage			ν 3 – 4 4 γ, 1033 – 2 3 ο μι γ	0.6				
FET ON voltage		V _{DS} Condition		I _D =1A, V _S =10V	0.0	v			
		V _{SD}		ID-17A, VS-10V	1.1				
FET diode forward voltage		Condition							
		IDSS		ISD=1A 250					
FET drain leaka	FET drain leakage current			V 100V V - 44V	250	μΑ			
		Condition		V _{DSS} =100V, V _S =44V					
	Active H	VIH	2						
		Condition		I _D =1A					
		VIL			0.8				
		Condition		V _{DSS} =100V					
IN terminal	Active L	VIH	2						
		Condition		V _{DSS} =100V		v			
		VıL			0.8	•			
		Condition		I _D =1A					
	Input	lı			±1	μΑ			
	current	Condition		$V_S=44V$, $V_I=0$ or $5V$					
SYNC terminal Input		VsyncH	4.0			V			
	Input	Condition		Synchronous chopping mode					
	voltage	VsyncL			0.8				
		Condition		Asynchronous chopping mode					
		IsyncH		1 ,	0.1				
	Input	Condition		Vs=44V, Vys=5V		mA			
	current	IsyncL			-0.1				
		Condition		Vs=44V, V _Y s=0V	<u> </u>				
REF terminal In cu		VREF	0	V3=11V, V13=3V	2.0				
	Input	Condition	<u> </u>	Reference voltage input	2.0				
	voltage	VREF	4.0	Tiererenee voitage input	5.5	v			
		Condition	₹.0	Output FET OFF	J.J				
		IREF		Output I E I OI I	±1				
	Input	Condition		No synchronous trigger	<u>±1</u>	μ,			
	current								
	Internal	RREF	Destruc	40	uo trianor	Ω			
resistance	resistance	Condition	Hesistan	ce between GND and REF terminal at synchronol	us irigger				
	Ton		1.5						
		Condition			Vs=24V, lb=1A				
Switching time	Tr		0.5						
	Condition	Vs=24V, l _D =1A							
	Tstg	0.9							
		Condition		Vs=24V, I _D =1A					
		Tf		0.1					
		Condition		Vs=24V, l _D =1A					
Chopping OFF time		Toff	12 V _S =24V						
		Condition							

■Internal Block Diagram

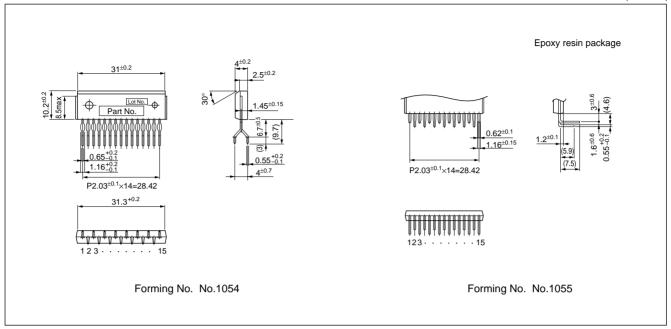


■Diagram of Standard External Circuit (Recommended Circuit Constants)



■External Dimensions

(Unit: mm)



Application Notes

■Outline

SMA7036M is a stepper motor driver IC developed to reduce the number of external parts required by the conventional SMA7029M. This IC successfully eliminates the need for some external parts without sacrificing the features of SMA7029M. The basic function pins are compatible with those of SMA7029M.

■Notes on Replacing SMA7029M

SMA7036M is pin-compatible with SMA7029M. When using the IC on an existing board, the following preparations are necessary:

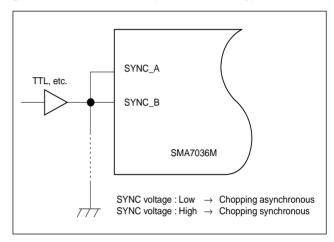
- (1) Remove the resistors and capacitors attached for setting the chopping OFF time. (r₃, r₄, C₁, and C₂ in the catalog)
- (2) Remove the resistors and capacitors attached for preventing noise in the detection voltage V_{RS} from causing malfunctioning and short the sections from which the resistors were removed using jumper wires. (r₅, r₆, C₃, and C₄ in the catalog)
- (3) Normally, keep pins 2 and 11 grounded because their functions have changed to synchronous and asynchronous switching (SYNC terminals). For details, see "Circuit for Preventing Abnormal Noise When the Motor Is Not Running (Synchronous circuit)." (Low: asynchronous, High: synchronous)

■Circuit for Preventing Abnormal Noise When the Motor Is Not Running (Synchronous Circuit)

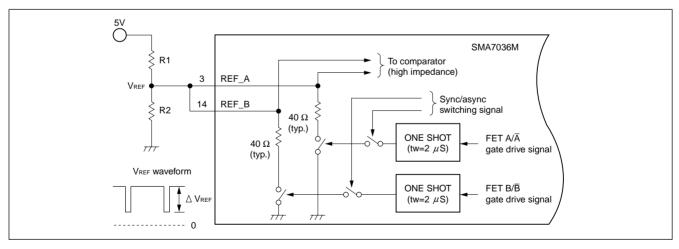
A motor may generate abnormal noise when it is not running. This phenomenon is attributable to asynchronous chopping between phases A and B. To prevent the phenomenon, SMA7036M contains a synchronous chopping circuit. Do not leave the SYNC terminals open because they are for CMOS input.

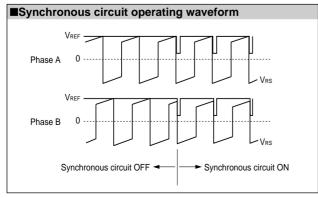
Connect TTL or similar to the SYNC terminals and switch the SYNC terminal level high or low.

When the motor is not running, set the TTL signal high (SYNC terminal voltage: 4 V or more) to make chopping synchronous. When the motor is running, set the TTL signal low (SYNC terminal voltage: 0.8 V or less) to make chopping asynchronous. If chopping is set to synchronous when the motor is running, the motor torque deteriorates before the coil current reaches the set value. If no abnormal noise occurs when the motor is not running, ground the SYNC terminals (TTL not necessary).



The built-in synchronous chopping circuit superimposes a trigger signal on the REF terminal for synchronization between the two phases. The figure below shows the internal circuit of the REF terminal. Since the Δ VREF varies depending on the values of R1 and R2, determine these values for when the motor is not running within the range where the two phases are synchronized.





■Determining the Output Current

Fig. 1 shows the waveform of the output current (motor coil current). The method of determining the peak value of the output current (Io) based on this waveform is shown below.

(Parameters for determining the output current Io)

V_b: Reference supply voltage

r₁,r₂: Voltage-divider resistors for the reference supply voltage

Rs: Current sense resistor

(1) Normal rotation mode

lo is determined as follows when current flows at the maximum level during motor rotation. (See Fig.2.)

$$I_0 \cong \frac{r_2}{r_1 + r_2} \bullet \frac{V_b}{R_s}$$
 (1)

(2) Power down mode

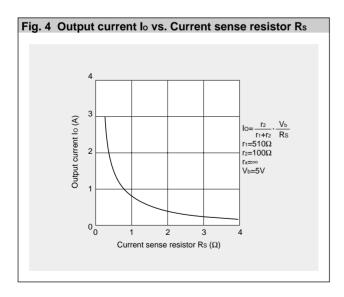
The circuit in Fig.3 (rx and Tr) is added in order to decrease the coil current. Io is then determined as follows.

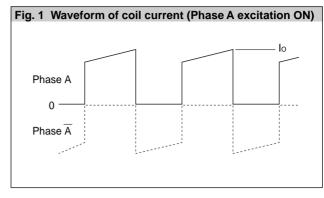
$$I_{\text{OPD}} \cong \frac{1}{1 + \frac{r_1(r_2 + r_X)}{r_2 \bullet r_X}} \bullet \frac{V_b}{R_s} \qquad (2)$$

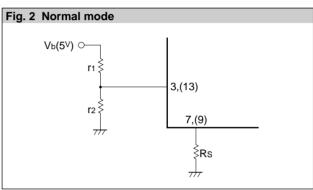
Equation (2) can be modified to obtain equation to determine rx.

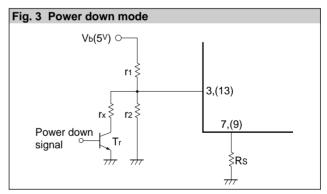
$$rx = \frac{1}{\frac{1}{r_1} \left(\frac{V_b}{R_s \bullet lopd} - 1 \right) - \frac{1}{r_2}}$$

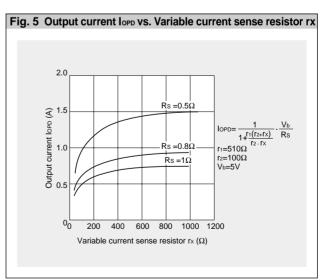
Fig. 4 and 5 show the graphs of equations (1) and (2) respectively.







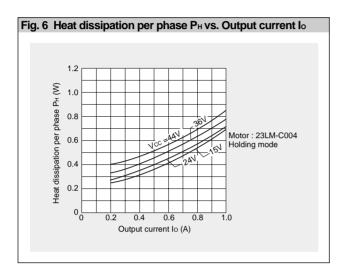


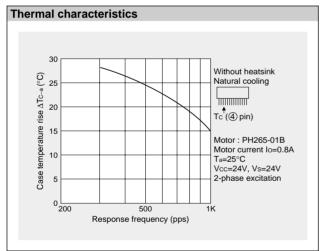


■Thermal Design

An outline of the method for calculating heat dissipation is shown below.

(1) Obtain the value of PH that corresponds to the motor coil current Io from Fig. 6 "Heat dissipation per phase Ph vs. Output current lo."

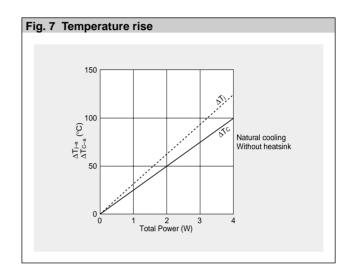




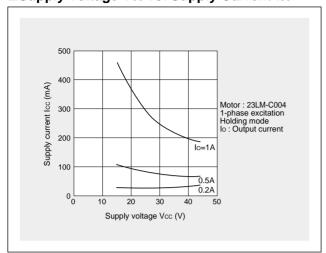
(2) The power dissipation Pdiss is obtained using the following

2-phase excitation: $P_{diss} \cong 2P_H+0.015 \times V_S$ (W) 1-2 phase excitation: $P_{\text{diss}} \cong \frac{3}{2} P_{\text{H}} + 0.015 \times V_{\text{S}}$ (W)

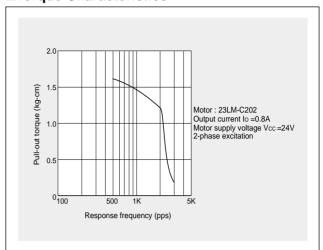
(3) Obtain the temperature rise that corresponds to the calculated value of Pdiss from Fig. 7 "Temperature rise."



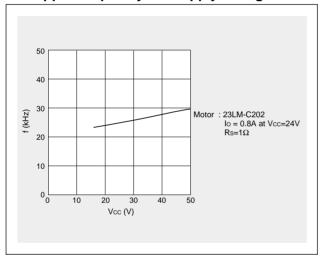
■Supply Voltage Vcc vs. Supply Current Icc



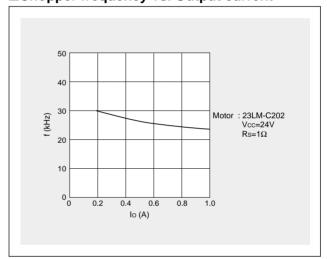
■Torque Characteristics



■Chopper frequency vs. Supply voltage



■Chopper frequency vs. Output current



■Handling Precautions

The input terminals of this product use C-MOS circuits. Observe the following precautions.

- Carefully control the humidity of the room to prevent the buildup of static electricity. Since static electricity is particularly a problem during the winter, be sure to take sufficient precautions.
- Take care to make sure that static electricity is not applied to the IC during wiring and assembly. Take precautions such as shorting the terminals of the printed wiring board to ensure that they are at the same electrical potential.