SMF05

ESD Protection Diode Array

Quad, Low Clamping Voltage

This quad monolithic silicon overvoltage suppressor is designed for applications requiring transient voltage protection capability. It is intended for use in ESD sensitive equipment such as computers, printers, cell phones, medical equipment, and other applications. Its quad junction common anode design protects four separate lines using only one package. These devices are ideal for situations where board space is at a premium.

Specification Features

- SC-88A Package Allows Four Separate Unidirectional Configurations
- Low Leakage < 5 μA @ 5 V
- Breakdown Voltage: 6.1 V 7.2 V @ 1 mA
- Low Capacitance (90 pF TYP)
- Provides Protection for IEC61000-4-2
- Pb-Free Packages are Available*

Mechanical Characteristics

- Void Free, Transfer-Molded, Thermosetting Plastic Case
- Corrosion Resistant Finish, Easily Solderable
- Package Designed for Optimal Automated Board Assembly
- Small Package Size for High Density Applications

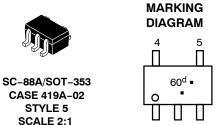
Applications

- Computers
- Printers
- Cell Phones
- Medical Equipment



ON Semiconductor®

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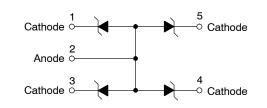


60 = Specific Device Code

d = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)



ORDERING INFORMATION

Device	Package	Shipping [†]
SMF05T1	SC-88A	3000 Tape & Reel
SMF05T1G	SC-88A (Pb-Free)	3000 Tape & Reel
SMF05T2G	SC-88A (Pb-Free)	3000 Tape & Reel
SMF05CT1	SC-88A	3000 Tape & Reel
SMF05CT1G	SC-88A (Pb-Free)	3000 Tape & Reel
SMF05CT2	SC-88A	3000 Tape & Reel
SMF05CT2G	SC-88A (Pb-Free)	3000 Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

SMF05

MAXIMUM RATINGS ($T_A = 25^{\circ}C$ unless otherwise noted)

	Characteristic	Symbol	Value	Unit
Peak Power Dissipation	n @ 8 X 20 μ s @T _A \leq 25°C (Note 1)	P_pk	200	W
Maximum Junction Temperature		T _{Jmax}	150	°C
Operating Junction and Storage Temperature Range		$T_{J_{I}}T_{stg}$	−55 to +150	°C
ESD Discharge	IEC61000-4-2, Air Discharge IEC61000-4-2, Contact Discharge	V_PP	16 9	kV
Lead Solder Temperature (10 seconds duration)		TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

		n Voltage nA (Volts)	Leakage Current I _R @ V _{RWM} = 5 V	Capacitance	Max V _F @ I _F = 200 mA	Max Clamping Voltage (V _C) @ I _{PP}		Max Clamping Voltage (V _C) @ I _{PP}	
Device	Min	Max	(μ A)	(pF)	(V)	I _{PP} (A)	V _C (V)	I _{PP} (A)	V _C (V)
SMF05	6.0	7.2	5.0	90	1.25	1.0	9.5	12	12.5

TYPICAL PERFORMANCE CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

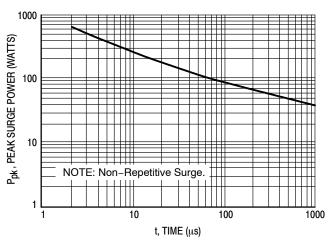


Figure 1. Peak Power Dissipation versus Pulse Width

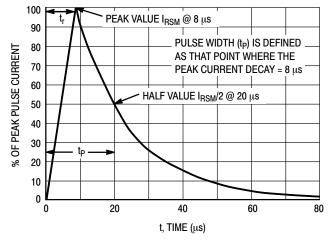


Figure 2. Pulse Waveform 8 x 20 μ s

^{1.} Non-repetitive current per Figure 2. Derate per Figure 3.

SMF05

TYPICAL PERFORMANCE CURVES

(T_A = 25°C unless otherwise noted)

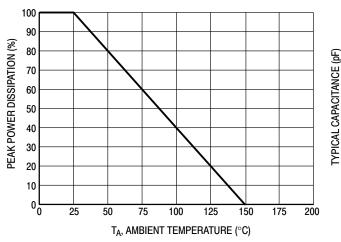


Figure 3. Power Derating Curve

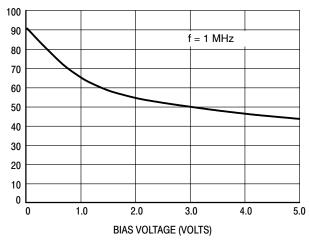


Figure 4. Junction Capacitance versus Reverse Voltage

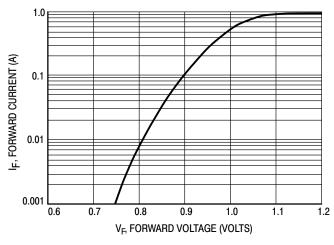


Figure 5. Forward Voltage Curve

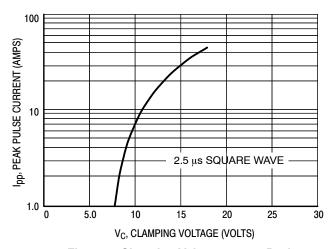


Figure 6. Clamping Voltage versus Peak Pulse Current (Reverse Direction)

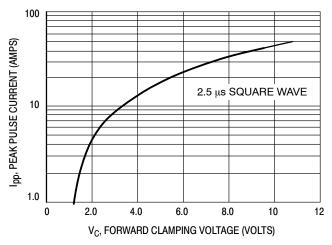


Figure 7. Clamping Voltage versus Peak Pulse Current (Forward Direction)



SC-88A (SC-70-5/SOT-353) CASE 419A-02 **ISSUE L**

DATE 17 JAN 2013



- TIES:
 DIMENSIONING AND TOLERANCING
 PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH.
 419A-01 OBSOLETE. NEW STANDARD 3.
- 419A-02.
 DIMENSIONS A AND B DO NOT INCLUDE
- MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.071	0.087	1.80	2.20
В	0.045	0.053	1.15	1.35
С	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026 BSC		0.65 BSC	
Н		0.004		0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008 REF		0.20	REF
S	0.079	0.087	2.00	2.20

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

= Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

-B-S D 5 PL 0.2 (0.008) M B M **SOLDER FOOTPRINT**

+++			
0.40			0.65 0.025
	<u>1.9</u> 0.0748	SCALE 20:1	$\left(\frac{\text{mm}}{\text{inches}}\right)$
OT # F 4	07450	07.45.0	

0.50 0.0197

STYLE 1:	STYLE 2:	STYLE 3:	STYLE 4:	STYLE 5:
PIN 1. BASE	PIN 1. ANODE	PIN 1. ANODE 1	PIN 1. SOURCE 1	PIN 1. CATHODE
2. EMITTER	2. EMITTER	2. N/C	2. DRAIN 1/2	2. COMMON ANODE
3. BASE	3. BASE	3. ANODE 2	3. SOURCE 1	3. CATHODE 2
4. COLLECTOR	4. COLLECTOR	4. CATHODE 2	4. GATE 1	4. CATHODE 3
4. COLLECTOR	4. COLLECTOR	4. CATHODE 2	4. GATE 1	4. CATHODE 3
5. COLLECTOR	5. CATHODE	5. CATHODE 1	5. GATE 2	5. CATHODE 4

J. GOLLLOTON	3. CATHODE	J. CATHODE I	J. GAIL 2	J. CATTODE 4
STYLE 6: PIN 1. EMITTER 2 2. BASE 2 3. EMITTER 1 4. COLLECTOR 5. COLLECTOR 2/BASE 1	STYLE 7: PIN 1. BASE 2. EMITTER 3. BASE 4. COLLECTOR 5. COLLECTOR	STYLE 8: PIN 1. CATHODE 2. COLLECTOR 3. N/C 4. BASE 5. EMITTER	STYLE 9: PIN 1. ANODE 2. CATHODE 3. ANODE 4. ANODE 5. ANODE	Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

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DESCRIPTION:	SC-88A (SC-70-5/SOT-353)		PAGE 1 OF 1	

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