

SRA2203E

PNP Silicon Transistor

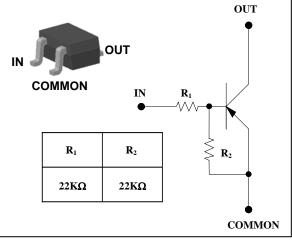
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

0			
Type NO.	Marking	Package Code	
SRA2203E	<u>3R</u> □ ① ②	SOT-523	
	Device Code (2) Year&Week Code		

(1) Device Code (2) Year&Week Code

Absolute Maximum Ratings

Absolute Maximum Ratings			(Ta=25°C	
Characteristic	Symbol	Rating	Unit	
Output voltage	Vo	-50	V	
Input voltage	VI	-40, 10	V	
Output current	Ι _ο	-100	mA	
Power dissipation	P _D	150	mW	
Junction temperature	TJ	150	°C	
Storage temperature range	T _{stg}	-55 ~ 150	°C	

Flectrical Characteristics

Electrical Characteristics (Ta=2						=25°C)
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = -50V, V_1 = 0$	-	-	-500	nA
DC current gain	Gı	$V_0 = -5V$, $I_0 = -10mA$	70	120	-	-
Output voltage	V _{O(ON)}	I ₀ =-10mA, I ₁ =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = -0.2V$, $I_0 = -5mA$	-	-2.1	-3.0	V
Input voltage (OFF)	V _{I (OFF)}	$V_0 = -5V$, $I_0 = -0.1mA$	-1.0	-1.2	-	V
Transition frequency	f _T *	V_0 =-10V, I_0 =-5mA, f=1MHz	-	200	-	MHz
Input current	I ₁	$V_1 = -5V, I_0 = 0$	-	-	-0.36	mA
Input resistor (Input to base)	R ₁	-	15.4	22	28.6	KΩ
Input resistor (Base to common)	R_2	-	15.4	22	28.6	KΩ

* : Characteristic of transistor only

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Electrical Characteristic Curves

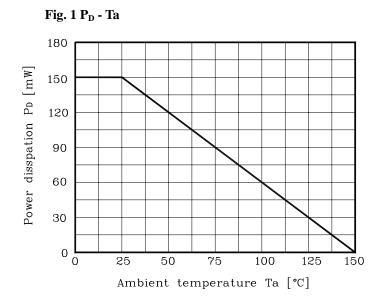


Fig. 3 I_{O} - $V_{I(OFF)}$

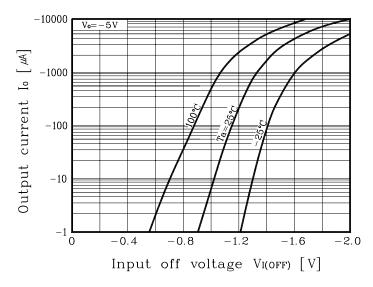


Fig. 2 I_O - $V_{I(ON)}$

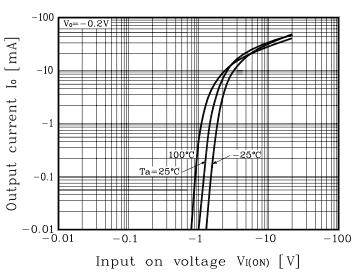
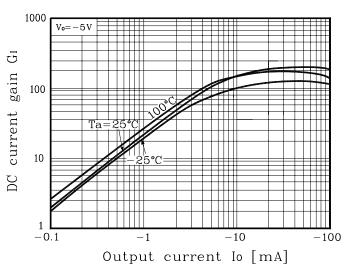
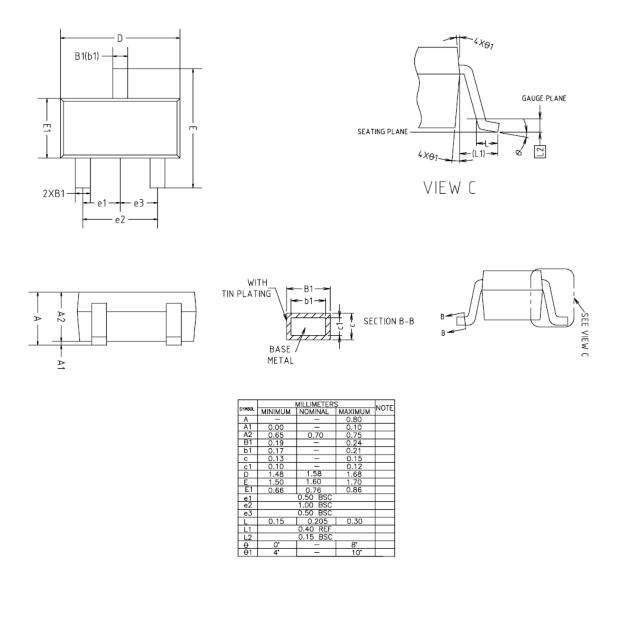


Fig. 4 G_I - I_O

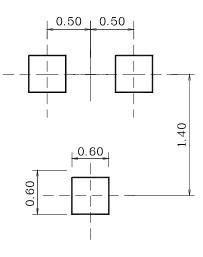


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Outline Dimension



*Recommend PCB solder land [Unit: mm]



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